

Features

- Meets the ISO 11898-2:2016 and SAE J2284-1 to SAE J2284-5 Physical Layer Standards
- · Supports CAN FD and Data Rating up to 5 Mbps
- Short Propagation Delay Times and Fast Loop Times
- 5-V Power Supply, I/O Voltage Range Supports 2.8-V to 5.5-V MCU Interface
- Ultra-Low Current Standby Mode with Bus Wake-up Capability
- Ideal Passive Behavior to CAN Bus when Unpowered
- Common-Mode Input Voltage: ±30 V
- Protection Feature:
 - IEC 61000-4-2 ESD Protection up to ±15 kV
 - Bus Fault Protection: ±70 V
 - VCC and VIO (V variants only) Under-Voltage Protection
 - TXD Dominant Time-out Function and Bus-Dominant Time-out Function
 - Thermal Shutdown Protection
- Available in SOP8 Package and Leadless DFN3X3 Package

Applications

- All Devices Supporting Highly Loaded CAN Networks
- Field Industrial Automation, Sensor and Driver Systems
- · Building, Security Control Systems
- · Energy Storage Systems
- · Telecom Base Station Status and Control

Description

The TPT1042 is a CAN transceiver that meets the ISO11898 high-speed CAN (Controller Area Network) physical layer standard. The device is designed to be used in CAN FD networks up to 5 Mbps, with enhanced timing margin and higher data rates in long and highly loaded networks. As designed, the device features crosswire, overvoltage, and loss of ground protection from -70 V to +70 V, over-temperature shutdown, with a -30 V to +30 V common-mode input voltage range. The TPT1042V has a secondary power supply input for I/O level shifting the input pin thresholds and RXD output level. This family has a low-current standby mode with CAN BUS waked-up capability. Additionally, all devices include many protection features to enhance the device and network robustness.

The TPT1042 and TPT1042V are available in SOP8 and DFN3X3-8L packages and are characterized from -40° C to $+125^{\circ}$ C.

Typical Application Circuit

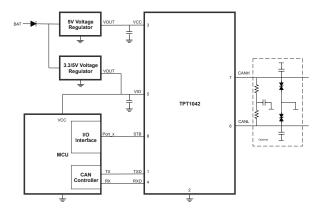




Table of Contents

Features	1
Applications	1
Description	1
Typical Application Circuit	1
Product Family Table	3
Revision History	3
Pin Configuration and Functions	4
Specifications	6
Absolute Maximum Ratings	6
ESD(Electrostatic Discharge Protection)	6
Recommended Operating Conditions	7
Thermal Information	7
Electrical Characteristics	8
AC Timing Requirements	11
Parameter Measurement Information	13
Test Circuit	13
Parameter Diagram	14
Detailed Description	15
Overview	15
Functional Block Diagram	15
Feature Description	16
Device Operating Modes	16
Device Local Faults	16
Application and Implementation	18
Application Information	18
Typical Application	18
Tape and Reel Information	19
Package Outline Dimensions	20
SOP-8	20
DFN3X3-8	21
Order Information	22
IMPORTANT NOTICE AND DISCLAIMER	23



Product Family Table

Order Number	VCC (V)	VIO (V)	BUS Protection (V)	Package
TPT1042V-SO1R-S	4.5 to 5.5	2.8 to 5.5	±70	SOP8
TPT1042V-DF6R-S	4.5 to 5.5	2.8 to 5.5	±70	DFN3X3-8L
TPT1042-SO1R-S	4.5 to 5.5	NC	±70	SOP8
TPT1042-DF6R-S	4.5 to 5.5	NC	±70	DFN3X3-8L

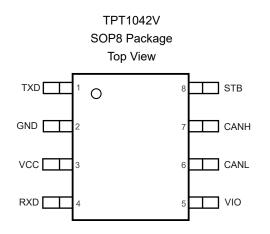
Revision History

Date	Revision	Notes
2020-12-05	Rev.Pre.0	Initial Version
2022-11-20	Rev.A.0	Released Version

www.3peak.com 3 / 24 CA20230202A0



Pin Configuration and Functions



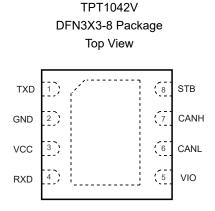
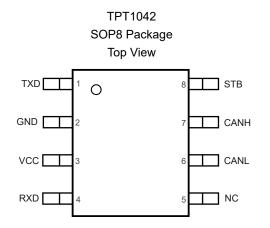


Table 1. Pin Functions: TPT1042VQ

Р	Pin		De a suitable su
No.	Name	I/O	Description
1	TXD	I	CAN transmit data input (LOW for dominant and HIGH for recessive bus states)
2	GND	GND	Ground
3	VCC	POWER	Transceiver 5 V supply voltage
4	RXD	0	CAN receive data output (LOW for dominant and HIGH for recessive bus states)
5	VIO	POWER	Transceiver I/O level shifting supply voltage
6	CANL	BUS I/O	Low level CAN bus input/output line
7	CANH	BUS I/O	High level CAN bus Input/output line
8	STB	I	Standby Mode control input (active high)

www.3peak.com 4 / 24 CA20230202A0





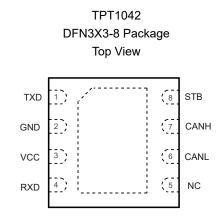


Table 2. Pin Functions: TPT1042Q

Р	in	1/0	D
No.	Name	I/O	Description
1	TXD	I	CAN transmit data input (LOW for dominant and HIGH for recessive bus states)
2	GND	GND	Ground
3	VCC	Power	Transceiver 5 V supply voltage
4	RXD	0	CAN receive data output (LOW for dominant and HIGH for recessive bus states)
5	NC	_	Not Connected
6	CANL	Bus I/O	Low-level CAN bus input/output line
7	CANH	Bus I/O	High-level CAN bus input/output line
8	STB	I	Standby Mode control input (active high)

www.3peak.com 5 / 24 CA20230202A0



Specifications

Absolute Maximum Ratings

	Parameter	Min	Max	Unit
Vcc	5-V Bus Supply Voltage Range	-0.3	7	V
V _{IO}	I/O Level-Shifting Voltage Range	-0.3	7	V
V _{BUS}	CAN Bus I/O Voltage Range (CANH, CANL)	-70	70	V
V _{BUS_DIFF}	Differential Voltage of CAN Bus, CANH - CANL	-70	70	V
V _{LOGIC}	Logic Input And Output Terminal Voltage Range (TXD, STB, RXD)	-0.3	7	V
I _{O_RXD}	RXD (Receiver) Output Current	-8	8	mA
TJ	Maximum Junction Temperature	-40	150	°C
T _{STG}	Storage Temperature Range	-65	150	°C
Тотр	Shutdown Junction Temperature		170	°C

⁽¹⁾ Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

ESD(Electrostatic Discharge Protection)

	Parameter	Condition	Minimum Level	Unit
IEC	IEC Contact Discharge	IEC-61000-4-2, Bus Pin	±15	kV
IEC	IEC Air-Gap Discharge	IEC-61000-4-2, Bus Pin	±15	kV
НВМ	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001, CAN Bus Pin	±8	kV
		ANSI/ESDA/JEDEC JS-001, All Pin	±7	kV
CDM	Charged Device Model ESD	ANSI/ESDA/JEDEC JS-002, All Pin	±1.5	kV
LU	Latch up	Latch up per JESD78, All Pin	±500	mA
		Pulse 1	-100	V
\	ISO7637-2 transients per IEC 62228-3, CANH, CANL	Pulse 2a	75	V
Vtran		Pulse 3a	-150	V
		Pulse 3b	100	V

⁽¹⁾ JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

www.3peak.com 6 / 24 CA20230202A0

⁽²⁾ This data was taken with the JEDEC low effective thermal conductivity test board.

⁽³⁾ This data was taken with the JEDEC standard multilayer test boards.

⁽²⁾ JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.



Recommended Operating Conditions

	Parameter	Min	Max	Unit
V _{IO}	Input/output voltage, TXD, RXD, STB	2.8	5.5	V
Vcc	Power Supply	4.5	5.5	V
I _{OH(RXD)}	RXD Terminal High-Level Output Current	-2		mA
I _{OL(RXD)}	RXD Terminal Low-Level Output Current		2	mA
T _A	Operating Ambient Temperature	-40	125	°C

Thermal Information

Package Type	θυΑ	Ө JС	Unit
SOP8	118	48	°C/W
DFN3x3-8	51	23	°C/W

www.3peak.com 7 / 24 CA20230202A0



Electrical Characteristics

All test conditions: V_{CC} = 4.5 V to 5.5 V, V_{IO} = 2.8 V to 5.5 V, R_L = 60 Ω , T = -40°C to 125°C, unless otherwise noted.

	Parameter	Conditions	Min	Тур	Max	Unit
Pin V _{CC} , (P	ower supply)					
Vcc	Supply Voltage		4.5		5.5	V
Vuv_vcc	Rising Undervoltage Detection on V_{cc} for Protected Mode		-	4.1	4.5	V
VUV_VCC	Falling Undervoltage Detection on V_{cc} for Protected Mode		3.6	3.9	-	V
V _{HYS_UVVCC}	Hysteresis Voltage on UV _{VCC} ⁽¹⁾		-	200	-	mV
Icc	Otan dha Mada Canada Canada	1042V, device with the "V" suffix, Standby Mode, V_{TXD} = V_{CC} , RL = 60Ω , C_L = open, V_{STB} = V_{CC}	-	3.5	5	μA
	Standby Mode Supply Current	1042, device without the "V" suffix, Standby Mode, $V_{TXD} = V_{IO}$, $RL = 60 \Omega$, $C_L = open$, $V_{STB} = V_{IO}$	-	10	20	μA
	Normal Mode Supply Current	Recessive, $V_{TXD} = V_{CC}$, R_L = 50 Ω , C_L = open, $V_{STB} = 0 V$	0.5	1.3	5	mA
		Dominant, $V_{TXD} = 0 \text{ V}$, $R_L = 60 \Omega$, $C_L = \text{open}$, $V_{STB} = 0 \text{ V}$	20	50	80	mA
		Dominant bus fault, short circuit on bus lines, V_{TXD} = V_{STB} = 0 V, -3 V < (V_{CANH} = V_{CANL}) < +18V, R_L = C_L = open	2	60	110	mA
PIN V _{IO} , (I/O	D Level Adapter Supply) ⁽²⁾					
V _{IO}	Supply Voltage on V _{io} Pin		2.8	-	5.5	V
V	Rising Undervoltage Detection on V _{io} For Protected Mode		-	2	2.7	V
Vuv_vio	Falling Undervoltage Detection on V _{io} For Protected Mode		1.3	1.9	-	V
V _{HYS_UVVIO}	Hysteresis Voltage on uv _{vio} for Protected Mode		-	100	-	mV
	Standby Mode Supply Current on Pin vio	Standby Mode, RXD Floating, V _{TXD} = V _{STB} = V _{IO}	5	10	14	μA
l _{IO}	Normal Mode Supply Current on Pin vio	Recessive, V _{STB} = 0 V, V _{TXD} = V _{IO}	-	15	30	μA
		Dominant, V _{STB} = 0 V,	-	200	5 80 110 5.5 2.7 - 14	μA

www.3peak.com 8 / 24 CA20230202A0



	Parameter		Conditions	Min	Тур	Max	Unit
			$V_{TXD} = 0 V$				
Pin STB,	(Standby Mode Control Input)						
V _{IH}	High-Level Input Voltage			0.7 x V _{IO}	-	V _{IO} + 0.3	V
VIL	Low-Level Input Voltage			-0.3	-	0.3 x V _{IO}	V
I _{IH}	High-Level Input Current		$V_{STB} = V_{CC} = V_{IO} = 5.5 \text{ V}$	-2	0	2	μΑ
I _{IL}	Low-Level Input Current		$V_{STB} = 0 \text{ V},$ $V_{CC} = V_{IO} = 5.5 \text{ V}$	-20	-11	-2	μA
I _{lkg(OFF)}	Unpowered Leakage Current		$V_{STB} = 5.5 \text{ V},$ $V_{CC} = V_{IO} = 0 \text{ V}$	-1	0	1	uA
Pin TXD,	(CAN Transmit Data Input)						
V _{IH}	High-Level Input Voltage			0.7 x V _{IO}	-	V _{IO} + 0.3	V
V _{IL}	Low-Level Input Voltage			-0.3	-	0.3 x V _{IO}	V
I _{IH}	High-Level Input Current		$V_{TXD} = V_{CC} = V_{IO} = 5.5 \text{ V}$	-2	0	2	μA
I _{IL}	Low-Level Input Current		$V_{TXD} = 0 \text{ V}, V_{CC} = V_{IO} = 5.5 \text{ V}$	-250	-160	-30	μA
I _{LKG_OFF}	Unpowered Leakage Current		$V_{TXD} = 5.5 \text{ V},$ $V_{CC} = V_{IO} = 5.5 \text{ V}$	-1	0	1	μA
Cı	Input Capacitance (1)			-	5	10	pF
Pin RXD,	(CAN Receive Data Output)						
I _{OH}	High-Level Output Current		VRXD = VIO - 0.4 V	-8	-6	-1	mA
loL	Low-Level Output Current		VRXD = 0.4 V, bus dominant	2	5	12	mA
I _{LKG_OFF}	Unpowered Leakage Current		$V_{RXD} = 5.5 \text{ V},$ $V_{CC} = V_{IO} = 0 \text{ V}$	-1	0	1	μA
Pins CAN	IH and CANL, (CAN Bus Lines)					
	Dominant Bus Output	CANH	$V_{TXD} = 0 \text{ V}, V_{STB} = 0 \text{ V}, 50 \Omega$	2.75	3.5	4.5	V
V _{O_DOM}	Voltage	CANL	\leq R _L \leq 65 Ω , C _L = open, t $<$ $t_{to(dom)TXD}$	0.5	1.5	2.25	V
V _{SYM_DC}	DC Output Symmetry (domina recessive) (V _{CC} – V _{O(CANH)} – V		V_{STB} = 0 V, R_L = 60 Ω , C_L = open,	-0.4	-	0.84	V
Vsym	Transient Symmetry (dominant or recessive) (Vo(CANH) + Vo(CANL)) / Vcc (1)		$4.75V \le V_{CC} \le 5.25 \text{ V, V}_{STB}$ = 0 V, R _L = 60 Ω, C _{SPLIT} = 4.7 nF, C_{L} = open, T _{XD} = 250 kHz, 1 MHz	0.9	1	1.1	V/V
V _{OD_DOM}	Differential Output Voltage (do	ominant)	Normal mode, $t < t_{to(dom)TXD}$, $V_{TXD} = 0$ V, $V_{STB} = 0$ V, 4.75 V $\leq V_{CC} \leq 5.25$ V, 45 $\Omega \leq R_L < 50$ Ω , $C_L = open$	1.5	-	3	V

www.3peak.com 9 / 24 CA20230202A0



	Parameter		Conditions	Min	Тур	Max	Unit
			Normal mode, t < $t_{to(dom)TXD,}V_{TXD} = 0 \text{ V, } V_{STB} \\ = 0 \text{ V, } 4.75\text{V} \leq V_{CC} \leq 5.25 \\ \text{V, } 50 \Omega \leq R_L < 65 \Omega, C_L = \\ \text{open}$	1.5	-	3	V
			Normal mode, $t < t_{to(dom)TXD}$, $V_{TXD} = 0$ V, $V_{STB} = 0$ V, 4.75 V $\leq V_{CC} \leq 5.25$ V, 65 $\Omega \leq R_L \leq 70$ Ω , $C_L = open$	1.5	-	3.3	V
			Normal mode, $t < t_{to(dom)TXD}$, $V_{TXD} = 0$ V, $V_{STB} = 0$ V, 4.75 V $\leq V_{CC} \leq 5.25$ V, $R_L = 2240$ Ω , $C_L = open$	1.5	-	5	V
V _{OD_REC}	Differential Output Voltage (re	(evisses	Normal mode, $V_{TXD} = V_{CC}$, $V_{STB} = 0$ V, no load	-50	-	50	mV
VOD_REC	Differential Output Voltage (recessive)		Standby mode, V _{TXD} = V _{STB} = V _{CC} , no load	-150	-	150	mV
V	Recessive Bus Output Voltage		Normal mode, $V_{TXD} = V_{IO} = V_{CC}$, $V_{STB} = 0$ V, no load	2	0.5 x VCC	3	V
V _{O_REC}			Standby mode, $V_{TXD} = V_{STB}$ = $V_{IO} = V_{CC}$, no load	-0.1	-	0.1	V
V _{CM}	Common Mode Range		V _{STB} = 0 or V _{CC} or V _{IO}	-30	_	30	V
			Normal mode, $V_{STB} = 0 \text{ V}$, $-30 \text{ V} \le V_{CANH} / V_{CANL} \le 30 \text{ V}$	0.5	0.7	0.9	V
V _{TH_RX_DIF}	Differential Receiver Thresho	ld Voltage	Standby mode, V _{STB} = V _{IO} , -30 V ≤ V _{CANH} / V _{CANL} ≤ 30 V	0.4	0.7	1.15	V
	Receiver Recessive Voltage,	Normal Mode		-4	-	0.5	V
V _{REC_RX}	Receiver Recessive Voltage,	Standby Mode	-30 V ≤ V _{CANH} /V _{CANL} ≤ 30 V	-4	-	0.4	V
V	Receiver Dominant Voltage, N	Normal Mode	_20 \/ <\/ < 20 \/	0.9	-	9	V
V _{DOM_RX}	Receiver Dominant Voltage, S	Standby Mode	-30 V ≤V _{CANH} /V _{CANL} ≤ 30 V	1.15	-	9	V
V _{HYS_RX_DI}	Differential Receiver Hyetoros	sis Voltage	Normal mode, −30 V ≤ V _{CANH} / V _{CANL} ≤ 30 V	50	120	200	mV
F	Differential Receiver Hysteresis Voltage		Standby mode, −30 V ≤ V _{CANH} / V _{CANL} ≤ 30 V	20	70	120	mV
lo so serv	Dominant Short-Circuit	CANH	V _{STB} = 0 V, V _{CANH} = -15 V to 18 V, CANL = open, V _{TXD} = 0 V	-115	-	-	mA
Io_sc_dom	Output Current	CANL	V _{STB} = 0 V, V _{CANL} = -15 V to 18 V, CANH = open, V _{TXD} = 0 V	-	-	115	mA

www.3peak.com 10 / 24 CA20230202A0



	Parameter	Conditions	Min	Тур	Max	Unit
Io_SC_REC	Recessive Short-Circuit Output Current	$-27 \text{ V} \le \text{V}_{\text{CANH}}/\text{V}_{\text{CANL}} \le 32$ V, $\text{V}_{\text{TXD}} = \text{V}_{\text{CC}},$ normal modes	-5	-	5	mA
I _{LKG_IOFF}	Power-off (unpowered) Bus Input Leakage Current	$V_{CC} = V_{IO} = 0 \text{ V or}$ $V_{CC} = V_{IO}$ shorted to ground via $47k\Omega$, $V_{CANH} = 5 \text{ V}$, $V_{CANL} = 5 \text{ V}$	-5	-	5	μА
R _{IN}	Input Resistance (CANH or CANL)	$V_{TXD} = V_{CC} = V_{IO} = 5 \text{ V, V}_{STB}$ = 0 V, -30 V \le V_{CM} \le +30 V	10	23	35	kΩ
R _{IN_M}	Input Resistance Matching: [1 - R _{IN(CANH)} / R _{IN(CANL)}] × 100%	$V_{TXD} = V_{CC} = V_{IO} = 5 \text{ V}, V_{STB}$ =0 V, $V_{CANH} = V_{CANL} = 5 \text{ V},$	-2	-	2	%
R _{ID}	Differential Input Resistance	$V_{TXD} = V_{CC} = V_{IO} = 5 \text{ V}, V_{STB}$ = 0 V, -30 V \le V_{CM} \le +30 V	30	47	60	kΩ
Cı	Input Capacitance to Ground (CANH or CANL) (1)		-	-	20	pF
C _{ID}	Differential Input Capacitance (1)		-	-	10	pF

⁽¹⁾ The Typ data is based on bench test and design simulation. The typical data is based on bench test by LRC meter E4980AL.

AC Timing Requirements

All test conditions: V_{CC} = 4.5 V to 5.5 V, V_{IO} = 2.8 V to 5.5 V, R_L = 60 Ω , T_A = -40°C to 125°C, unless otherwise noted.

	Parameter	Conditions	Min	Тур	Max	Unit
Transceiver	Switching Characteristics					
t_{pLD}	Propagation delay time, low TXD to driver dominant (recessive to dominant)		-	60	100	ns
t _{pHR}	Propagation delay time, high TXD to driver recessive (dominant to recessive)	Normal mode, V _{STB} = 0 V,	-	60	100	ns
t _{SK_P}	Pulse Skew (t _{pHR} - t _{pLD})	$R_L = 60 \Omega, C_L = 100 pF$	_	10	35	ns
t _R	Differential Output Signal Rise Time ⁽¹⁾		-	45	-	ns
t _F	Differential Output Signal Fall Time (1)		-	45	-	ns
tPROP_TXDL-RXDL	Total loop delay, driver input (TXD) low to receiver output (RXD) low, recessive to dominant	Normal mode, V_{STB} = 0 V, R_L = 60 Ω , C_L = 100 pF, $C_{L(RXD)}$ = 15 pF,	-	110	220	ns

⁽²⁾ Only device with V suffix (TPT1042V) have a V_{IO} Pin, device without V suffix (TPT1042) V_{IO} connoted to V_{CC} internally.



	Parameter	Conditions	Min	Тур	Max	Unit
tPROP_TXDH-RXDH	Total loop delay, driver input (TXD) high to receiver output (RXD) high, dominant to recessive		-	140	220	ns
t _{pRH}	Propagation Delay Time, Bus Recessive Input to RXD High Output (Dominant to Recessive) ⁽¹⁾		-	90	120	ns
t _{pDL}	Propagation Delay Time, Bus Dominant Input to RXD Low Output (Recessive to Dominant) ⁽¹⁾	V _{STB} = 0 V, C _{L(RXD)} = 15 pF	-	90	120	ns
t _{R_R}	RXD Output Signal Rise Time		-	20	-	ns
t _{R_F}	RXD Output Signal Fall Time (1)		-	20	-	ns
FD Timing P	arameters					
	Bit time on CAN bus output pins with $t_{BIT(TXD)} = 500 \text{ ns}^{(1)}$		435	-	530	ns
t _{BIT_BUS}	Bit time on CAN bus output pins with $t_{BIT(TXD)} = 200 \text{ ns}^{(1)}$		155	-	210	ns
	Bit time on RXD output pins with t _{BIT(TXD)} = 500 ns	$V_{STB} = 0 \text{ V}, R_L = 60 \Omega, C_L =$	400	-	550	ns
t _{BIT_RXD}	Bit time on RXD output pins with t _{BIT(TXD)} = 200 ns	100 pF, $C_{L(RXD)}$ = 15 pF, Δt_{REC} = $t_{BIT(RXD)}$ - $t_{BIT(BUS)}$	120	-	220	ns
	Receiver timing symmetry with $t_{BIT(TXD)} = 500 \text{ ns}^{(1)}$		-65	-	40	ns
Δt _{REC}	Receiver timing symmetry with $t_{BIT(TXD)} = 200 \text{ ns}^{(1)}$		-45	-	15	ns
Device Timir	ng Parameters					
t _{TXD_DTO}	TXD dominant time-out time	normal mode, $V_{STB} = 0 \text{ V}$, $R_L = 60$ Ω , $C_L = \text{open}$, $V_{TXD} = 0 \text{ V}$	0.3	2	5	ms
t _{BUS_DTO}	Bus dominant time-out time	Standby mode	0.3	2	5	ms
t _{BUS_WAKE_FIL}	Bus wake-up filter time	Standby mode	0.5	1.5	5	μs
tdMODE	Standby to normal mode delay time		3	8	50	μs

⁽¹⁾ The test data is based on bench test and design simulation.

www.3peak.com 12 / 24 CA20230202A0



Parameter Measurement Information

Test Circuit

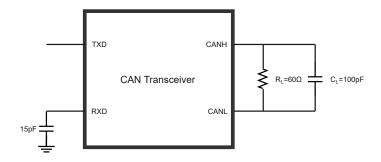


Figure 1. CAN transceiver timing parameter test circuit

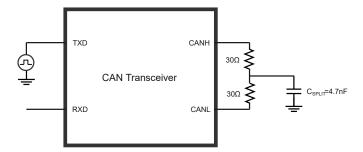


Figure 2. CAN transceiver driver symmetry test circuit

www.3peak.com 13 / 24 CA20230202A0



Parameter Diagram

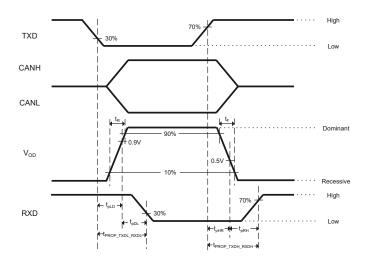


Figure 3. CAN transceiver timing diagram

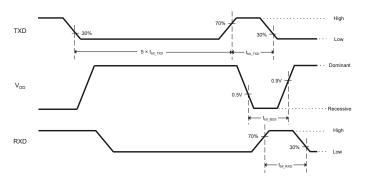


Figure 4. CAN FD timing parameter diagram

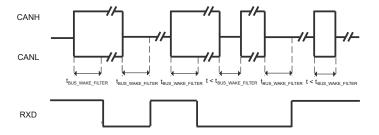


Figure 5. Wake-up timing diagram

www.3peak.com 14 / 24 CA20230202A0



Detailed Description

Overview

The TPT1042 is a CAN transceiver that meets the ISO11898 high-speed CAN (Controller Area Network) physical layer standard. The device is designed to be used in CAN FD networks up to 5 Mbps, with enhanced timing margin and higher data rates in long and highly loaded networks. As designed, the device features crosswire, overvoltage, and loss of ground protection from -70 V to +70 V, over-temperature shutdown, with a -30 V to +30 V common-mode input voltage range. The TPT1042V has a secondary power supply input for I/O level shifting the input pin thresholds and RXD output level. This family has a low-current standby mode with CAN BUS waked-up capability. Additionally, all devices include many protection features to enhance the device and network robustness.

Functional Block Diagram

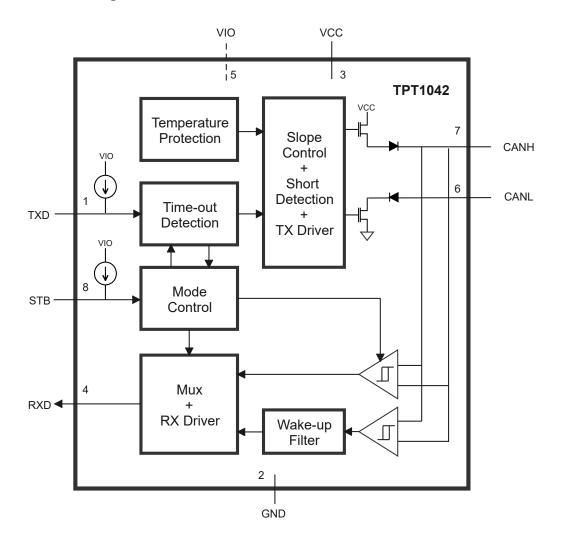


Figure 6. Functional Block Diagram

www.3peak.com 15 / 24 CA20230202A0



Feature Description

Table 3. Driver Function Table

Davies	Inp	uts	Out	Driven BUS State	
Device	STB	TXD	CANH	CANL	Driven BUS State
		L	Н	L	Dominant
All Devices	L	H or Open	Z	Z	Recessive
	H or Open	Х	Z	Z	Recessive

Table 4. Receiver Function Table

Device Mode	CAN Differential Inputs V _{ID} = V _{CANH} - V _{CANL}	Bus State	RXD Terminal	
	$V_{ID} \ge V_{IT+(MAX)}$	Dominant	L	
Normal or Standby	$V_{\text{IT-(MIN)}} < V_{\text{ID}} < V_{\text{IT+(MAX)}}$	Indeterminate	Indeterminate	
Normal or Standby	$V_{\text{ID}} \leq V_{\text{IT-(MIN)}}$	Recessive	Н	
	Open (V _{ID} ≈ 0 V)	Open	Н	

Device Operating Modes

Normal Mode

A low level on the STB pin selects the normal mode. In this mode, the transceiver will transmit and receive data via the bus lines CANH and CANL. The differential receiver converts the analog data on the bus lines into digital data, which is output to the RXD pin. The slopes of the output signals on the bus lines are controlled internally and optimized to guarantee the lowest possibility for Electro Magnetic Emission (EME).

Standby Mode

Activate the low-power standby mode by setting the STB terminal high. In this mode, the bus transmitter will not send data, nor will the normal mode receiver accept data as the bus lines are biased to ground minimizing the system supply current. Only the low-power receiver will be actively monitoring the bus for activity. RXD indicates a valid wake-up event after a valid wake-up signal has been detected on the Bus. The low-power receiver is powered using the VIO pin only. This allows V_{CC} to be removed reducing power consumption further.

Device Local Faults

TXD Dominant Time-out

The device will detect TXD dominant time-out and prevents a permanent low on the TXD pin drive CAN bus into permanent dominant blocking the CAN bus network. If the TXD remains low for longer than t_{TXD_DTO} , the transmitter will be disabled until the fault flag has been cleared. The TXD dominant time-out time also defines that the data rate should be faster than 40 kbit/s.

Bus Dominant Time-out

The device will detect Bus dominant time-out and prevent a permanent dominant on the CAN bus blocking the CAN bus network. The fault flag is set if the CAN bus remains dominant for longer than t_{BUS_DTO}, and is released as soon as the bus returns to recessive.

www.3peak.com 16 / 24 CA20230202A0



Under-Voltage Lockout (UVLO)

The device integrated under-voltage detect and lockout circuit of the supply terminal to keep the device in the protected mode if the supply voltage drops below the threshold until the supply voltage is higher than the UVLO threshold. This protects the device and system during the under-voltage event on supply terminals.

Over-Temperature Protection (OTP)

The output drivers are protected against over-temperature conditions. If the virtual junction temperature exceeds the shutdown junction temperature T_{OTP} , the output drivers will be disabled until the virtual junction temperature falls below T_{OTP} and TXD becomes recessive again. Including the TXD condition minimizes output driver oscillation due to temperature drift.

www.3peak.com 17 / 24 CA20230202A0



Application and Implementation

Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Application Information

The TPT1042 is a CAN transceiver to support CAN FD function up to 5 Mbps, with BUS protection voltage from –70 V to +70 V, overtemperature shutdown, a –30 V to +30 V common-mode range. The VIO of TPT1042 can support the voltage level of TXD and RXD from 2.8 V to 5.5 V. The following sections show a typical application of the TPT1042.

Typical Application

Figure 7 shows the typical application schematic of the TPT1042.

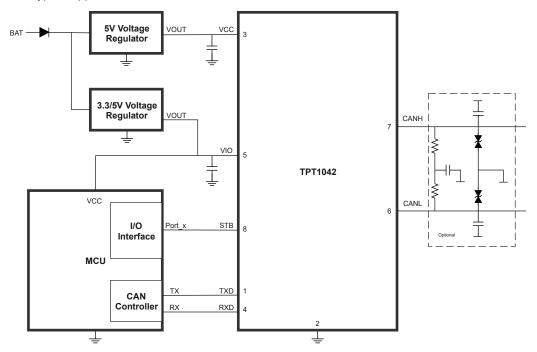
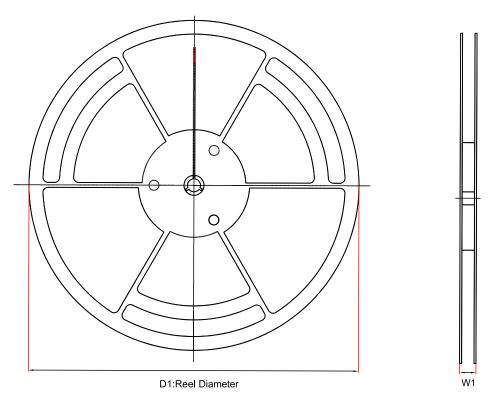


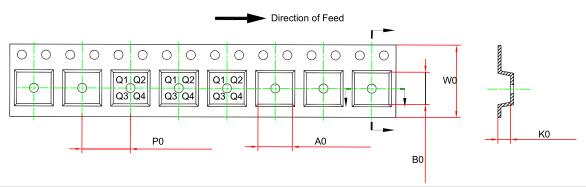
Figure 7. Typical Application Circuit

www.3peak.com 18 / 24 CA20230202A0



Tape and Reel Information



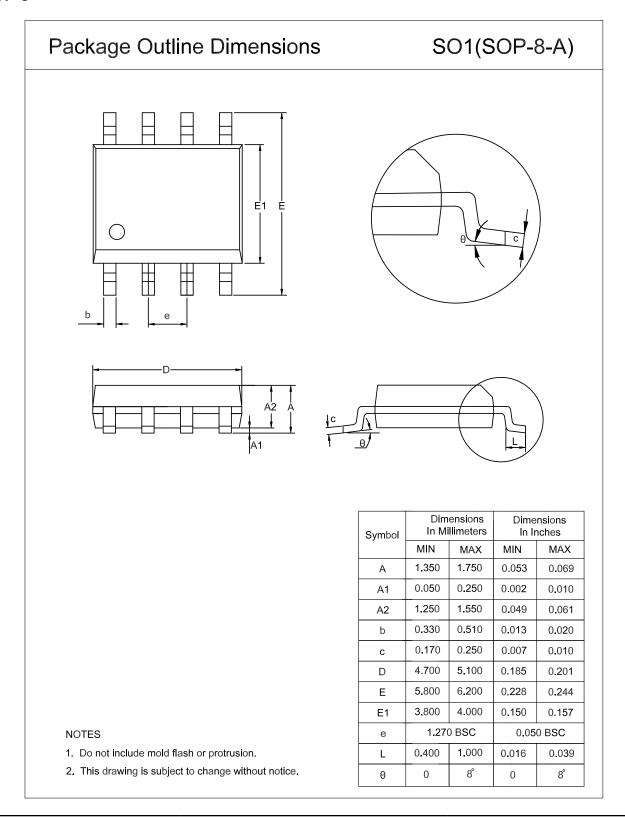


Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPT1042V- SO1R-S	SOP8	330	17.6	6.5	5.4	2.0	8.0	12.0	Q1
TPT1042V- DF6R-S	DFN3x3-8	330	17.6	3.3	3.3	1.1	8.0	12.0	Q1
TPT1042- SO1R-S	SOP8	330	17.6	6.5	5.4	2.0	8.0	12.0	Q1
TPT1042- DF6R-S	DFN3x3-8	330	17.6	3.3	3.3	1.1	8.0	12.0	Q1



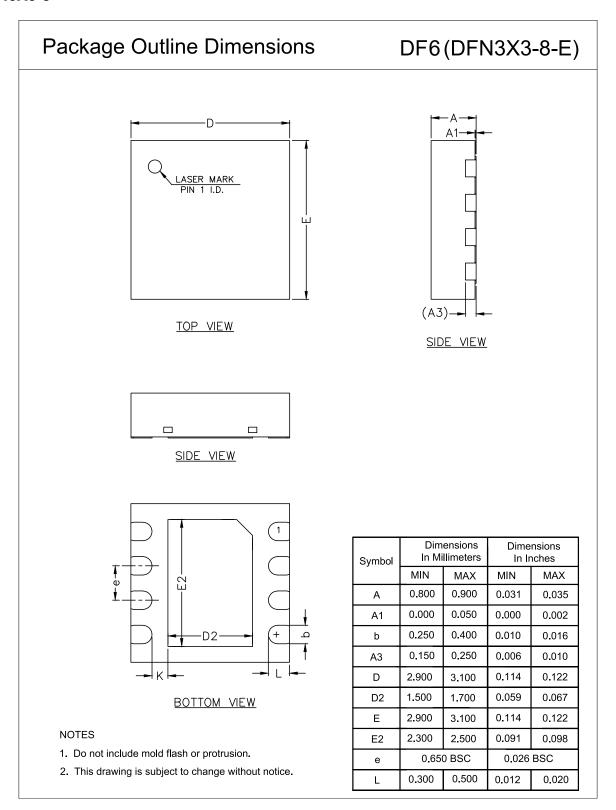
Package Outline Dimensions

SOP-8





DFN3X3-8





Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPT1042V-SO1R-S	−40 to 125°C	SOP8	1042V	MSL3	Tape and Reel, 4000	Green
TPT1042V-DF6R-S	−40 to 125°C	DFN3x3-8	1042V	MSL3	Tape and Reel, 4000	Green
TPT1042-SO1R-S	−40 to 125°C	SOP8	T1042	MSL3	Tape and Reel, 4000	Green
TPT1042-DF6R-S	-40 to 125°C	DFN3x3-8	T1042	MSL3	Tape and Reel, 4000	Green

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.



IMPORTANT NOTICE AND DISCLAIMER

Copyright[©] 3PEAK 2012-2023. All rights reserved.

Trademarks. Any of the 思瑞浦 or 3PEAK trade names, trademarks, graphic marks, and domain names contained in this document /material are the property of 3PEAK. You may NOT reproduce, modify, publish, transmit or distribute any Trademark without the prior written consent of 3PEAK.

Performance Information. Performance tests or performance range contained in this document/material are either results of design simulation or actual tests conducted under designated testing environment. Any variation in testing environment or simulation environment, including but not limited to testing method, testing process or testing temperature, may affect actual performance of the product.

Disclaimer. 3PEAK provides technical and reliability data (including data sheets), design resources (including reference designs), application or other design recommendations, networking tools, security information and other resources "As Is". 3PEAK makes no warranty as to the absence of defects, and makes no warranties of any kind, express or implied, including without limitation, implied warranties as to merchantability, fitness for a particular purpose or non-infringement of any third-party's intellectual property rights. Unless otherwise specified in writing, products supplied by 3PEAK are not designed to be used in any life-threatening scenarios, including critical medical applications, automotive safety-critical systems, aviation, aerospace, or any situations where failure could result in bodily harm, loss of life, or significant property damage. 3PEAK disclaims all liability for any such unauthorized use.

www.3peak.com 23 / 24 CA20230202A0



This page intentionally left blank