HEF40106B-Q100

Hex inverting Schmitt trigger

Rev. 4 — 8 August 2024

Product data sheet

1. General description

The HEF40106B is a hex inverter with Schmitt-trigger inputs. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{DD} .

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Wide supply voltage range from 3.0 V to 15.0 V
- · CMOS low power dissipation
- High noise immunity
- Schmitt trigger input discrimination
- Fully static operation
- 5 V, 10 V, and 15 V parametric ratings
- Standardized symmetrical output characteristics
- Complies with JEDEC standard JESD 13-B
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V

3. Applications

- Wave and pulse shapers
- Astable multivibrators
- Monostable multivibrators

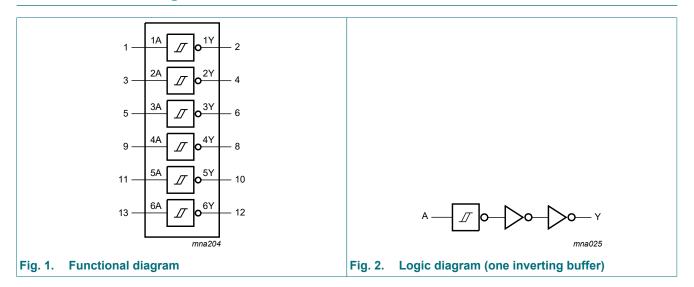
4. Ordering information

Table 1. Ordering information

Type number	Package	ackage						
	Temperature range	Name	Description	Version				
HEF40106BT-Q100	-40 °C to +125 °C	SO14	plastic small outline package; 14 leads; body width 3.9 mm	SOT108-1				
HEF40106BTT-Q100	-40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	SOT402-1				

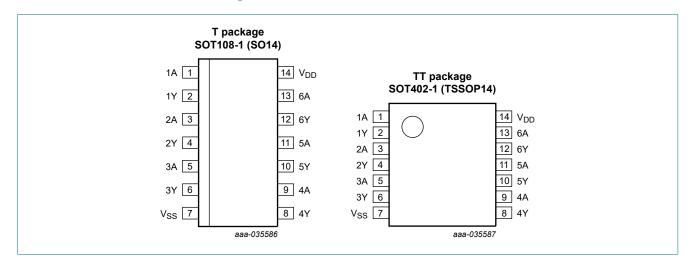


5. Functional diagram



6. Pinning information

6.1. Pinning



6.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
1A, 2A, 3A, 4A, 5A, 6A	1, 3, 5, 9, 11, 13	input
1Y, 2Y, 3Y, 4Y, 5Y, 6Y	2, 4, 6, 8, 10, 12	output
V_{DD}	14	supply voltage
V _{SS}	7	ground (0 V)

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7. Functional description

Table 3. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$

Input	Output
nA	nY
L	Н
Н	L

8. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to $V_{SS} = 0 \text{ V}$ (ground).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DD}	supply voltage		-0.5	+18	V
I _{IK}	input clamping current	$V_{I} < -0.5 \text{ V or } V_{I} > V_{DD} + 0.5 \text{ V}$	-	±10	mA
VI	input voltage		-0.5	V _{DD} + 0.5	V
I _{OK}	output clamping current	$V_{O} < -0.5 \text{ V or } V_{O} > V_{DD} + 0.5 \text{ V}$	-	±10	mA
I _{I/O}	input/output current		-	±10	mA
I _{DD}	supply current		-	50	mA
T _{stg}	storage temperature		-65	+150	°C
T _{amb}	ambient temperature		-40	+125	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$ [1]	-	500	mW
Р	power dissipation	per output	-	100	mW

^[1] For SOT108-1 (SO14) package: P_{tot} derates linearly with 10.1 mW/K above 100 °C. For SOT402-1 (TSSOP14) package: P_{tot} derates linearly with 7.3 mW/K above 81 °C.

9. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DD}	supply voltage		3	15	V
VI	input voltage		0	V_{DD}	V
T _{amb}	ambient temperature	in free air	-40	+125	°C

10. Static characteristics

Table 6. Static characteristics

 $V_{SS} = 0 \ V$; $V_{I} = V_{SS} \ or \ V_{DD}$; unless otherwise specified.

Symbol	Parameter	Conditions	V_{DD}	T _{amb} =	-40 °C	T _{amb} =	+25 °C	T _{amb} =	+85 °C	T _{amb} = +125 °C		Unit
				Min	Max	Min	Max	Min	Max	Min	Max	
V _{OH}	HIGH-level	I _O < 1 μA	5 V	4.95	-	4.95	-	4.95	-	4.95	-	V
	output voltage		10 V	9.95	-	9.95	-	9.95	-	9.95	-	V
			15 V	14.95	-	14.95	-	14.95	-	14.95	-	V
V _{OL}	LOW-level	I _O < 1 μA	5 V	-	0.05	-	0.05	-	0.05	-	0.05	V
	output voltage		10 V	-	0.05	-	0.05	-	0.05	-	0.05	V
			15 V	-	0.05	-	0.05	-	0.05	-	0.05	V
I _{OH}	HIGH-level	V _O = 2.5 V	5 V	-	-1.7	-	-1.4	-	-1.1	-	-1.1	mA
	output current	V _O = 4.6 V	5 V	-	-0.64	-	-0.5	-	-0.36	-	-0.36	mA
		V _O = 9.5 V	10 V	-	-1.6	-	-1.3	-	-0.9	-	-0.9	mA
		V _O = 13.5 V	15 V	-	-4.2	-	-3.4	-	-2.4	-	-2.4	mA
I _{OL}	LOW-level	V _O = 0.4 V	5 V	0.64	-	0.5	-	0.36	-	0.36	-	mA
	output current	V _O = 0.5 V	10 V	1.6	-	1.3	-	0.9	-	0.9	-	mA
		V _O = 1.5 V	15 V	4.2	-	3.4	-	2.4	-	2.4	-	mA
I _I	input leakage current		15 V	-	±0.1	-	±0.1	-	±1.0	-	±1.0	μΑ
I _{DD}	supply current	all valid input	5 V	-	0.25	-	0.25	-	7.5	-	7.5	μΑ
		combinations;	10 V	-	0.5	-	0.5	-	15.0	-	15.0	μΑ
		I _O = 0 A	15 V	-	1.0	-	1.0	-	30.0	-	30.0	μΑ
C _I	input capacitance			-	-	-	7.5	-	-	-	-	pF

11. Dynamic characteristics

Table 7. Dynamic characteristics

 T_{amb} = 25 °C; C_L = 50 pF; t_r = $t_f \le$ 20 ns unless otherwise specified.

For waveforms see Fig. 3; for test circuit see Fig. 4;

Symbol	Parameter	Conditions	V_{DD}	Extrapolation formula [1]	Min	Тур	Max	Unit
t _{PHL}	HIGH to LOW	nA to nY	5 V	63 ns + (0.55 ns/pF)C _L	-	90	180	ns
	propagation delay		10 V	29 ns + (0.23 ns/pF)C _L	-	35	70	ns
			15 V	22 ns + (0.16 ns/pF)C _L	-	30	60	ns
t _{PLH}	LOW to HIGH	nA to nY	5 V	58 ns + (0.55 ns/pF)C _L	-	75	150	ns
	propagation delay		10 V	29 ns + (0.23 ns/pF)C _L	-	35	70	ns
			15 V	22 ns + (0.16 ns/pF)C _L	-	30	60	ns
t _{THL}	HIGH to LOW output	nY to LOW	5 V	10 ns + (1.00 ns/pF)C _L	-	60	120	ns
	transition time		10 V	9 ns + (0.42 ns/pF)C _L	-	30	60	ns
			15 V	6 ns + (0.28 ns/pF)C _L	-	20	40	ns
t _{TLH}	LOW to HIGH output	nY to HIGH	5 V	10 ns + (1.00 ns/pF)C _L	-	60	120	ns
	transition time		10 V	9 ns + (0.42 ns/pF)C _L	-	30	60	ns
			15 V	6 ns + (0.28 ns/pF)C _L	-	20	40	ns

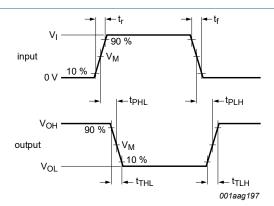
^[1] Typical value of the propagation delay and output transition time can be calculated with the extrapolation formula (C_L in pF).

Table 8. Dynamic power dissipation

 $V_{SS} = 0 \ V; \ t_r = t_f \le 20 \ ns; \ T_{amb} = 25 \ ^{\circ}C.$

Symbol	Parameter	V_{DD}	Typical formula	where:
P_D	dynamic power	5 V	. (0 2) 22 (.)	f _i = input frequency in MHz;
	dissipation	10 V		f _o = output frequency in MHz; C _L = output load capacitance in pF;
		15 V		$\Sigma(f_o \times C_L)$ = sum of the outputs; V_{DD} = supply voltage in V.

11.1. Waveforms and test circuit



Measurement points are given in Table 9.

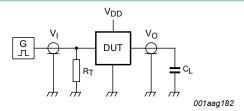
Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

 t_r , t_f = input rise and fall times.

Fig. 3. Propagation delay and output transition time

Table 9. Measurement points

Supply voltage	Input	Output
V_{DD}	V _M	V _M
5 V to 15 V	$0.5 \times V_{DD}$	0.5 × V _{DD}



Test data given in Table 10.

Definitions test circuit:

C_L = load capacitance including jig and probe capacitance;

 R_T = termination resistance should be equal to the output impedance Z_0 of the pulse generator.

Fig. 4. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Input	Load	
V_{DD}	VI	t _r , t _f	CL
5 V to 15 V	V _{SS} or V _{DD}	≤ 20 ns	50 pF

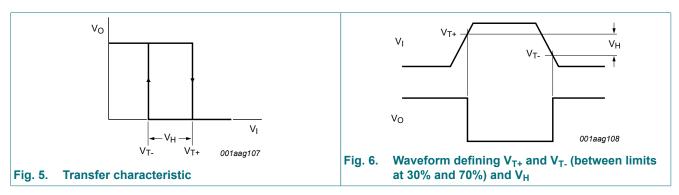
12. Transfer characteristics

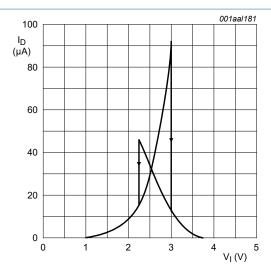
Table 11. Transfer characteristics

 $V_{SS} = 0 V$; see Fig. 5 and Fig. 6.

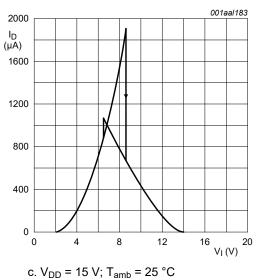
Symbol	Parameter	Conditions	V_{DD}	T _{amb} =	-40 °C to	+85 °C	T _{amb} = -40 °	Unit	
				Min	Typ [1]	Max	Min	Max	
V _{T+}	positive-going		5 V	2.0	3.0	3.5	2.0	3.5	V
	threshold voltage		10 V	3.7	5.8	7.0	3.7	7.0	V
			15 V	4.9	8.3	11.0	4.9	11.0	V
V _{T-}	negative-going threshold voltage		5 V	1.5	2.2	3.0	1.5	3.0	V
			10 V	3.0	4.5	6.3	3.0	6.3	V
			15 V	4.0	6.5	10.1	4.0	10.1	V
V_{H}	hysteresis voltage		5 V	0.5	0.8	-	0.5	-	V
			10 V	0.7	1.3	-	0.7	-	V
			15 V	0.9	1.8	-	0.9	-	V

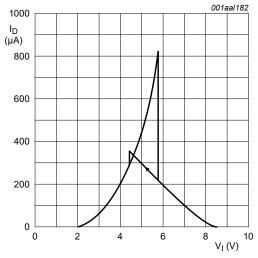
[1] All typical values are measured at T_{amb} = 25 °C.





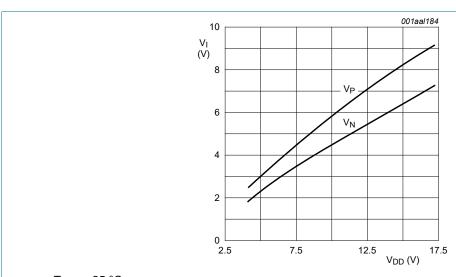
a. V_{DD} = 5 V; T_{amb} = 25 °C





b. V_{DD} = 10 V; T_{amb} = 25 °C

Fig. 7. Typical drain current as a function of input



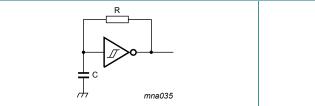
 T_{amb} = 25 °C.

Fig. 8. Typical switching levels as a function of supply voltage

13. Application information

Some examples of applications for the HEF40106B-Q100 are:

- Wave and pulse shapers
- Astable multivibrators
- · Monostable multivibrators



CP C C O01aal185

Fig. 9. Astable multivibrator

Fig. 10. Schmitt trigger driven via a high-impedance input

If a Schmitt trigger is driven via a high-impedance (R > 1 k Ω), then it is necessary to incorporate a capacitor C with a value of $\frac{C}{C_P} > \frac{V_{DD} - V_{SS}}{V_H}$; otherwise oscillation can occur on the edges of a pulse.

 C_p is the external parasitic capacitance between inputs and output; the value depends on the circuit board layout.

14. Package outline

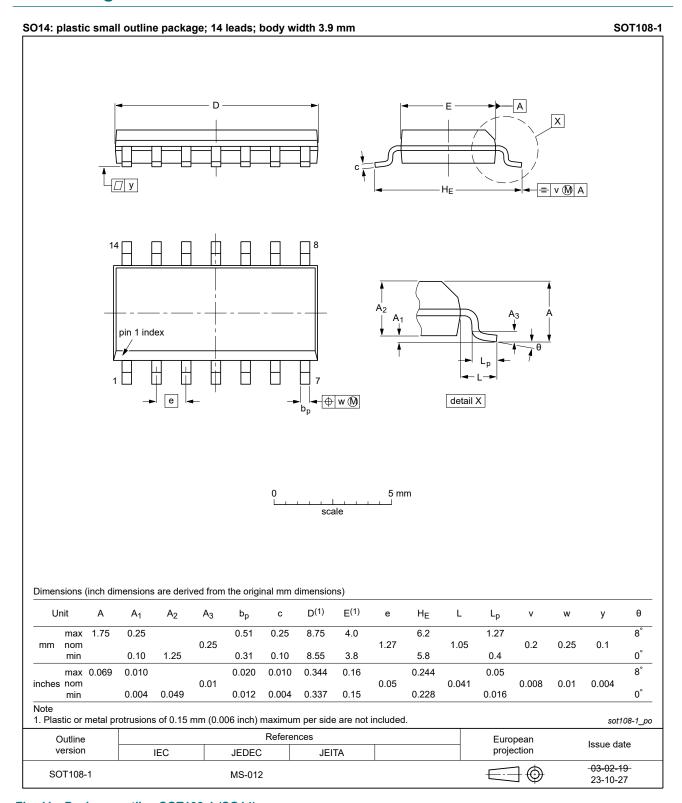


Fig. 11. Package outline SOT108-1 (SO14)

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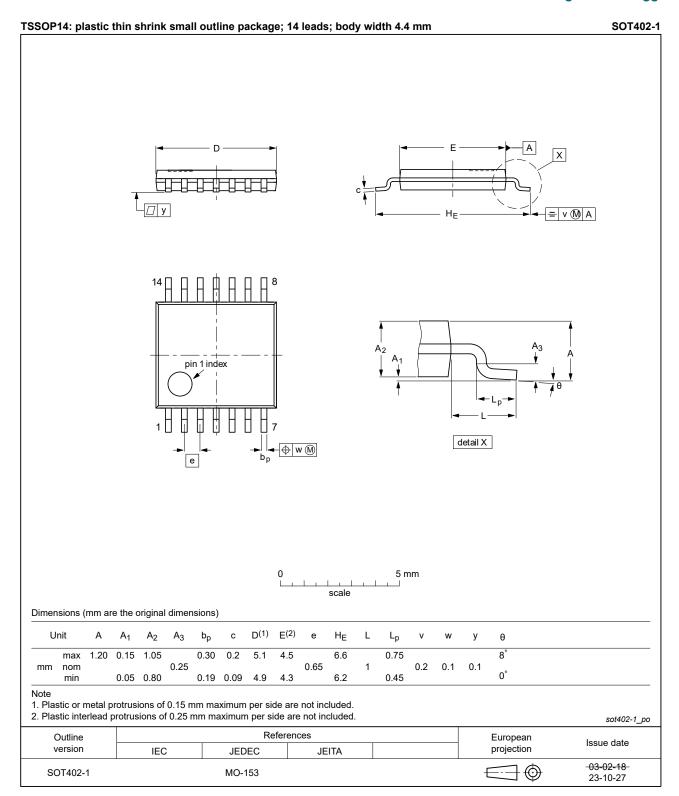


Fig. 12. Package outline SOT402-1 (TSSOP14)

15. Abbreviations

Table 12. Abbreviations

Acronym	Description
ANSI	American National Standards Institute
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
ESDA	ElectroStatic Discharge Association
НВМ	Human Body Model
JEDEC	Joint Electron Device Engineering Council

16. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
HEF40106B_Q100 v.4	20240808	Product data sheet	-	HEF40106B_Q100 v.3	
Modifications:	 Section 2: ESD specification updated according to the latest JEDEC standard. Fig. 11, Fig. 12: Aligned SO and TSSOP package outline drawings to JEDEC MS-012 and MO-153 				
HEF40106B_Q100 v.3	20221007	Product data sheet	-	HEF40106B_Q100 v.2	
Modifications	<u>Table 7</u> : Typo corrected.				
HEF40106B_Q100 v.2	20211122	Product data sheet	-	HEF40106B_Q100 v.1	
Modifications	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Section 1 and Section 2 updated. Table 4: Derating values for Ptot total power dissipation updated. 				
HEF40106B_Q100 v.1	20120807	Product data sheet	-	-	

17. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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Product data sheet

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