Ordering number : EN7117B



ON Semiconductor DATA SHEET

LB11983 — Monolithic Digital IC For Fan Motor Driver for Refrigerator 3-Phase Sensorless Motor Driver

Overview

The LB11983 is a 3-phase full-wave current linear sensorless motor driver. It is optimal for refrigerator fan motor drive.

Features

- Current linear driving technique.
- Current limiter circuit.
- Over saturation prevention circuit for output stage.
- Provides coil back EMF FG output.
- Thermal shoutdown circuit.
- Beat lock pervention circuit.

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

| _ | | | | |
|-----------------------------|---------------------|----------------|------------------------------|------|
| Parameter | Symbol | Conditions | Ratings | Unit |
| Supply voltage | V _{CC} max | | 14.5 | V |
| Output application voltage | V _O max | | 14.5 | V |
| Input application voltage | V _I max | | -0.3 to V _{CC} +0.3 | V |
| Output current | I _O max | | 1.0 | Α |
| Allowable power dissipation | Pd max | Independent IC | 1.0 | W |
| Operating temperature | Topr | | -30 to +85 | °C |
| Storage temperature | Tstg | | -55 to +150 | °C |

Operating Conditions at $Ta = 25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|----------------|--------|------------|-----------|------|
| Supply voltage | VCC | | 7 to 13.8 | V |

LB11983

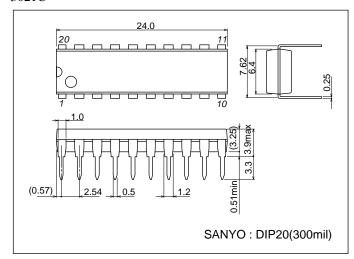
Electrical Characteristics at Ta = 25°C, $V_{CC} = 12V$

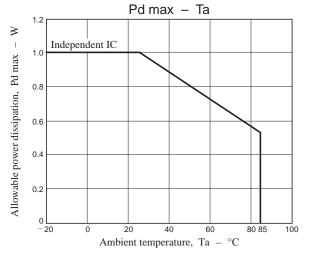
| Parameter | Cumbal | Conditions | Ratings | | | 1.114 | |
|--|---------------------|--|---------|------|--------------------|-------|--|
| Parameter | Symbol | Conditions | min | typ | max | Unit | |
| Supply current | Icc | $VC = V_{CC}$ | | 20 | 30 | mA | |
| Output saturation voltage 1 | V _O sat1 | I _O = 0.4A, Source + Sink | | 1.4 | 2.0 | ٧ | |
| Output saturation voltage 2 | V _O sat2 | $I_O = 0.8A$, Source + Sink, RF = 0Ω | | 1.8 | 2.6 | ٧ | |
| MCOM pin common-mode input voltage range | VIC | | 0 | | V _{CC} -2 | ٧ | |
| PCOUT output current 1 | IPCOU | Source side | | -90 | | μΑ | |
| PCOUT output current 2 | IPCOD | Sink side | | 90 | | μΑ | |
| VCOIN input current | IVCOIN | VCOIN = 5V | | 0.1 | 0.2 | μΑ | |
| VCO minimum frequency | fVCOMIN | VCOIN = open | | 400 | | Hz | |
| VCO maximum frequency | fVCOMAX | VCOIN = 5V | | 18.5 | | kHz | |
| C1, C2 source current ratio | RSOURCE | IC1SOURCE/IC2SOURCE | -12 | | +12 | % | |
| C1, C2 sink current ratio | RSINK | IC1SINK/IC2SINK | -12 | | +12 | % | |
| C1 source and sink current ratio | RC1 | IC1SOURCE/IC1SINK | -35 | | +15 | % | |
| C2 source and sink current ratio | RC2 | IC2SOURCE/IC2SINK | -35 | | +15 | % | |
| Counter FG output ON volt | V _{OL} | | | | 0.4 | V | |
| Counter FG output OFF vol | Vон | | 4 | | | V | |
| Thermal shutdown operating temperature | TTSD | Design target value * | 150 | 180 | 210 | °C | |
| Thermal shutdown hysteresis | ΔTTSD | Design target value * | | 15 | | °C | |

Note: * These items are design target values and are not tested.

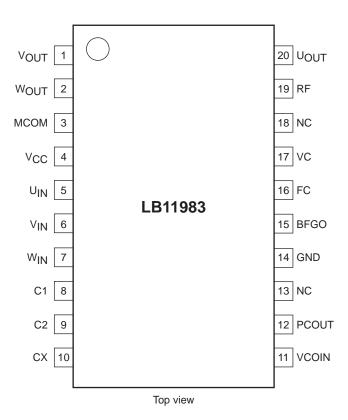
Package Dimensions

unit : mm (typ) 3021C

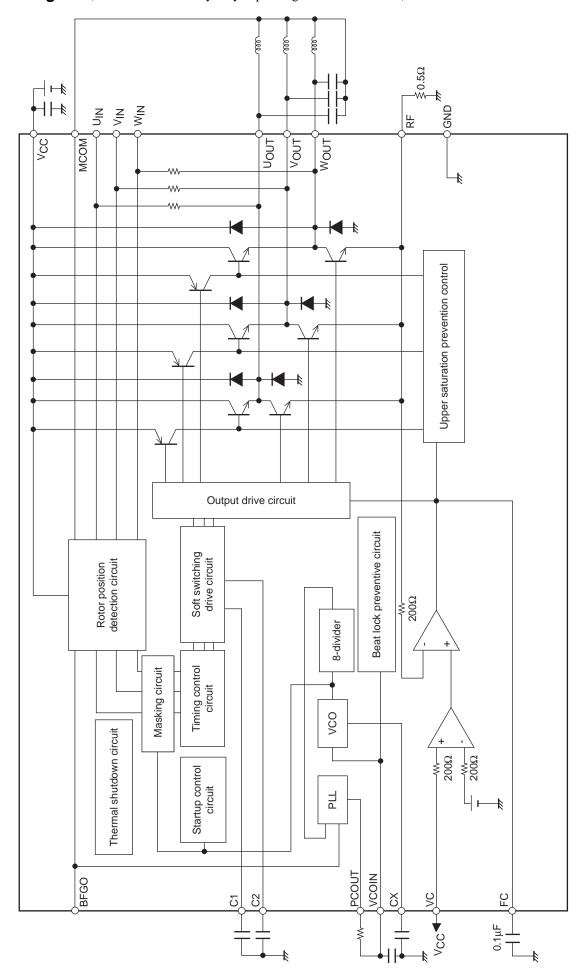




Pin Assignment



Block Diagram (External constant may vary depending on the motor used.)



LB11983

Pin Functions

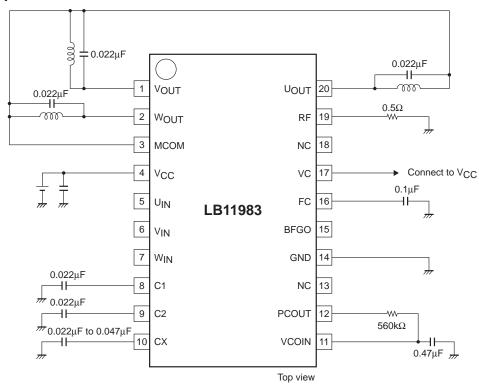
| | nctions | T | |
|---------|------------------|--|---|
| Pin No. | Pin name | Function | Equivalent circuit |
| 20 | U _{OUT} | Drum motor driver output pin. | Vcc |
| 2 | VOUT | | |
| 19 | WOUT RF | Minimum potential of the drum motor driver output transistor. This voltage is detected for constantcurrent control. The current limiter is also activated upon detection of this potential. | 3.9Ω 10kΩ 20μA 1 3.9Ω 20μ 5 3.9Ω 20 5 |
| 4 | VCC | Power supply pin. (8 to 13.8V) | |
| 3 | MCOM | Middle point input pin of motor coil. The coil waveform is detected with reference to this voltage. | Vcc |
| 5 | U _{IN} | Input pin of the coil waveform detection comparator. Connected to each phase output with a built-in resistor of $10 k\Omega$ | 5 \$10kΩ 200Ω 3 7 \$\frac{Q}{N}\$ 200Ω |
| 6 | VIN | | - 200Ω |
| 7 | WIN | | |
| 8 | C1 | Triangular wave generating capacitor connection pin. This triangsular wave causes soft switching of coil output waveform. | VCC VREG 15μΑ 15μΑ 9 5μΑ 1 |
| 9 | C2 | | 2S 1/2VREG-VF |
| 10 | СХ | The operating frequency range and minimum operating frequency are determined from the value of capacitor connected to this pin and GND in the VCO circuit. | VCC VREG 100μA 300Ω |
| 11 | VCOIN | VCO circuit voltage input pin. Inputs the PCOUT pin voltage through CR filtering. | 10kΩ \$ 50kΩ \$ 50μA \$ 50μA |

Continued on next page.

Continued from preceding page.

| Pin No. | Pin name | Function | Equivalent circuit |
|---------|----------|--|---|
| 12 | PCOUT | VCO circuit PLL output pin. | VREG VCC |
| 14 | GND | GND for others than the output transistor. | |
| 15 | BFGO | FG output to detect motor reverse feeder voltage. (Composition of three phases) | VREG + VF \sim VREG |
| 16 | FC | Frequency characteristics compensation pin. Insertion of a capacitor between this pin and GND stops oscillation of the current control closed loop. | VREG VCC 1kΩ 10kΩ ₹5kΩ |
| 17 | VC | Speed control pin. The control is a constant-current control under current feedback from RF. Normally, this pin is connected to V _{CC} for use. | VCC 50μΑ → 50μΑ ≥ 27kΩ 40kΩ → 200Ω → 7π |

Sample Application Circuit (Reference)



Notes 1. Be sure to connect the VC pin to VCC directly before use.

- 2. For the constant of capacitor, etc., our value established through examination is given for reference. Adjust the value according to the motor to be used when considering this IC.
- 3. If the output is not oscillated with the motor used, a capacitor inserted between output coil ends is not necessary.
- 4. Pins 5 through 7 (U_{IN}, V_{IN}, and W_{IN}) are not to be used by a user. These are connected inside IC and should always be kept independent and open.
- 5. NC pins (14 and 18) are not connected inside IC and can be used as relay pins.

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