Ordering number : ENA2035

## **LB1947VC**



http://onsemi.com

# PWM Current Control Type Forward/Reverse Motor Driver

#### Overview

The LB1947VC is a PWM current control type forward/reverse motor driver IC. The IC is optimal for use in driving brushed DC motors for printers.

#### **Features**

- PWM current control (fixed OFF time)
- Selectable current decay pattern (FAST, SLOW, and MIX DECAY modes)
- Simultaneous ON prevention function (feed-through current prevention)
- Built-in thermal shutdown circuit
- Built-in noise canceler

#### **Specifications**

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum motor supply voltage	V <sub>BB</sub> max		50	V
Output peak current	I <sub>O</sub> peak	t <sub>W</sub> ≤ 20μs	2.25	Α
Output continuous current	I <sub>O</sub> max		2.0	Α
Logic supply voltage	V <sub>CC</sub> max		7.0	V
Logic input voltage range	VIN		-0.3 to V <sub>CC</sub>	V
Emitter output voltage	V <sub>E</sub> max		1.1	V
Reference voltage	V <sub>REF</sub>		-0.3 to V <sub>CC</sub>	V
Allowable power dissipation	Pd max	Independent IC	1.3	W
Operating temperature	Topr		-20 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

Caution 1) Absolute maximum ratings represent the value which cannot be exceeded for any length of time.

Caution 2) Even when the device is used within the range of absolute maximum ratings, as a result of continuous usage under high temperature, high current, high voltage, or drastic temperature change, the reliability of the IC may be degraded. Please contact us for the further details.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

## Allowable Operating Ranges at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Motor supply voltage	V <sub>BB</sub>		10 to 45	٧
Logic supply voltage	VCC		4.75 to 5.25	V
Reference voltage	V <sub>REF</sub>		0 to V <sub>CC</sub> -2	V

## **Electrical Characteristics** at Ta = 25°C, $V_{CC} = 5V$

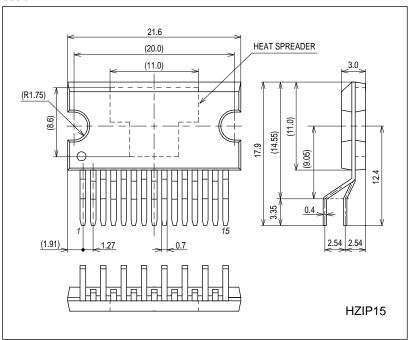
	1	<del>, ee</del>					
Parameter	Symbol	Conditions		Ratings		Unit	
- Gramoto		Conditions	min	typ	max	Jiil	
Output Block							
Output stage supply current	I <sub>BB</sub> ON	No-load state, Forward	0.4	0.6	1.0	mA	
	I <sub>BB</sub> BR	No-load state, Brake	0.2	0.4	0.8	mA	
	I <sub>BB</sub> OFF	No-load state, Output off	0.2	0.4	0.8	mA	
	I <sub>BB</sub> wt	No-load state, Standby mode			0.1	mA	
Output saturation voltage	V <sub>O</sub> sat1	I <sub>O</sub> = +1.0A, Sink		1.2	1.5	>	
	V <sub>O</sub> sat2	I <sub>O</sub> = +2.0A, Sink		1.6	1.9	>	
	V <sub>O</sub> sat3	I <sub>O</sub> = -1.0A, Source		1.8	2.2	V	
	V <sub>O</sub> sat4	I <sub>O</sub> = -2.0A, Source		2.1	2.4	V	
Output leak current	I <sub>O</sub> 1(leak)	$V_O = V_{BB}$ , Sink			50	μΑ	
	I <sub>O</sub> 2(leak)	V <sub>O</sub> = 0V, Source	-50			μΑ	
Output sustain voltage	V <sub>SUS</sub>	L = 3.9mH, I <sub>O</sub> = 2.0A, Design guarantee value*	50			V	
Logic Block							
Logic supply current	I <sub>CC</sub> ON	IN1: High, IN2: Low, ST: High	11	16	21	mA	
	I <sub>CC</sub> BR	IN1: Low, IN2: High, ST: High	11	16	21	mA	
	I <sub>CC</sub> OFF	IN1: Low, IN2: Low, ST: High	11	16	21	mA	
	I <sub>CC</sub> wt	ST: Low	1.0	2	3.0	mA	
Logic pin input voltage	V <sub>IN</sub> H	High level voltage	2			V	
(ST, IN1, IN2, VI)	V <sub>IN</sub> L	Low level voltage			0.8	V	
Logic pin input current	I <sub>IN</sub> H	V <sub>IN</sub> = 5V	60	90	120	μА	
(ST, IN1, IN2, VI)	I <sub>IN</sub> L	V <sub>IN</sub> = 0.8V	6	10	13	μΑ	
Sensing voltage	٧E		0		1.1	V	
Sensing voltage 25H	V <sub>EH</sub> 25	VI = High, V <sub>REF</sub> = 2.5V	0.970	1.0	1.030	V	
Sensing voltage 25L	V <sub>EL</sub> 25	VI = Low, V <sub>REF</sub> = 2.5V	0.483	0.5	0.513	V	
Sensing voltage 15H	V <sub>EH</sub> 15	VI = High, V <sub>REF</sub> = 1.5V	0.385	0.4	0.410	V	
Sensing voltage 15L	V <sub>EL</sub> 15	VI = Low, V <sub>REF</sub> = 1.5V	0.190	0.2	0.210	V	
Sensing voltage 05H	V <sub>EH</sub> 05	VI = High, V <sub>REF</sub> = 0.5V	0.190	0.2	0.210	V	
Sensing voltage 05L	V <sub>EL</sub> 05	VI = Low, V <sub>REF</sub> = 0.5V	0.092	0.1	0.108	V	
Reference current	Iref	V <sub>REF</sub> = 1.0V	-0.5		+0.5	μΑ	
CR pin current	ICR	CR = 1.0V	-1.56	-1.3	-1.04	mA	
MD pin input voltage	V <sub>MD</sub> H	High level voltage	V <sub>CC</sub> -0.3			V	
	V <sub>MD</sub> M	Middle level voltage	0.3V <sub>CC</sub>		V <sub>CC</sub> -1.0	V	
	V <sub>MD</sub> L	Low level voltage			.0.4	V	
MD pin input current	I <sub>MD</sub> H	MD = V <sub>CC</sub> -0.5V, CR = 1.0V	-1.0		+1.0	μΑ	
	I <sub>MD</sub> L	MD = 0.4V, CR = 2.0V	-5.0			μΑ	
Thermal shutdown temperature	TSD	Design guarantee value*		170		°C	

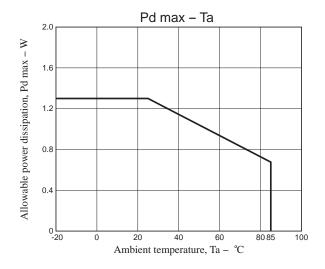
<sup>\*</sup> Design guarantee value, Do not measurement.

## **Package Dimensions**

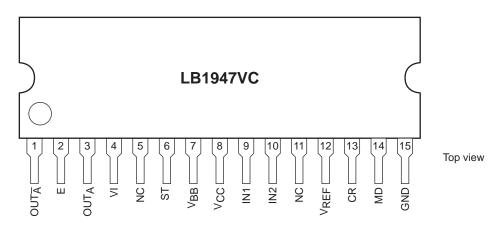
unit: mm (typ)

3336

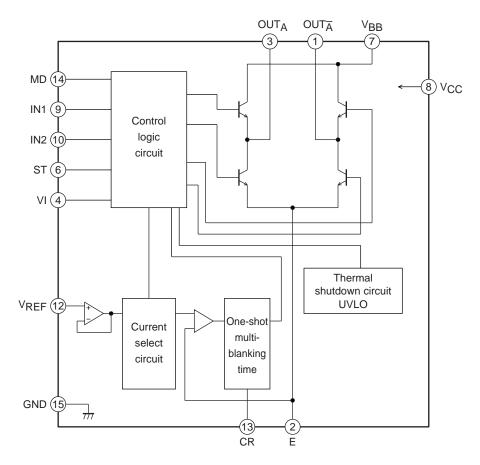




## **Pin Assignment**



## **Block Diagram**



#### **Truth Table**

IN1	IN2	ST	VI	MD	OUTA	OUTA	Operating mode	
Н	L	Н	Н	L	Н	L	Forward, 2/5 times, FAST	
Н	L	Н	Н	М	Н	L	Forward, 2/5 times, MIX	
Н	L	Н	Н	Н	Н	L	Forward, 2/5 times, SLOW	
Н	L	Н	L	L	Н	L	Forward, 1/5 times, FAST	
Н	L	Н	L	М	Н	L	Forward, 1/5 times, MIX	
Н	L	Н	L	Н	Н	L	Forward, 1/5 times, SLOW	
Н	Н	Н	Н	L	L	Н	Reverse, 2/5 times, FAST	
Н	Н	Н	Н	М	L	Н	Reverse, 2/5 times, MIX	
Н	Н	Н	Н	Н	L	Н	Reverse, 2/5 times, SLOW	
Н	Н	Н	L	L	L	Н	Reverse, 1/5 times, FAST	
Н	Н	Н	L	М	L	Н	Reverse, 1/5 times, MIX	
Н	Н	Н	L	Н	L	Н	Reverse, 1/5 times, SLOW	
L	Н	Н	Н	L	L	L	Brake, 2/5 times, FAST	
L	Н	Н	Н	М	L	L	Brake, 2/5 times, MIX	
L	Н	Н	L	L	L	L	Brake, 1/5 times, FAST	
L	Н	Н	L	М	L	L	Brake, 1/5 times, MIX	
L	Н	Н	Х	Н	L	L	Brake, no current limiting	
L	L	Н	Х	Х	OFF	OFF	Output OFF	
Х	Х	L or OPEN	Х	Х	OFF	OFF	Standby mode (circuit OFF)	

Except for MD pin, Low at input OPEN.

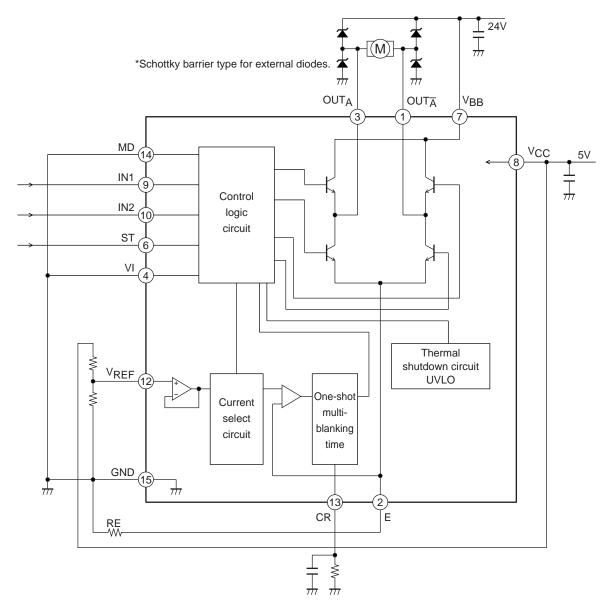
MD M: determined by external voltage.

## **Pin Function**

Pin No.	Pin name	Function	Equivalent circuit
1	OUTA	Output pin.	
3	OUTA		
2	E	Sense voltage control pin.	
4	VI	High: sense voltage is 2/5 of V <sub>REF</sub> Low: sense voltage is 1/5 of V <sub>REF</sub>	V <sub>CC</sub> 100μA
6	ST	High: circuit operation ON Low: standby mode	
9	IN1	High: rotation mode Low: brake mode	\$50kΩ W 40kΩ
10	IN2	High: reverse mode Low: forward mode	VI \$40.02
7	V <sub>BB</sub>	Motor power supply voltage.	
8	Vcc	Logic power supply voltage.	
12	VREF	Output current setting reference pin. Setting range: 0 to (V <sub>CC</sub> -2V)	V <sub>CC</sub> 5 <sub>μ</sub> A  3 <sub>s</sub> 1 <sub>s</sub> V <sub>REF</sub>
13	CR	Oscillator with self-excitation.	
14	MD	Current attenuation switching pin.  Low: FAST DECAY  High: SLOW DECAY  M: MIX DECAY  M is set by external power supply voltage.  Range: 1.1 to 4.0V	
15	GND	Ground pin	
5, 11	NC	No connect	

## **Sample Application Circuits**

1. Forward/reverse motor with current limiter

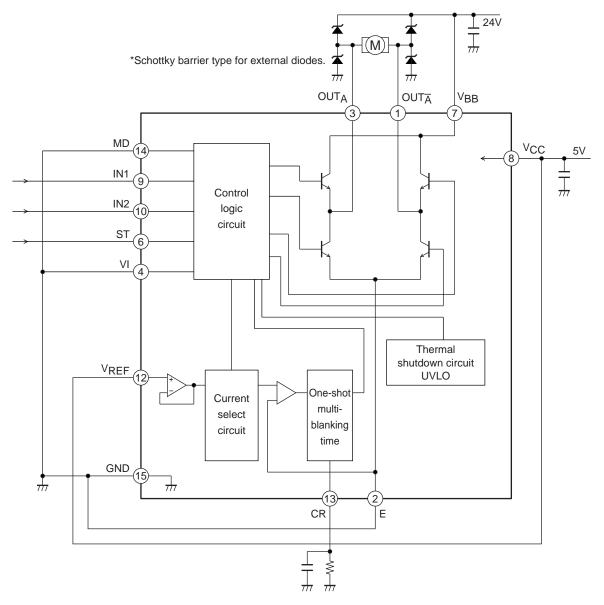


Limiter current setting method

 $I = V_{REF} / (5 \times RE)$ 

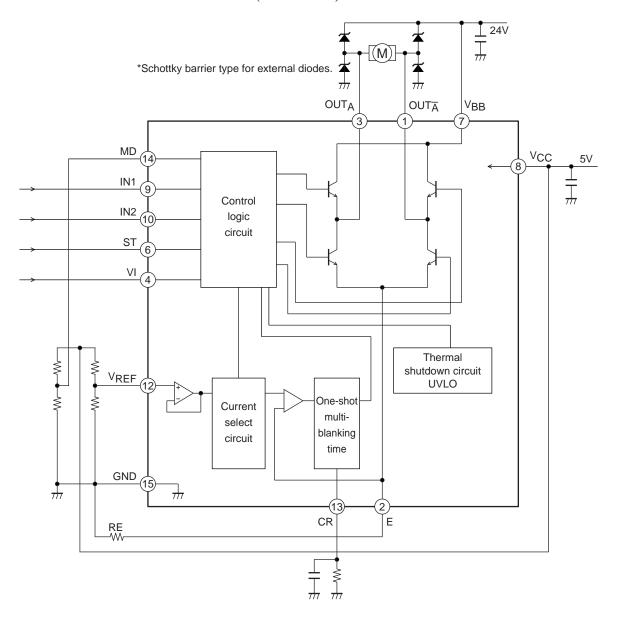
IN1	IN2	ST	OUTA	OUTA	Mode
Н	L	Н	Н	L	Forward
Н	Н	Н	L	Н	Reverse
L	Н	Н	L	L	Brake
L	L	Н	OFF	OFF	Output OFF
-	-	L	OFF	OFF	Standby mode

#### 2. Forward/reverse motor



IN1	IN2	ST	OUTA	OUTA	Mode
Н	L	Н	Н	L	Forward
Н	Н	Н	L	Н	Reverse
L	Н	Н	L	L	Brake
L	L	Н	OFF	OFF	Output OFF
-	-	L	OFF	OFF	Standby mode

#### 3. PWM current control forward/reverse motor (MIX DECAY)



#### **Notes on Usage**

#### 1. VREF pin

Because the VREF pin serves for input of the set current reference voltage, precautions against noise must be taken.

#### 2. GND pin

The ground circuit for this IC must be designed so as to allow for high-current switching. Blocks where high current flows must use low-impedance patterns and must be removed from small-signal lines. Especially the ground connection for the sensing resistor RE at pin E, and the ground connection for the Schottky barrier diodes should be in close proximity to the IC ground.

The capacitors between  $V_{CC}$  and ground, and  $V_{BB}$  and ground should be placed close to the  $V_{CC}$  and  $V_{BB}$  pins, respectively.

#### 3. CR pin setting (Switching off time, Noise cancel time)

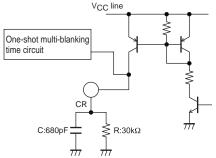
The noise cancel time (Tn) and the switching off time (Toff) are set by the following expressions:

Noise cancel time: Tn  $\approx$  C  $\times$  R  $\times$  ln  $\{(1.0 - RI) / (4.0 - RI) [sec]\}$ 

CR charge current: 1.3mA

Switching off time: Toff  $\approx -C \times R \times \ln (1.0 / 4.8)$  [sec]

#### Internal configuration at CR pin



CR constant range: R = 4.7k to  $100k\Omega$ C = 330pF to 2200pF

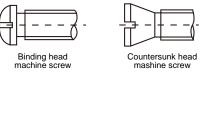
#### Heat sink attachment

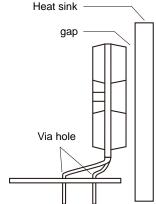
Heat sinks are used to lower the semiconductor device junction temperature by leading the head generated by the device to the outer environment and dissipating that heat.

a. Unless otherwise specified, for power ICs with tabs and power ICs with attached heat sinks, solder must not be applied to the heat sink or tabs.

#### b. Heat sink attachment

- · Use flat-head screws to attach heat sinks.
- · Use also washer to protect the package.
- · Use tightening torques in the ranges 39-59Ncm(4-6kgcm).
- · If tapping screws are used, do not use screws with a diameter larger than the holes in the semiconductor device itself.
- · Do not make gap, dust, or other contaminants to get between the semiconductor device and the tab or heat sink.
- · Take care a position of via hole.
- · Do not allow dirt, dust, or other contaminants to get between the semiconductor device and the tab or heat sink.
- · Verify that there are no press burrs or screw-hole burrs on the heat sink.
- · Warping in heat sinks and printed circuit boards must be no more than 0.05 mm between screw holes, for either concave or convex warping.
- · Twisting must be limited to under 0.05 mm.
- Heat sink and semiconductor device are mounted in parallel.
   Take care of electric or compressed air drivers
- The speed of these torque wrenches should never exceed 700 rpm, and should typically be about 400 rpm.





#### c. Silicone grease

- · Spread the silicone grease evenly when mounting heat sinks.
- · Our recommends YG-6260 (Momentive Performance Materials Japan LLC)

#### d. Mount

- · First mount the heat sink on the semiconductor device, and then mount that assembly on the printed circuit board.
- · When attaching a heat sink after mounting a semiconductor device into the printed circuit board, when tightening up a heat sink with the screw, the mechanical stress which is impossible to the semiconductor device and the pin doesn't hang.
- e. When mounting the semiconductor device to the heat sink using jigs, etc.,
  - · Take care not to allow the device to ride onto the jig or positioning dowel.
  - $\cdot$  Design the jig so that no unreasonable mechanical stress is not applied to the semiconductor device.

#### f. Heat sink screw holes

- · Be sure that chamfering and shear drop of heat sinks must not be larger than the diameter of screw head used.
- · When using nuts, do not make the heat sink hole diameters larger than the diameter of the head of the screws used. A hole diameter about 15% larger than the diameter of the screw is desirable.
- · When tap screws are used, be sure that the diameter of the holes in the heat sink are not too small. A diameter about 15% smaller than the diameter of the screw is desirable.
- g. There is a method to mount the semiconductor device to the heat sink by using a spring band. But this method is not recommended because of possible displacement due to fluctuation of the spring force with time or vibration.

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equa