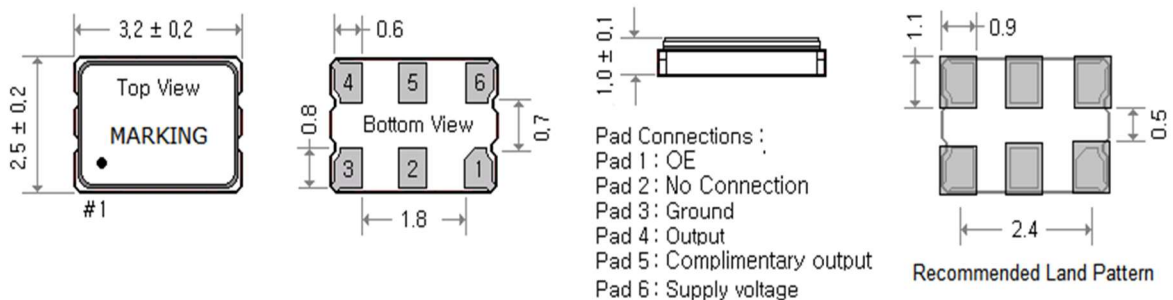


OSCLD-S3 Series: LVDS SMD Crystal Clock Oscillator 3.2 x 2.5 mm 6 Pads 13.500 - 220.000 MHz

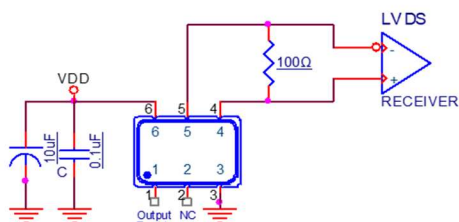
● SPECIFICATION

Model	OSCLD-S3 Series						
Power supply voltage	1.8V DC ±5%		2.5V DC ±5%		3.3V DC ±10%		
Output level	LVDS						
Frequency range	13.500 ~ 220.000 MHz						
Storage temperature range	- 55°C ~ + 125°C						
Operation temperature range	- 20°C ~ + 70°C , - 40°C ~ + 85°C , - 40°C ~ + 125°C , specify						
Frequency stability	±25 ppm ~ ±100 ppm						
Current consumption	16 mA (typ) , 27 mA (max)						
Output Load	100 Ω						
Output voltage level (VOH)	1.60V (max)						
Output voltage level (VOL)	0.90V (min)						
Differential Output voltage	350 mV (typ)						
Start-up Time	5 mS (typ) , 10 mS (max)						
Duty cycle	45% ~ 55%						
Enable input voltage (VIH)	1.260V (min)		1.750V (min)		2.310V (min)		
Disable input voltage (VIH)	0.540V (max)		0.750V (max)		0.990V (max)		
RISE / FALL TIME (20% / 80%)	0.3 nS (typ) , 0.5 nS (max)						
Phase Jitter RMS (12KHz~20KHz)	0.2 pS (typ) , 0.5 pS (max)						
SSB Phase Noise [dBc / Hz (typ.)]	Offset	10 Hz	100 Hz	1 KHz	10 KHz	100 KHz	1 MHz
	62.5 MHz	-50	-82	-116	-138	-144	-149
	156.250 MHz	-50	-80	-115	-135	-142	-147
Aging	±3ppm / Year (max)						

● DIMENSIONS (mm)



● TEST CIRCUIT



● OUTPUT WAVEFORM

