

# TCXO

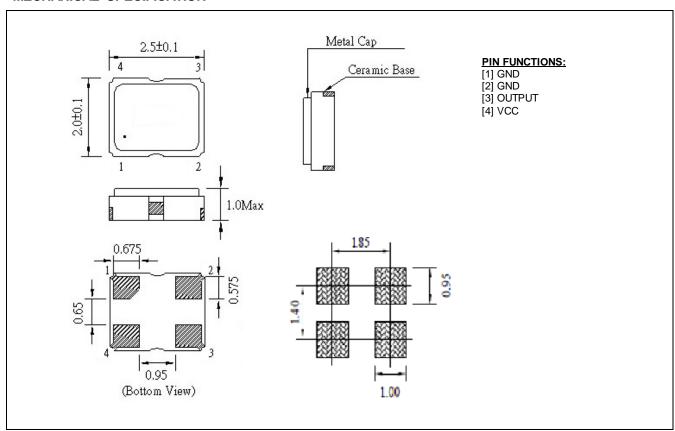
## RTX-2520AF333-S-32.000-TR

Page 1 of 3

#### **■ ELECTRICAL SPECIFICATION**

PARAMETER	SYMBOL	CONDITIONS	VALUE	UNIT
Nominal Frequency	fo	Vcc ±5%	32.000	MHz
Supply voltage, nom.	V <sub>cc</sub>	Vcc ±5%	3.3	VDC
Supply current, max	Is	Vcc ±5%	2.5	mA
Operating temperature	Та		-30 ~ +85	°C
Storage temperature	T(stg)	Absolute max	-40 ~ <b>+</b> 85	°C
Frequency Stability				
vs. Temperature	∆f/fo(Ta)	Reference to +25°±2°C (-30 to 85°C)	±0.5	ppm
vs. Supply Voltage	$\Delta f/f_{\lor}$	Vcc ±5%	±0.2	ppm
vs. Load	$\Delta f/f_L$	Load ±10%	±0.2	ppm
vs. Aging Max	∆f/fo(year)	Per Year at +25°C ± 2°C	±1.0	ppm
Initial Frequency Calibration, max	f <sub>C</sub>	Measured at 25°C, before shipment	±1.0	ppm
Reflow Shift, max		2 consecutive reflows, after 2 hours relaxation	±1.0	ppm
Start Up Time			2	ms
Output Level, Clipped Sine Wave, min		10kΩ // 10 pF ±10%	0.8	$V_{P-P}$
		@10Hz	-87	dBc/Hz
		@100Hz	-112	dBc/Hz
Phase Noise @ Freq. offset, typ	£ (∆f)	@1kHz	-132	dBc/Hz
		@10kHz	-147	dBc/Hz
		@100kHz	-151	dBc/Hz

#### MECHANICAL SPECIFICATION



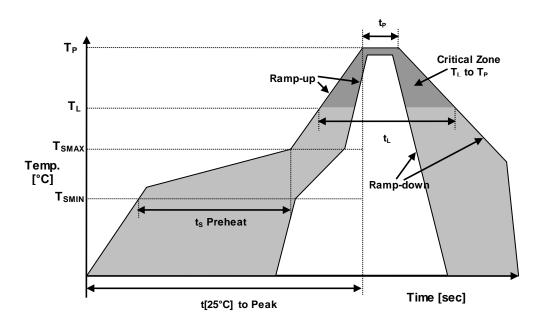


# **TCXO**

## RTX-2520AF333-S-32.000-TR

Page 2 of 3

#### **■ REFLOW PROFILE**



Reflow profile		
Temperature Min Preheat	T <sub>SMIN</sub>	150°C
Temperature Max Preheat	T <sub>SMAX</sub>	200°C
Time (T <sub>SMIN</sub> to T <sub>SMAX</sub> )	t <sub>S</sub>	60-180 sec.
Temperature	TL	217°C
Peak Temperature	T <sub>P</sub>	260°C
Ramp-up rate	R <sub>UP</sub>	3°C/sec max.
Ramp-down rate	R <sub>DOWN</sub>	6°C/sec max.
Time within 5°C of Peak Temperature	t <sub>P</sub>	10 sec.
Time t[25°C] to Peak Temperature	t[25°C] to Peak	480 sec.
Time	tı	60-150 sec.

## ENVIRONMENTAL

PARAMETER	VALUE
MOISTURE SENSITIVITY LEVEL	1
REACH	Compliant
RoHS	Compliant
TERMINATION FINISH	Au







## RTX-2520AF333-S-32.000-TR

Page 3 of 1

#### MARKING

Rx320 •AF3yw

x – Internal Production ID code

y – Year code

w - Week code

YEAR CODE		
Year	Code	
2018	8	
2019	9	
2020	0	
2021	1	
2022	2	
2023	3	
2024	4	
2025	5	
2026	6	
2027	7	
2028	8	
2029	9	

ALPHA WEEK CODE TABLE					
Week	Code	Week	Code	Week	Code
1	а	19	S	37	K
2	b	20	t	38	L
3	С	21	u	39	M
4	d	22	V	40	N
5	е	23	W	41	0
6	f	24	Х	42	Р
7	g	25	У	43	Q
8	h	26	Z	44	R
9	i	27	Α	45	S
10	j	28	В	46	Т
11	k	29	С	47	U
12	I	30	D	48	V
13	m	31	Е	49	W
14	n	32	F	50	X
15	0	33	G	51	Υ
16	р	34	Н	52	Z
17	q	35			
18	r	36	J		

## APPROVALS

RALTRON	
Created by, date:	KJackson, August 10, 2017
Eng. approval, date:	JIvens, August 10, 2017
Revision: A Initial Release B CP, November 17, 2021 Updated to the current spec level	

Rattron Electronics / RAMI Technology USA, LLC, including its affiliates, employees, agents and other persons acting on its behalf (collectively Rattron/RAMI Tech), disclaim any and all liability for any errors or inaccuracies contained in this data sheet. While Rattron/RAMI Tech has made every reasonable effort ensure the accuracy of all product information, specifications and data contained herein, Rattron/RAMI Tech does not guarantee that the information is accurate, reliable or current. The product information is provided only for reference purposes only and is subject to change, correction or revision, at any time without notice. Rattron/RAMI Tech does not assume any liability arising out of an application or use of any product described herein and disclaims any warranties expressed or implied. The user of products in such applications shall assume all risks of such use and will agree to hold Rattron/RAMI Tech, harmless against all damages.

Copyright © 2016, Raltron Electronics / RAMI Technology USA, LLC. All rights reserved. No part of this document may be reproduced in any form without the prior written permission of Raltron Electronics / RAMI Technology USA, LLC.