DELIVE	ERY SPE	<u>CIFICATIO</u>	N SPEC. N DATE	o. A-YFF-j : June, 2024
То		Non-Co	ontrolled (Сору
CUSTOMER'S PR	ODUCT NAME	Tape	JCT NAME ninal Feed Throug packaging 【RoHS 5, YFF18, YFF21,	2 compliant]
If orders are placed accepted by your s	d without returned s	K representatives with y specification, please all	our signature.	
		DATE: Y	EAR MO	NTH DAY
Test conditions i	n this specificatio	on based on AEC-Q2	200 for automotiv	e application.
TDK Corporation Sales Electronic Compon Sales & Marketing	ents	Engineering Electronic Compone		
APPROVED	Person in charge	APPROVED	CHECKED	Person in charge

SCOPE

This delivery specification shall be applied to 3-terminal feed through filter to be delivered to

PRODUCTION PLACES

Production places defined in this specification shall be TDK Corporation, TDK(Suzhou)Co.,Ltd and TDK Components U.S.A.,Inc.

PRODUCT NAME

The name of the product to be defined in this specifications shall be $\underline{YFF} \diamond OO \triangle \Delta \Box \Box \Box \times$.

CONTENTS

- 1. CODE CONSTRUCTION
- 2. RATED CURRENT
- 3. OPERATING TEMPERATURE RANGE
- 4. STORING CONDITION AND TERM
- 5. INDUSTRIAL WASTE DISPOSAL
- 6. PERFORMANCE
- 7. INSIDE STRUCTURE AND MATERIAL
- 8. PACKAGING
- 9. SOLDERING CONDITION
- 10. EQUIVALENT CIRCUIT DIAGRAM
- 11. CAUTION
- 12. TAPE PACKAGING SPECIFICATION

<EXPLANATORY NOTE>

When the mistrust in the spec arises, this specification is given priority. And it will be confirmed by written spec change after conference of both posts involved.

This specification warrants the quality of the 3-terminal feed through filter. Products should be evaluated or confirmed a state of mounted on your product.

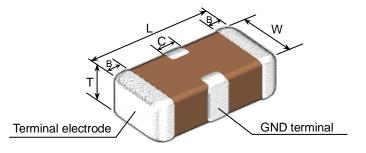
If the use of the products goes beyond the bounds of this specification, we can not afford to guarantee.

Date	SPEC. No.
June, 2024	A-YFF-j

1. CODE CONSTRUCTION

(Example)	YFF18	AC	1C	104	М	Т	0000
	YFF21	AC	1E	104	Μ	Т	<u>0000</u>
	YFF31	AH	2A	105	Μ	<u> </u>	<u>0000</u>
	(1)	(2)	(3)	(4)	(5)	(6)	(7)

(1)Type



Туре	Dimensions (mm)					
	L	W	Т	В	С	
YFF15	1.00±0.20	0.50±0.20	0.40±0.10	0.18±0.10	0.30±0.10	
	1.60±0.20	0.80±0.15	0.60±0.10			
YFF18		0.80±0.20	0.80±0.20	0.25±0.20	0.40±0.20	
	1.60 ^{+0.30} -0.10	0.80 ^{+0.30} -0.10	0.80 ^{+0.30} -0.10			
YFF21	2.00±0.20	1.25±0.20	0.85±0.15	0.30±0.20	0.50 ^{+0.30} -0.20	
YFF31	3.20±0.20	1.60±0.20	1.30±0.20	0.40±0.30	1.20±0.30	

*As for each item, please refer to to detail page on TDK web.

(2) Product Classification

Symbol Product Classification			
AC	For Automotive general use		
AH	For Automotive Large-current power Line		

(3)Rated Voltage

(4)Rated Capacitance	(Example)			
Stated in three digits and in units of pico farads (pF). The first and Second digits identify the first and second significant figures of the capacitance, the	Symbol	Rated Capacitance		
third digit identifies the multiplier.	104	100,000pF		
	105	1,000,000pF		
(5)Capacitance tolerance	Symbol	Tolerance		
	М	± 20 %		
(6)Packaging	Symbol	Packaging		
	Т	Taping		

(7)TDK internal code

2. RATED CURRENT

Rated current depend on operating temperature. As for details, please refer to detail page on TDK web.

3. OPERATING TEMPERATURE RANGE

Min. operating	Max. operating	Reference	
Temperature	Temperature	Temperature	
-55°C	125°C	25°C	

4. STORING CONDITION AND TERM

Storing temperature	Storing humidity	Storing term	
5~40°C	20~70%RH	Within 6 months upon receipt.	

5. INDUSTRIAL WASTE DISPOSAL

Dispose this product as industrial waste in accordance with the industrial Waste Law.

6. PERFORMANCE

Table 1

			Table 1			
No.	lt	em	Performance	Test or inspection method		
1	External Appearance		No defects which may affect performance.	Inspect with magnifying glass (3×).		
2	Insulation Resistance		10,000M Ω or 500M Ω · μ F min. (As for the products of rated voltage 16V DC and lower, 100M Ω · μ F min.), whichever smaller.	Measuring voltage : Rated voltage Voltage application time : 60s.		
3	Direct Curr Resistance (Rdc)		Please refer to detail page on TDK web.	Measuring current shall be 100mA max.		
4	Voltage Pro	oof	Withstand test voltage without insulation breakdown or other damage.	Apply voltage : 2.5 × rated voltage Voltage application time : 1s. Charge / discharge current : 50mA or lower		
5	5 Capacitance		Within the specified tolerance.	As for measuring condition, please contac with our sales representative.		
6	6 Robustness of Terminations		No sign of termination coming off, breakage of ceramic, or other abnormal signs.	Reflow solder the products on a P.C.Board shown in Appendix 2. Apply a pushing force gradually to a specimen as shown in the following figure. pushing force : 17.7N. (2N is applied for YFF15.) Pushing force Pushing force Solder land P.C.Board		
7	Bending	External appearance Capacitance	No mechanical damage.	Reflow solder the products on a P.C.Board shown in Appendix 1and bend it for 2mm. (1mm is applied for YFF18AC1A105M, YFF18AC0G106M,		
			Change from the value before test	YFF21AC1A475M and YFF31AH type.)		
			± 12.5 %	50 F		
		Direct current Resistance (Rdc)	Please contact with our sales representative.	2 45 45 45 45 45 45 45 2		
				(Unit : mm)		

(continued)

(cor	itinued)			1	
No.	lte	Item Performance		Test	or inspection method
8	Solderability		New solder to cover over 75% of termination. 25% may have pin holes or rough spots but not concentrated in one spot. Ceramic surface of A sections shall not be exposed due to	Solder : Flux :	Sn-3.0Ag-0.5Cu Isopropyl alcohol (JIS K 8839) Rosin (JIS K 5902) 25% solid solution.
			melting or shifting of termination	Solder temp. :	
			material.	Dwell time :	3±0.3s.
			A section	Solder position :	Until both terminations are completely soaked.
9	Resistance	External appearance	No cracks are allowed and terminations shall be covered at	Solder :	Sn-3.0Ag-0.5Cu
	to solder heat	Capacitance	least 60% with new solder.	Flux :	Isopropyl alcohol (JIS K 8839) Rosin (JIS K 5902)
			Change from the value before test		25% solid solution.
			± 7.5 %	Solder temp. :	
		Discut		Dwell time :	10±1s.
		Direct current resistance (Rdc)	Please contact with our sales representative.	Solder position : Pre-heating :	Until both terminations are completely soaked. Temp. — 110 ~ 140° C Time — 30 ~ $60s$.
					ducts in ambient condition for measurement.
10	Vibration	External appearance	No mechanical damage.	Applied force	
		Capacitance		Frequency : ² Reciprocating	g sweep time : 20 min.
			Change from the value before test	Cycle : 12 cy	cles in each 3 mutually endicular directions.
			± 7.5 %	perpe	
		Direct current resistance (Rdc)	Please contact with our sales representative.		r the products on a own in Appendix 2 before

(continued)

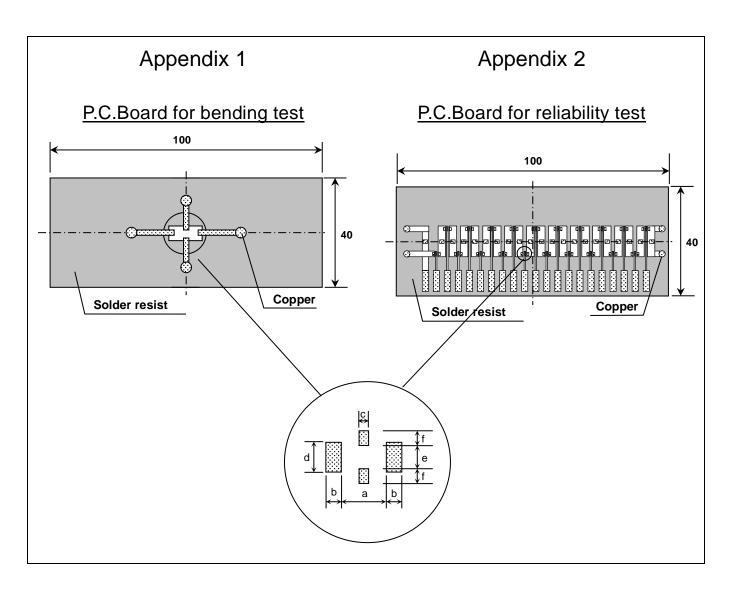
	ntinued)						
No.	lte	em	Performance	Test or inspection method			
11	Temperature cycle	External appearance Capacitance	No mechanical damage.	Expose the products in the condition step1 through step 4 listed in the following table.			
			Change from the value before test	Temp. o	cycle: 1,000 cycles		
			Please contact with our sales	Step	Temperature(°C)	Time (min.)	
		Discot	representative.	1	Min. operating temp. ±3	30 ± 3	
		Direct current	Please contact with our sales	2	Ambient Temp.	2 ~ 5	
		resistance (Rdc)	representative.	3	Max. operating temp. ±2	30 ± 2	
		Insulation Resistance	Meet the initial spec.	4	Ambient Temp.	2 ~ 5	
				As for Min./ Max. operating temp., please refer to "3.OPERATING TEMPERATURE RANGE". Leave the products in ambient condition for 24±2h before measurement. Reflow solder the products on a P.C.Board shown in Appendix 2 before testing.			
12	Moisture Resistance	External appearance	No mechanical damage.		mp.:40±2°C midity:90~95%RH		
	(Steady State)			Tost tin	Test time : 500 +24,0h		
			Change from the value before test		10 1 000 124,011		
			Please contact with our sales representative.		the products in ambie before measurement.	nt condition for	
		Direct current resistance (Rdc)	Please contact with our sales representative.		solder the products o in Appendix 2 before		
		Insulation Resistance	1,000M Ω or 50M Ω · μ F min. (As for the products of rated voltage 16V DC and lower, 10M Ω · μ F min.), whichever smaller.				

(continued)

No.	lt	em	Performance	Test or inspection method	
13	Moisture Resistance	External appearance	No mechanical damage.	Test temp. : 85±2°C Test humidity : 85%RH Applied voltage : Rated voltage	
		Capacitance	Change from the value before test Please contact with our sales representative. Please contact with our sales	Test time : 1,000 +48,0h Charge/discharge current : 50mA or lowe Leave the products in ambient condition for 24±2h before measurement.	
		current resistance (Rdc)	representative.	Reflow solder the products on a P.C.Board shown in Appendix2 before testing.	
		Insulation Resistance	500MΩ or 25MΩ·µF min. (As for the products of rated voltage 16V DC and lower, 5MΩ·µF min.), whichever smaller.	Initial value setting Voltage conditioning 《After voltage treat the products under testing temperature and voltage for 1 hour,》 leave the products in ambient condition for 24±2h before measurement. Use this measurement for initial value.	
14	Life	External appearance Capacitance	No mechanical damage.	Test temp. : Maximum operating temperature±2°C Applied voltage : Please contact with ou sales representative.	
			Please contact with our sales representative.	Test time : 1,000 +48,0h Charge/discharge current : 50mA or lov	
		Direct current resistance (Rdc)	Please contact with our sales representative.	Leave the products in ambient condition for 24±2h before measurement. Reflow solder the products on a	
		Insulation Resistance	1,000M Ω or 50M Ω ·µF min. (As for the products of rated voltage 16V DC and lower, 10M Ω ·µF min.), whichever smaller.	 P.C.Board shown in Appendix2 before testing. Initial value setting Voltage conditioning 《After voltage treat the products under testing temperature and voltage for 1 hour,》 leave the products in ambient condition for 24±2h before measurement. Use this measurement for initial value. 	

*As for the initial measurement of product on number 7, 9, 10, 11 and 12, leave products at 150 0,-10°C for 1h and measure the value after leaving product for 24±2h in ambient condition.

AC110A0010



(Unit:mm)

Symbol	Dimensions							
Туре	a	b	С	d	e	f		
YFF15	0.70	0.30	0.19	0.60	0.25	0.25		
YFF18	1.00	0.60	0.40	0.60	0.40	0.40		
YFF21	1.40	0.60	0.50	0.80	0.60	0.65		
YFF31	2.50	1.20	1.40	1.30	0.80	0.90		

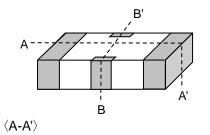
1. Material : Glass Epoxy(As per JIS C6484 GE4)

: Appendix 2 — 1.6mm

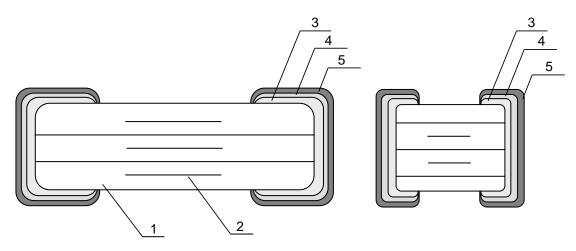
Copper Thickness: Appendix 1 — 0.035mm Appendix 2 — 0.070mm

Solder resist

7. INSIDE STRUCTURE AND MATERIAL



 $\langle \text{B-B'} \rangle$



No.	NAME	MATERIAL
1	Dielectric	CaZrO ₃ or BaTiO ₃
2	Electrode	Ni
3		Cu
4	Termination	Ni
5		Sn

8. PACKAGING

Packaging shall be done to protect the components from the damage during transportation and storing, and a label which has the following information shall be attached. Tape packaging is as per 12. TAPE PACKAGING SPECIFICATION.

- 1) Inspection No.
- 2) TDK P/N
- 3) Customer's P/N
- 4) Quantity

*Composition of Inspection No.

- Example $\underline{F} \ \underline{4} \ \underline{A} \ \ \underline{23} \ \ \underline{001}$ (a) (b) (c) (d) (e)
 - (a) Line code
 - (b) Last digit of the year
 - (c) Month and A for January and B for February and so on. (Skip I)
 - (d) Inspection Date of the month.
 - (e) Serial No. of the day

*Composition of new Inspection No.

(Implemented on and after May 1, 2019 in sequence)

Example

Э	I	F	4	Е	2	3	А	0	0	1
	(a)	(b)	(C)	(d)	(6	e)	(1	f)	(0	g)

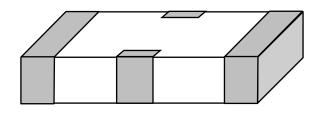
- (a) Prefix
- (b) Line code
- (c) Last digit of the year
- (d) Month and A for January and B for February and so on. (Skip I)
- (e) Inspection Date of the month.
- (f) Serial No. of the day(00 ~ ZZ)
- (g) Suffix(00 ~ ZZ)
- * It was shifted to the new inspection No. on and after May 2019, but the implementation timing may be different depending on shipment bases.

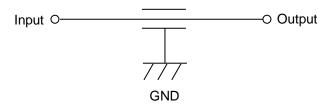
Until the shift is completed, either current or new composition of inspection No. will be applied.

9. SOLDERING CONDITION

Reflow soldering only.

10. EQUIVALENT CIRCUIT DIAGRAM





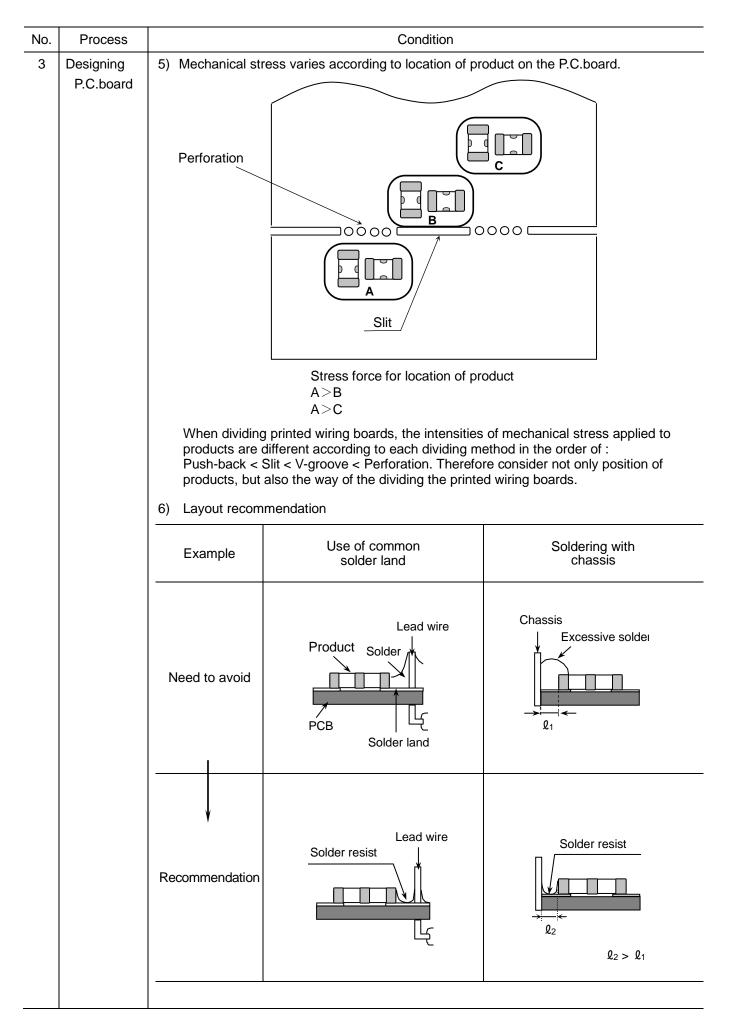
11. CAUTION

No.	Process	Condition
1	Operating	1-1. Storage, Use
	Condition (Storage, Use, Transportation)	The products must be stored in an ambient temperature of 5 to 40°C with a relative humidity of 20 to 70%RH. JIS C 60721-3-1 Class 1K2 should be followed for the other climatic conditions.
		 High temperature and humidity environment may affect a product's solder ability because it accelerates terminal oxidization. They also deteriorate performance of taping and packaging. Therefore, SMD products shall be used within 6 months. For products with terminal electrodes consisting of silver or silver-palladium which tend to become oxidized or sulfurized, use as soon as possible, such as within one month after opening the bag.
		 2) When products are stored for a longer time period than 6 months, confirm the solderability of the products prior to use. During storage, keep the minimum packaging unit in its original packaging without opening it. Do not deviate from the above temperature and humidity conditions even for a short term.
		3) Corrosive gasses in the air or atmosphere may result in deterioration of the reliability, such as poor solderability of the terminal electrodes. Do not store products where they will be exposed to corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine ammonia etc.)
		4) Solderability and electrical performance may deteriorate due to photochemical change in the terminal electrode if stored in direct sunlight, or due to condensation from rapid changes in humidity. The products especially which use resin material must be operated and stored in an environment free of dew condensation, as moisture absorption due to condensation may affect the performance.
		5) Refer to JIS C 60721-3-1, class 1K2 for other climate conditions.
		 1-2. Handling in transportation In case of the transportation of the products, the performance of the product may be deteriorated depending on the transportation condition. (Refer to JEITA RCR-2335C 9.2 Handling in transportation)
2	Circuit design	 2-1. Operating temperature 1) Upper category temperature (maximum operating temperature) is specified. It is necessary to select a product whose rated temperature us higher than the operating temperature. Also, it is necessary to consider the temperature distribution in the equipment and seasonal temperature variation.
		2) Surface temperature including self heating should be below maximum operating
		temperature. Due to dielectric loss, products will heat itself when AC is applied due to ESR. Especially at high frequencies, please be careful that the heat might be so extreme. Also, even if the surface temperature of the product includes self-heating and is the maximum operating temperature or lower, excessive heating of the product due to self-heating may cause deterioration of the characteristics and reliability of the product.
		The self-heating temperature rise of the product changes depending on the difference in heat radiation due to the mounting method to the device, the ambient temperature, the cooling method of the device and circuit board material and the design, etc. The load should be contained so that the self-heating temperature rise of the
		product body in a natural convection environment at an ambient temperature of 25°C remain below 20°C.
		When using in a high-frequency circuit or a circuit in which a product generates heat, such as when a high-frequency ripple current flows, pay attention to the above precautions. (Note that accurate measurement may not be possible with self-heating measurement when the equipment applies cooling other than natural convection such as a cooling fan.)

No.	Process	Condition
2	Circuit design	 The electrical characteristics of the products will vary depending on the temperature. The products should be selected and designed in taking the temperature into consideration.
		2-2. When overvoltage is applied
		Applying overvoltage to a product may cause dielectric breakdown and result in a short circuit. The duration until dielectric breakdown depends on the applied voltage and the ambient temperature.
		 2-3. Operating voltage 1) Operating voltage across the terminals should be below the rated voltage. When AC and DC are super imposed, V_{0-P} must be below the rated voltage.
		AC or pulse with overshooting, V_{P-P} must be below the rated voltage. — (3), (4) and (5)
		When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use the product within rated voltage containing these Irregular voltage.
		Voltage (1) DC voltage (2) DC+AC voltage (3) AC voltage
		Positional Measurement (Rated voltage)
		Voltage (4) Pulse voltage (A) (5) Pulse voltage (B)
		Positional Measurement (Rated voltage) V_{P-P} V_{P-P}
		 2) Even below the rated voltage, if repetitive high frequency AC or pulse is applied, the reliability of the products may be reduced.
		 The effective capacitance will vary depending on applied DC and AC voltages. The products should be selected and designed in taking the voltages into consideration.
		 Abnormal voltage (surge voltage, static electricity, pulse voltage, etc.) shall not exceed the rated voltage.
		5) When products are used in a series connection, it is necessary to add a balancing circuit such as voltage dividing resistors in order to avoid an imbalance in the voltage applied to each product.
		2-4. Frequency When the products are used in AC and/or pulse voltages, the products may vibrate themselves and generate audible sound.
		2-5. Derating currentThis product allows DC current to flow inside.Do not use this product above the rated DC current.

3 Designing P.C.board	The amount of solder a 1) The greater the am likely that it will brea solder lands to have 2) Avoid using common solder land for each 3) Size and recommen Chip mounted size <yff15></yff15>	rount of sold ak. When des proper amo n solder land terminations aded land din c Throu Ø0.1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	ler, the hig signing a F bunt of solo d for multip s. mensions.	gher the P.C.boar der on tl	YF Gre uni free obt	ess on the letermine t ermination fons and p l Resist l Land patt J Land patt Cound struct necessary equency ra the following nsidered a tain the be	e products, the shape a s. rovide indivi- tern tern & Resis s having th cture and e v noise at th inge. g points sh at the pad o etter perfor pattern sh d as big as	and the more and size of the vidual t he unique eliminate he wide ould be design to mance. hould be s possible.
	< -> <	a b /FF31>	→			1) Ground	pattern sh d as big as	ould be s possible.
					4			e and connect ern of the chip
			b	- f f	2	mounted 3) Through designe terminal 4) Connec GND ten patterns otes) *If thr solde the h	d side. h hole shou d as close l as possibl et all the inp rminals to e s. rough hole er paste wa nole and m nection with	uld be to GND le. but/output and each land is too big, ay came into
	Symbol	a	b	с		d	е	(mm) f
	YFF15	0.70	0.30	0.19		0.60	0.25	0.25
	YFF15	1.00	0.30	0.19		0.60	0.25	0.25
	YFF21	1.40	0.60	0.40		0.80	0.40	0.40
		2.50	0.00	0.00	·	1.30	0.80	0.90

No.	Process	Condition						
3	Designing P.C.board	4) Recommended product layout is as following.						
	1.0.50010		Disadvantage against bending stress	Advantage against bending stress				
		Mounting face						
		face	Break P.C.board with mounted side up.	Break P.C.board with mounted side down.				
			Mount perpendicularly to perforation or slit	Mount in parallel with perforation or slit				
		Chip arrangement (Direction)	Perforation or slit	Perforation or slit				
			Closer to slit is higher stress	Away from slit is less stress				
		Distance from slit	$\begin{pmatrix} l_1 \\ \vdots \\ \vdots \\ \vdots \\ \vdots \\ \vdots \\ \vdots \\ (l_1 < l_2)$	$ \begin{array}{c} $				



No.	Process		Condition	Condition					
4	Mounting	 4-1. Stress from mounting head If the mounting head is adjusted too low, it may induce excessive stress in the product to result in cracking. Please take following precautions. 1) Adjust the bottom dead center of the mounting head to reach on the P.C.board surface and not press it. 2) Adjust the mounting head pressure to be 1 to 3N of static weight. 							
		 To minimize the impact energy from mounting head, it is important to provide support from the bottom side of the P.C.board. See following examples. 							
			Not recommended	Recommended					
		Single sided mounting	Crack	A support pin is not to be underneath the product.					
		Double-sides mounting	Solder peeling Crack	Support pin					
		to cause crack. Pl	ng jaw is worn out, it may give me ease control the close up dimensi preventive maintenance and repla	on of the centering jaw and					

	Process	Condition						
5	Soldering	5-1. Flux selection Flux can seriously affect the performance of products. Confirm the following to select the appropriate flux.						
		1) It is recommended to u Strong flux is not recom		ilux (less than 0.1wt% chlorine				
		2) Excessive flux must be	avoided. Please provide pr	oper amount of flux.				
		3) When water-soluble flux	x is used, enough washing i	s necessary.				
		5-2. Recommended Reflow	v soldering profile					
			Reflow soldering					
			Soldering Preheating	ral cooling				
		Peak Temp						
		Temp. (°C)	ΔT					
		dua		N				
		Ĕ						
		0 <u> </u>	Over 60 sec.					
			Peak Temp time					
		5-3. Recommended solder	ing peak temp and peak ter	np duration for Reflow solderir				
			ing peak temp and peak ter nended, but if Sn-37Pb mus	np duration for Reflow solderin t be used, refer to below.				
		Pb free solder is recomm	nended, but if Sn-37Pb mus	t be used, refer to below.				
			nended, but if Sn-37Pb mus	t be used, refer to below.				
		Pb free solder is recomm	nended, but if Sn-37Pb mus	t be used, refer to below.				
		Pb free solder is recomm	nended, but if Sn-37Pb must uration Reflow s Peak temp(°C)	be used, refer to below.				
		Pb free solder is recomm	nended, but if Sn-37Pb must uration Reflow s Peak temp(°C)	t be used, refer to below. oldering Duration(sec.)				
		Pb free solder is recomm Temp./Du Solder Lead Free So Sn-Pb Solder	hended, but if Sn-37Pb must uration Reflow s Peak temp(°C) Ider 260 max. 230 max.	t be used, refer to below. oldering Duration(sec.) 10 max.				
		Pb free solder is recomm Temp./Du Solder Lead Free So Sn-Pb Solder Recommended solder	nended, but if Sn-37Pb must uration Reflow s Peak temp(°C) Ider 260 max. 230 max. compositions	t be used, refer to below. oldering Duration(sec.) 10 max.				
		Pb free solder is recomm Temp./Du Solder Lead Free So Sn-Pb Solder Recommended solder of Lead Free Solder : Sn	hended, but if Sn-37Pb must uration Reflow s Peak temp(°C) Ider 260 max. 230 max. compositions h-3.0Ag-0.5Cu	t be used, refer to below. oldering Duration(sec.) 10 max.				
		Pb free solder is recomm Temp./Du Solder Lead Free So Sn-Pb Solder Recommended solder Lead Free Solder : Sn 5-4. Avoiding thermal shock	hended, but if Sn-37Pb must uration Reflow s Peak temp(°C) Ider 260 max. 230 max. compositions h-3.0Ag-0.5Cu	t be used, refer to below. oldering Duration(sec.) 10 max.				
		Pb free solder is recomm Temp./Du Solder Lead Free So Sn-Pb Solder Recommended solder of Lead Free Solder : Sn	hended, but if Sn-37Pb must uration Reflow s Peak temp(°C) Ider 260 max. 230 max. compositions h-3.0Ag-0.5Cu	t be used, refer to below. oldering Duration(sec.) 10 max.				
		Pb free solder is recomm Temp./Du Solder Lead Free So Sn-Pb Solder Recommended solder Lead Free Solder : Sn 5-4. Avoiding thermal shock	hended, but if Sn-37Pb must uration Reflow s Peak temp(°C) Ider 260 max. 230 max. compositions h-3.0Ag-0.5Cu	t be used, refer to below. oldering Duration(sec.) 10 max.				
		Pb free solder is recomm Temp./Du Solder Lead Free So Sn-Pb Solder Recommended solder Lead Free Solder : Sn 5-4. Avoiding thermal shock 1) Preheating condition	hended, but if Sn-37Pb must uration Reflow s Peak temp(°C) Ider 260 max. 230 max. compositions h-3.0Ag-0.5Cu k	t be used, refer to below. oldering Duration(sec.) 10 max.				
		Pb free solder is recomm Temp./Du Solder Lead Free So Sn-Pb Solder Recommended solder Lead Free Solder : Sn 5-4. Avoiding thermal shock 1) Preheating condition Soldering Reflow soldering	rended, but if Sn-37Pb must reaction Reflow s Peak temp(°C) Ider 260 max. 230 max. compositions r-3.0Ag-0.5Cu k Temp. (°C)	t be used, refer to below. oldering Duration(sec.) 10 max.				
		Pb free solder is recomm Temp./Du Solder Lead Free So Sn-Pb Solder Recommended solder of Lead Free Solder : Sn 5-4. Avoiding thermal shock 1) Preheating condition Soldering Reflow soldering 2) Cooling condition	hended, but if Sn-37Pb must ration Reflow s Peak temp(°C) Ider 260 max. 230 max. compositions h-3.0Ag-0.5Cu k Temp. (°C) $\Delta T \leq 150$	t be used, refer to below. oldering Duration(sec.) 10 max. 20 max.				
		Pb free solder is recomm Temp./Du Solder Lead Free So Sn-Pb Solder Recommended solder of Lead Free Solder : Sn 5-4. Avoiding thermal shock 1) Preheating condition Soldering Reflow soldering 2) Cooling condition Natural cooling using	hended, but if Sn-37Pb must ration Reflow s Peak temp(°C) Ider 260 max. 230 max. compositions h-3.0Ag-0.5Cu k Temp. (°C) $\Delta T \leq 150$	t be used, refer to below. oldering Duration(sec.) 10 max. 20 max.				

No.	Process	Condition					
5	Soldering	5-5. Amount of solder Excessive solder will induce higher tensile force in product when temperature changes and it may result in chip cracking. In sufficient solder may detach the product from the P.C.board.					
		Excessive solder Higher tensile force in product to cause crack					
		Adequate					
		Insufficient solder Low robustness may cause contact failure or product come off the P.C.board.					
		 5-6. Sn-Zn solder Sn-Zn solder affects product reliability. Please contact TDK in advance when utilize Sn-Zn solder. 5-7. Countermeasure for tombstone The misalignment between the mounted positions of the products and the land patterns should be minimized. The tombstone phenomenon may occur especially the products are mounted (in longitudinal direction) in the same direction of the reflow soldering. (Refer to JEITA RCR-2335C Annex A (Informative) Recommendations to prevent the tombstone phenomenon) 					

No.	Process		Condi	ition						
6	Solder repairing	Solder repairing is unavoid	dable, refer to belo	OW.						
		6-1. Solder repair by solde								
		1) Selection of the solder								
		Tip temperature of solder iron varies by its type, P.C.board material and solder								
		land size. The higher	the tip temperatu	re, the quicker the	operation. However.					
		heat shock may cause a crack in the product. Please make sure the tip temp. before soldering and keep the peak temp								
		time in accordance with following recommended condition.								
		Manual soldering (Solder iron)								
		Peak		r fron)	7					
		Temp	\uparrow	_\`						
		Q								
		0 L								
		Recommended solder iron condition (Sn-Pb Solder and Lead Free								
					· · · · ·					
		Temp. (°C)	Duration (sec.)	Wattage (W)	Shape (mm)					
		350 max.	3 max.	20 max.	Ø 3.0 max.					
		* Please preheat the produ	ucts with the condit	ion in 6-2 to avoid	the thermal shock.					
		2) Direct contact of the s	oldering iron with	ceramic dielectric	of products may cause					
		 Direct contact of the soldering iron with ceramic dielectric of products may cause crack. Do not touch the ceramic dielectric and the terminations by solder iron. 								
		2) It is not recommende	d to rouge diamou	unted producto						
		3) It is not recommende	a to reuse dismou	inted products.						
		6-2. Avoiding thermal show	ck							
		Preheating condition								
		Soldering	Tem	ıp. (°C)						
		Manual solde	ring ΔT	≦ 150						
			I							

No.	Process	Condition						
7	Cleaning	 If an unsuitable cleaning fluid is used, flux residue or some foreign articles may stick to product surface to deteriorate especially the insulation resistance. 						
		2) If cleaning condition is not suitable, it may damage the product.						
		2)-1. Insufficient washing						
		(1) Terminal electrodes may corrode by Halogen in the flux.						
		(2) Halogen in the flux may adhere on the surface of product, and lower the insulation resistance.						
		(3) Water soluble flux has higher tendency to have above mentioned problems (1) and (2).						
		2)-2. Excessive washing						
		When ultrasonic cleaning is used, excessively high ultrasonic energy output can affect the connection between the ceramic product body and the terminal electrode. To avoid this, following is the recommended condition.						
		Power : 20 W/ℓ max.						
		Frequency : 40 kHz max.						
		Washing time : 5 minutes max.						
		 If the cleaning fluid is contaminated, density of Halogen increases, and it may bring the same result as insufficient cleaning. 						
8	Coating and molding of the	1) When the P.C.board is coated, please verify the quality influence on the product.						
	P.C.board	 Please verify carefully that there is no harmful decomposing or reaction gas emission during curing which may damage the product. 						
		3) Please verify the curing temperature.						
9	Handling after chip mounted	 Please pay attention not to bend or distort the P.C.board after soldering in handling otherwise the product may crack. 						
	∠! Caution	Bend Twist						

No.	Process	Condition							
9	Handling after chip mounted	 2) Printed circuit board cropping should not be carried out by hand, but by using the proper tooling. Printed circuit board cropping should be carried out using a board cropping jig as shown in the following figure or a board cropping apparatus to prevent inducing mechanical stress on the board. (1)Example of a board cropping jig Recommended example: The board should be pushed from the back side, close to the cropping jig so that the board is not bent and the stress applied to the product is compressive. Unrecommended example: If the pushing point is far from the cropping jig and the pushing direction is from the front side of the board, large tensile stress is applied to the product, which may cause cracks. 							
		Outline of jig Printed Circuit board Slot Printed Components Slot							
		 (2)Example of a board cropping machine An outline of a printed circuit board cropping machine is shown below. The top and bottom blades are aligned with one another along the lines with the V-grooves on printed circuit board when cropping the board. Unrecommended example: Misalignment of blade position between top and bottom, right and left, or front and rear blades may cause a crack in the product. 							
		Outline of machine Principle of operation Top blade Printed circuit board Printed circuit board 0 V-groove Bottom blade							
		Cross-section diagram Printed circuit board V-groove Bottom blade							
		Unrecommended Recommended Top-bottom Left-right Front-rear Top blade Top blade Top blade Top blade Top blade Board Top blade Top blade Top blade Top blade							
		Bottom blade Bottom blade Bottom blade Bottom blade							

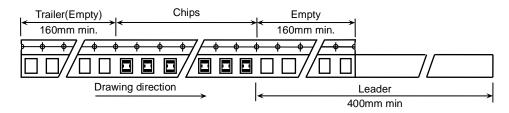
No.	Process	Condition					
9	Handling after chip mounted Caution	3) When functional check of the P.C.board is performed, check pin pressure tends to be adjusted higher for fear of loose contact. But if the pressure is excessive and bend the P.C.board, it may crack the product or peel the terminations off. Please adjust the check pins not to bend the P.C.board.					
		Item Not recommended Recommended					
		Board bending					
10	Handling of loose product	 If dropped the product may crack. Once dropped do not use it. Especially, the large case sized product are tendency to have cracks easily, so please handle with care. 					
		 2) Piling the P.C.board after mounting for storage or handling, the corner of board may hit the product of another board to cause crack. P.C.board Crack 					

No.	Process	Condition
11	Caution during operation of equipment	 A product shall not be touched directly with bare hands during operation in order to avoid electric shock. Electric energy held by the product may be discharged through the human body when touched with a bare hand. Even when the equipment is off, a product may stay charged. The product should be handled after being completely discharged using a resistor. The terminals of a product shall not be short-circuited by any accidental contact with a conductive object. A product shall not be exposed to a conductive liquid such as an acid or alkali solution. A conductive object or liquid, such as acid and alkali, between the terminals may lead to the breakdown of a product due to short circuit Confirm that the environment to which the equipment will be exposed during
		 (1) Environment where a product is spattered with water or oil (2) Environment where a product is spattered with water or oil (3) Environment where a product is exposed to direct sunlight (3) Environment where a product is exposed to Ozone, ultraviolet rays or radiation (4) Environment where a product exposed to corrosive gas(e.g. hydrogen sulfide, sulfur dioxide, chlorine. ammonia gas etc.) (5) Environment where a product exposed to vibration or mechanical shock exceeding the specified limits. (6) Atmosphere change with causes condensation
12	Others Caution	The product listed in this specification is intended for use in automotive applications under-normal operation and usage conditions. The product is not designed or warranted to meet the requirements of application listed below, whose performance and/or quality requires a more stringent level of safety or reliability, or whose failure, malfunction or defect could cause serious damage to society, person or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below or for any other use exceeding the range or conditions set forth in this specification sheet. If you intend to use the products in the applications listed below or if you have special requirements exceeding the range or conditions set forth in this specification, please contact us. (1) Aerospace/Aviation equipment (2) Transportation equipment (electric trains, ships etc.) (3) Medical equipment (Excepting Pharmaceutical Affairs Law classification Class1, 2) (4) Power-generation control equipment (5) Atomic energy-related equipment (6) Seabed equipment (7) Transportation control equipment (8) Public information-processing equipment (10) Electric heating apparatus, burning equipment (11) Disaster prevention/crime prevention equipment (12) Safety equipment (13) Other applications that are not considered general-purpose applications, you are kindly requested to take into consideration securing protection circuit/device or providing backup circuits in your equipment. In addition, although the product listed in this specification is intended for use in automotive applications as described above, it is not prohibited to use for general electronic equipment, whose performance and/or quality doesn't require a more stringent level of safety or reliability, or whose failure, malfunction or defect could not cause serious damage to society, person or property. Therefore, the description of this caution will be applied, when the product is used in general electronic equipment under

12. TAPE PACKAGING SPECIFICATION

1. CONSTRUCTION AND DIMENSION OF TAPING

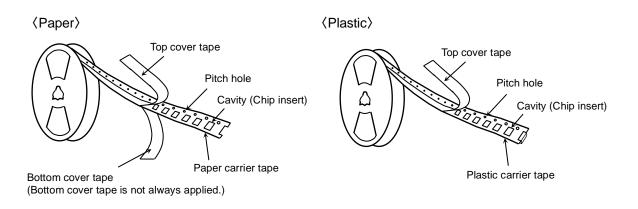
- 1-1. Dimensions of carrier tape
 - Dimensions of paper tape shall be according to Appendix 3, 4. Dimensions of plastic tape shall be according to Appendix 5.
- 1-2. Bulk part and leader of taping



1-3. Dimensions of reel

Dimensions of ϕ 178 reel shall be according to Appendix 6. Dimensions of ϕ 330 reel shall be according to Appendix 7.

1-4. Structure of taping

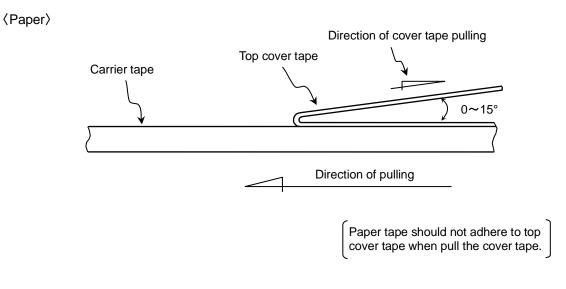


2. PRODUCT QUANTITY

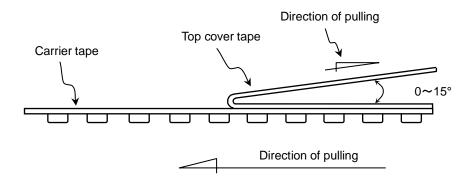
Please refer to detail page on TDK web..

3. PERFORMANCE SPECIFICATIONS

3-1. Fixing peeling strength (top tape) 0.05N < Peeling strength < 0.7N

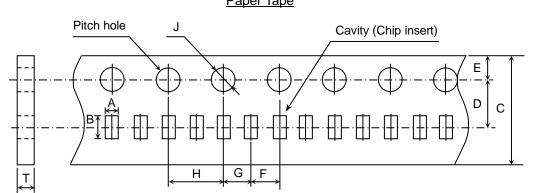


<Plastic>



- 3-2. Carrier tape shall be flexible enough to be wound around a minimum radius of 30mm with components in tape.
- 3-3. The missing of components shall be less than 0.1%
- 3-4. Components shall not stick to fixing tape.
- 3-5. When removing the cover tape, there shall not be difficulties by unfitting clearance gap, burrs and crushes of cavities. Also the sprocket holes shall not be covered by absorbing dust into the suction nozzle.

Appendix 3 Paper Tape



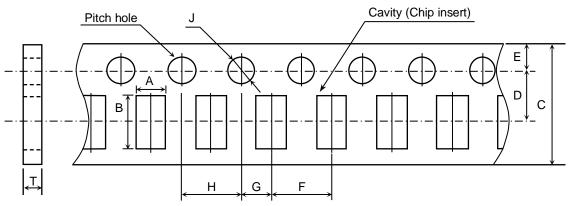
(Unit : mm)

Symbol Type	А	В	С	D	Е	F
YFF15	(0.75)	(1.18)	8.00 ± 0.30	3.50 ± 0.05	1.75 ± 0.10	2.00 ± 0.05
Symbol Type	G	Н	J	Т		
YFF15	2.00 ± 0.05	4.00 ± 0.10	Ø 1.50 +0.10 0	0.70 max.		
	nco valuo					

() Reference value.

Appendix 4

Paper Tape

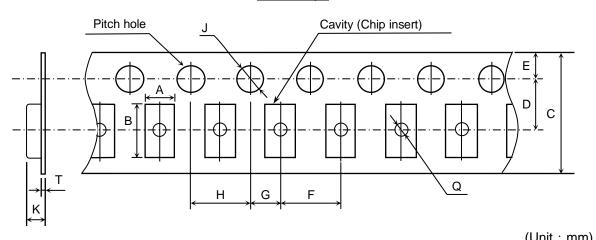


(Unit : mm)

Symbol Type	А	В	С	D	E	F	
YFF18	(1.10)	(1.90)	8.00 ± 0.30	3.50 ± 0.05	1.75 ± 0.10	4.00 ± 0.10	
YFF21	(1.50)	(2.30)	0.00 ± 0.30	5.50 ± 0.05	1.75 ± 0.10	4.00 ± 0.10	
Symbol Type	G	Н	J	Т			
YFF18	2.00 ± 0.05	4.00 ± 0.10	Ø 1.5 +0.10	1.20 max.			
YFF21	2.00 ± 0.05	4.00 ± 0.10	0 1.5	1.20 MdX.			
() Reference value.							

— 26 —

Appendix 5 Plastic Tape



						(Unit : mm)
Symbol Type	А	В	С	D	Е	F
YFF18 (10μF)	(1.10)	(1.90)	8.00±0.30	3.50±0.05	1.75±0.10	4.00±0.10
YFF31	(1.90)	(3.50)				
Symbol Type	G	Н	J	К	т	Q
YFF18 (10μF)	2.00±0.05	4.00±0.10	$\phi_{1.5}^{+0.10}$	1.60 max.	0.30 max.	ϕ 0.50 min.
YFF31			¢ 0	2.50 max.		F F

() Reference value.

Appendix 6 Dimensions of reel (Material : Polystyrene)								
		R	C			Unit : mm)		
Symbol	А	В	С	D	E	W1		
Dimension	φ178±2.0	φ60±2.0	φ13±0.5	φ21±0.8	2.0±0.5	9.0±0.3		
Symbol	W2	R						
Dimension	13.0±1.4	1.0						

Appendix 7 Dimensions of reel (Material : Polystyrene)

