

DELIVERY SPECIFICATION

SPEC. No. C-YFF-i

D A T E : June,2024

To

Non-Controlled Copy

CUSTOMER'S PRODUCT NAME	TDK'S PRODUCT NAME
	3-terminal Feed Through Filter
	Tape packaging 【RoHS2 compliant】
	YFF15,YFF18,YFF21,YFF31 Type

Please return this specification to TDK representatives with your signature.
If orders are placed without returned specification, please allow us to judge that specification is accepted by your side.

RECEIPT CONFIRMATION

DATE: YEAR MONTH DAY

TDK Corporation
Sales
Electronic Components
Sales & Marketing Group

Engineering
Electronic Components Business Company

APPROVED	Person in charge

APPROVED	CHECKED	Person in charge

SCOPE

This delivery specification shall be applied to 3-terminal feed through filter to be delivered to _____.

PRODUCTION PLACES

Production places defined in this specification shall be TDK Corporation, TDK(Suzhou)Co.,Ltd and TDK Components U.S.A.,Inc.

PRODUCT NAME

The name of the product to be defined in this specifications shall be YFF○○◇◇△△□□□×.

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<EXPLANATORY NOTE>

When the mistrust in the spec arises, this specification is given priority. And it will be confirmed by written spec change after conference of both posts involved.
This specification warrants the quality of the 3-terminal feed through filter. Products should be evaluated or confirmed a state of mounted on your product.
If the use of the products goes beyond

Date	SPEC. No.
June, 2024	C-YFF-i

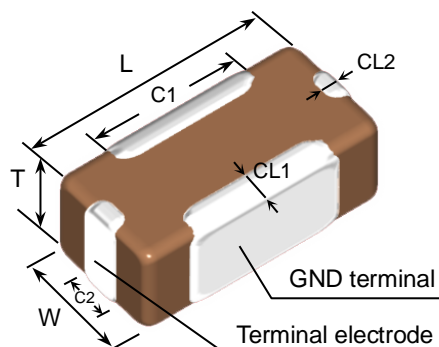
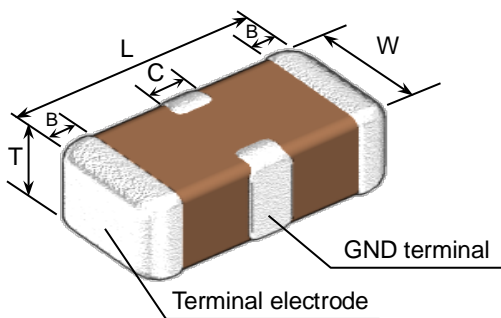
1. CODE CONSTRUCTION

(Example)	YFF15	PC	0G	105	M	T	0000
	<u>YFF18</u>	<u>PC</u>	<u>1C</u>	<u>104</u>	<u>M</u>	<u>T</u>	<u>0000</u>
	(1)	(2)	(3)	(4)	(5)	(6)	(7)

(1) Case size

<YFF15,YFF18PC/PH/SC,YFF21,YFF31>

<YFF18PW>



Type	Dimensions (Unit : mm)				
	L	W	T	B	C
YFF15	1.00±0.05	0.55±0.05	0.30±0.05	0.09 min.	0.30±0.10
	1.05±0.05	0.65±0.05	0.45±0.05		
	1.00±0.20	0.50±0.20	0.40±0.10	0.18±0.10	
YFF18SC	1.60±0.10	0.80±0.10	0.60±0.10	0.25±0.15	0.40±0.10
YFF18PC	1.60±0.20	0.80±0.10	0.60±0.10		
YFF18PH	1.60±0.20	0.80±0.10	0.80±0.10		
	1.60 ^{+0.30} _{-0.10}	0.80 ^{+0.30} _{-0.10}	0.80 ^{+0.30} _{-0.10}	0.25±0.20	0.40±0.20
YFF21	2.00±0.20	1.25±0.20	0.85±0.15	0.30±0.10	0.45±0.15
				*10.30±0.20	*10.50 ^{+0.30} _{-0.20}
YFF31PC	3.20±0.20	1.60±0.20	1.30±0.20	0.40±0.30	0.95±0.25
YFF31HC	3.20±0.20	1.60±0.20	1.30±0.20	0.40±0.30	*21.10±0.40

Type	Dimensions (Unit : mm)				
	L	W	T	C1	CL1
YFF18PW	1.60±0.10	0.80±0.10	0.60±0.10	0.80 ^{+0.20} _{-0.10}	0.15±0.10
	C2	CL2			
	0.40±0.10	0.15±0.10			

* As for each item, please refer to detail page on TDK web.

*1 : Applied to YFF21PC1A475M

*2 : Applied to YFF31PC0J226M

(2) Product Classification

Symbol	Product Classification
P C	for Power line
P H	for Power line (thickness:0.8mm)
P W	for Power line (Low ESL type)
S C	for Signal line
H C	for Large-current power line

(3) Rated Voltage

Symbol	Rated Voltage
2 A	DC 100 V
1 H	DC 50 V
1 V	DC 35 V
1 E	DC 25 V
1 C	DC 16 V
1 A	DC 10 V
0 J	DC 6.3 V
0 G	DC 4 V

(4) Rated Capacitance

Stated in three digits and in units of pico farads (pF).
The first and Second digits identify the first and second significant figures of the capacitance, the third digit identifies the multiplier.

(Example)

Symbol	Rated Capacitance
104	100,000 pF
105	1,000,000 pF

(5) Capacitance tolerance

Symbol	Tolerance
M	$\pm 20 \%$

(6) Packaging

Symbol	Packaging
T	Taping

(7) TDK internal code

2. RATED CURRENT

Rated current depend on operating temperature.
As for details, please refer to detail page on TDK web.

3. OPERATING TEMPERATURE RANGE

Min. operating Temperature	Max. operating Temperature	Reference Temperature
-55°C	85°C	25°C
-55°C	105°C	25°C
-55°C	125°C	25°C

4. STORING CONDITION AND TERM

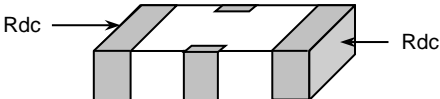
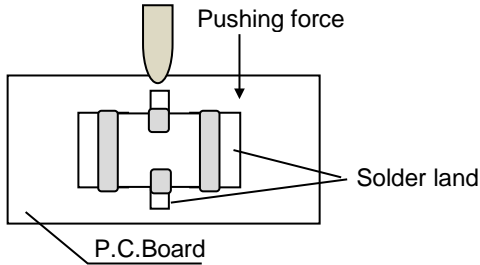
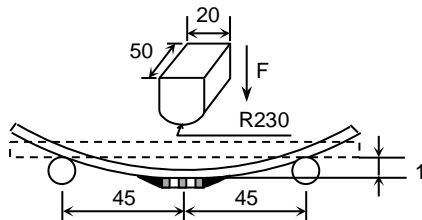
Storing temperature	Storing humidity	Storing term
5~40°C	20~70%RH	Within 6 months upon receipt.

5. INDUSTRIAL WASTE DISPOSAL

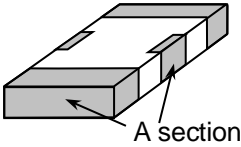
Dispose this product as industrial waste in accordance with the industrial Waste Law.

6. PERFORMANCE

table 1

No.	Item		Performance	Test or inspection method
1	External Appearance		No defects which may affect performance.	Inspect with magnifying glass (3×).
2	Insulation Resistance		10,000MΩ or 500MΩ·μF min. (As for the products of rated voltage 16V DC and lower, 100MΩ·μF min.), whichever smaller.	Measuring voltage : Rated voltage Voltage application time : 60s.
3	Direct Current Resistance (Rdc)		Please refer to detail page on TDK web.	Measuring current shall be 100mA max. 
4	Voltage Proof		Withstand test voltage without insulation breakdown or other damage.	Apply voltage : 2.5 × rated voltage Voltage application time : 1s. Charge / discharge current : 50mA or lower
5	Capacitance		Within the specified tolerance.	As for measuring condition, please contact with our sales representative. YFF15PC0G435M : Heat treat the products at 150 0,-10°C for 1h and measure the value after leaving products for 250±4h in ambient condition.
6	Robustness of Terminations		No sign of termination coming off, breakage of ceramic, or other abnormal signs.	Reflow solder the products on a P.C.Board shown in Appendix2. Apply a pushing force gradually to a specimen as shown in the following figure. pushing force : 5N. (2N is applied for YFF15 type.) 
7	Bending	External appearance	No mechanical damage.	Reflow solder the products on a P.C.Board shown in Appendix1 and bend it for 1mm.  (Unit : mm)
		Capacitance	Change from the value before test ± 12.5 %	
		Direct current Resistance (Rdc)	Please contact with our sales representative.	

(continued)

No.	Item		Performance	Test or inspection method
8	Solderability		<p>New solder to cover over 75% of termination. 25% may have pin holes or rough spots but not concentrated in one spot. Ceramic surface of A sections shall not be exposed due to melting or shifting of termination material.</p> 	<p>Solder : Sn-3.0Ag-0.5Cu</p> <p>Flux : Isopropyl alcohol (JIS K 8839) Rosin (JIS K 5902) 25% solid solution.</p> <p>Solder temp. : 245±5°C</p> <p>Dwell time : 3±0.3s.</p> <p>Solder position : Until both terminations are completely soaked.</p>
9	Resistance to solder heat	External appearance	No cracks are allowed and terminations shall be covered at least 60% with new solder.	<p>Solder : Sn-3.0Ag-0.5Cu</p> <p>Flux : Isopropyl alcohol (JIS K 8839) Rosin (JIS K 5902) 25% solid solution.</p> <p>Solder temp. : 260±5°C</p> <p>Dwell time : 10±1s.</p> <p>Solder position : Until both terminations are completely soaked.</p> <p>Pre-heating : Temp. — 110~140°C Time — 30~60s. Leave the products in ambient condition for 24±2h before measurement.</p>
		Capacitance	<p>Change from the value before test</p> <p>± 7.5 %</p>	
		Direct current resistance (Rdc)	Please contact with our sales representative.	
10	Vibration	External appearance	No mechanical damage.	<p>Frequency : 10~55~10Hz</p> <p>Reciprocating sweep time : 1 min.</p> <p>Amplitude : 1.5mm</p> <p>Repeat this for 2h each in 3 perpendicular directions(Total 6h).</p> <p>Reflow solder the products on a P.C.Board shown in Appendix 2 before testing.</p>
		Capacitance	<p>Change from the value before test</p> <p>± 7.5 %</p>	
		Direct current resistance (Rdc)	Please contact with our sales representative.	

(continued)

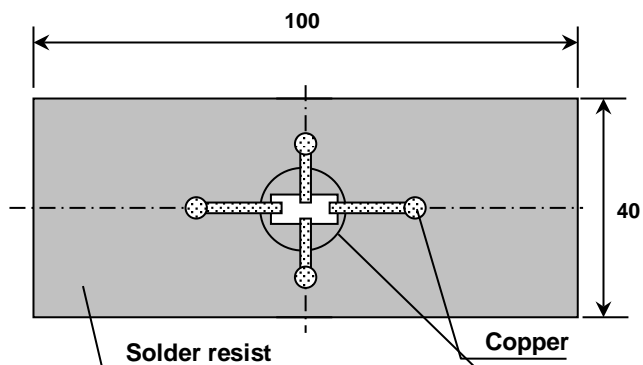
No.	Item		Performance	Test or inspection method																	
11	Temperature cycle	External appearance	No mechanical damage.	<div>Expose the products in the condition step1 through step 4 listed in the following table.</div> <div>Temp. cycle : 5 cycles</div> <table><tr><th>Step</th><th>Temperature (°C)</th><th>Time(min.)</th></tr><tr><td>1</td><td>Min. operating temp. ± 3</td><td>30 ± 3</td></tr><tr><td>2</td><td>Ambient Temp.</td><td>2 ~ 5</td></tr><tr><td>3</td><td>Max. operating temp. ± 2</td><td>30 ± 2</td></tr><tr><td>4</td><td>Ambient Temp.</td><td>2 ~ 5</td></tr></table> <div>As for Min./Max. operating temp., please contact with our sales representative.</div> <div>Leave the products in ambient condition for 24±2h before measurement.</div> <div>Reflow solder the products on a P.C.Board shown in Appendix2 before testing.</div>			Step	Temperature (°C)	Time(min.)	1	Min. operating temp. ± 3	30 ± 3	2	Ambient Temp.	2 ~ 5	3	Max. operating temp. ± 2	30 ± 2	4	Ambient Temp.	2 ~ 5
		Step	Temperature (°C)				Time(min.)														
		1	Min. operating temp. ± 3				30 ± 3														
		2	Ambient Temp.				2 ~ 5														
		3	Max. operating temp. ± 2				30 ± 2														
4	Ambient Temp.	2 ~ 5																			
Capacitance	<div>Change from the value before test</div> <div>Please contact with our sales representative.</div>																				
Resistance for DC (Rdc)	Please contact with our sales representative.																				
Insulation Resistance	Meet the initial spec.																				
12	Moisture Resistance (Steady State)	External appearance	No mechanical damage.	<div>Test temp. : 40±2°C</div> <div>Test humidity : 90 to 95%RH</div> <div>Test time : 500 +24,0h.</div> <div>Leave the products in ambient condition for 24±2h before measurement.</div> <div>Reflow solder the products on a P.C.Board shown in Appendix2 before testing.</div>																	
		Capacitance	<div>Change from the value before test</div> <div>Please contact with our sales representative.</div>																		
		Resistance for DC (Rdc)	Please contact with our sales representative.																		
		Insulation Resistance	1,000MΩ or 50MΩ·μF min. (As for the products of rated voltage 16V DC and lower, 10MΩ·μF min.), whichever smaller.																		

(continued)

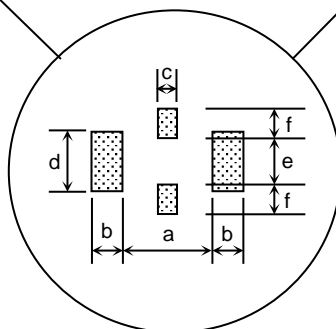
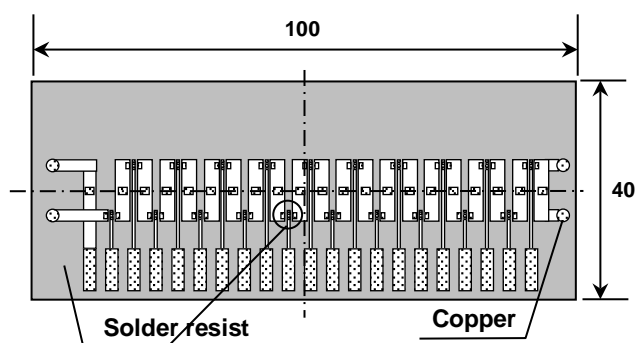
No.	Item		Performance	Test or inspection method
13	Moisture Resistance	External appearance	No mechanical damage.	Test temp. : $40\pm 2^{\circ}\text{C}$ Test humidity : 90~95%RH Applied voltage : Rated voltage Test time : 500 +24,0h Charge/discharge current : 50mA or lower Leave the products in ambient condition for $24\pm 2\text{h}$ before measurement. Reflow solder the products on a P.C.Board shown in Appendix2 before testing. Initial value setting Voltage conditioning 《After voltage treat the products under testing temperature and voltage for 1 hour,》 leave the products in ambient condition for $24\pm 2\text{h}$ before measurement. Use this measurement for initial value.
		Capacitance	Change from the value before test Please contact with our sales representative.	
		Direct current resistance (Rdc)	Please contact with our sales representative.	
		Insulation Resistance	500M Ω or 25M $\Omega\cdot\mu\text{F}$ min. (As for the products of rated voltage 16V DC and lower, 5M $\Omega\cdot\mu\text{F}$ min.), whichever smaller.	
14	Life	External appearance	No mechanical damage.	Test temp. : Please contact with our sales representative. Applied voltage : Please contact with our sales representative. Test time : 1,000 +48,0h Charge/discharge current : 50mA or lower Leave the products in ambient condition for $24\pm 2\text{h}$ before measurement. Reflow solder the products on a P.C.Board shown in Appendix2 before testing. Initial value setting Voltage conditioning 《After voltage treat the products under testing temperature and voltage for 1 hour,》 leave the products in ambient condition for $24\pm 2\text{h}$ before measurement. Use this measurement for initial value.
		Capacitance	Change from the value before test Please contact with our sales representative.	
		Resistance for DC (Rdc)	Please contact with our sales representative.	
		Insulation Resistance	1,000M Ω or 50M $\Omega\cdot\mu\text{F}$ min. (As for the products of rated voltage 16V DC and lower, 10M $\Omega\cdot\mu\text{F}$ min.), whichever smaller.	

*As for the initial measurement of product on number 7, 9, 10, 11 and 12, leave product at $150\pm 10^{\circ}\text{C}$ for 1h and measure the value after leaving product for $24\pm 2\text{h}$ in ambient condition.

Appendix 1

P.C.Board for bending test

Appendix 2

P.C.Board for reliability test

(Unit : mm)

Type \ Symbol	a	b	c	d	e	f
YFF15	0.70	0.30	0.19	0.60	0.25	0.25
YFF18PC/PH/SC	1.00	0.60	0.40	0.60	0.40	0.40
YFF18PW	1.20	0.40	0.80	0.40	0.40	0.40
YFF21	1.40	0.60	0.50	0.80	0.60	0.65
YFF31	2.50	1.20	1.40	1.30	0.80	0.90

1. Material : Glass Epoxy (As per JIS C6484 GE4)

2. Thickness : Appendix 1 — 0.8mm (YFF15)

— 1.0mm (YFF18,YFF21,YFF31)

: Appendix 2 — 1.6mm



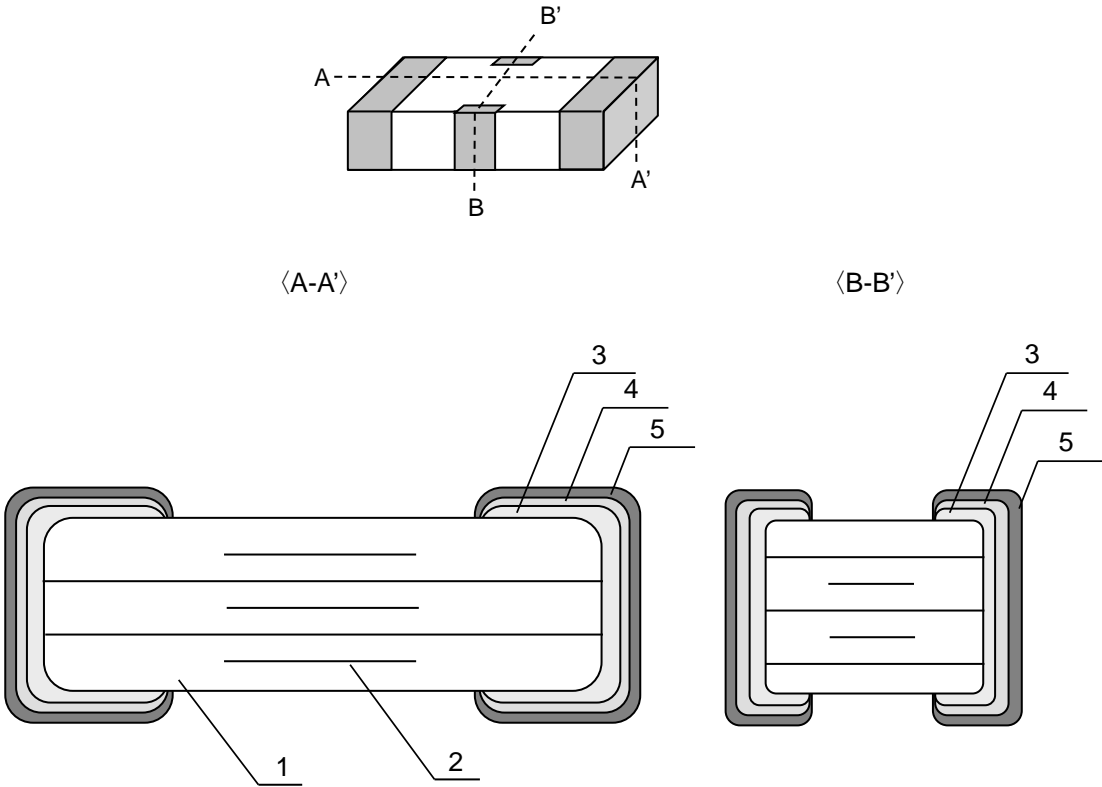
Copper Thickness: Appendix 1 — 0.035mm

Appendix 2 — 0.070mm



Solder resist

7. INSIDE STRUCTURE AND MATERIAL



No.	NAME	MATERIAL
1	Dielectric	CaZrO ₃ or BaTiO ₃
2	Electrode	Nickel (Ni)
3	Termination	Copper (Cu)
4		Nickel (Ni)
5		Tin (Sn)

8. PACKAGING

Packaging shall be done to protect the components from the damage during transportation and storing, and a label which has the following information shall be attached.
Tape packaging is as per 12. TAPE PACKAGING SPECIFICATION.

- 1) Inspection No.*
- 2) TDK P/N
- 3) Customer's P/N
- 4) Quantity

*Composition of Inspection No.

Example F 4 A - 23 - 001
 (a) (b) (c) (d) (e)

- Line code
- Last digit of the year
- Month and A for January and B for February and so on. (Skip I)
- Inspection Date of the month.
- Serial No. of the day

*Composition of new Inspection No.

(Implemented on and after May 1, 2019 in sequence)

Example

I	F	4	E	2	3	A	0	0	1
(a)	(b)	(c)	(d)	(e)		(f)		(g)	

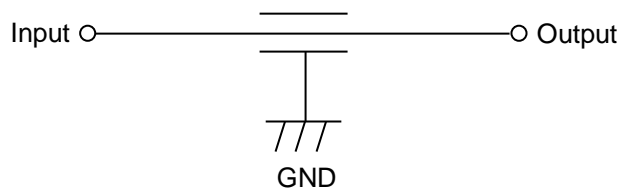
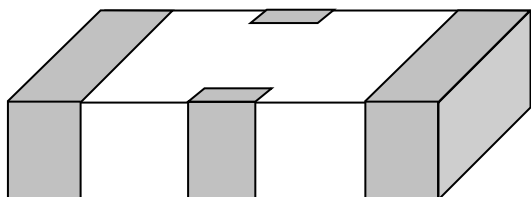
- (a) Prefix
- (b) Line code
- (c) Last digit of the year
- (d) Month and A for January and B for February and so on. (Skip I)
- (e) Inspection Date of the month.
- (f) Serial No. of the day(00 ~ ZZ)
- (g) Suffix(00 ~ ZZ)

*It was shifted to the new inspection No. on and after May 2019, but the implementation timing may be different depending on shipment bases.
Until the shift is completed, either current or new composition of inspection No. will be applied.


9. SOLDERING CONDITION

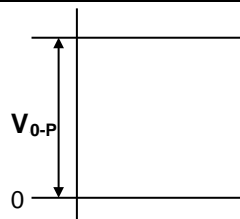
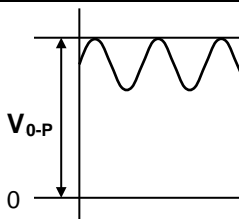
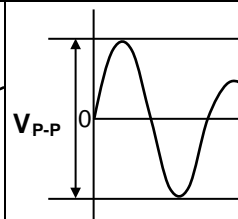
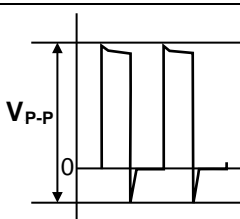
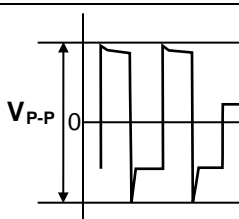
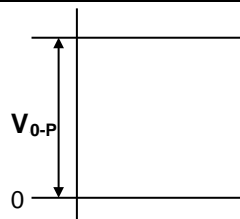
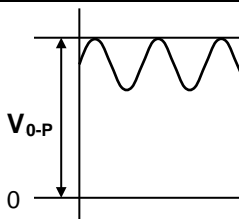
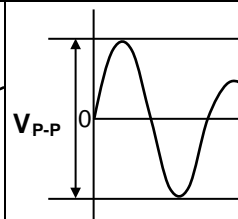
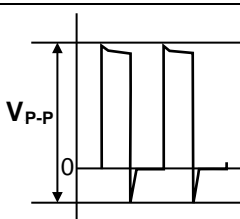
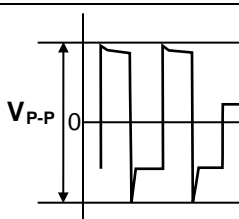
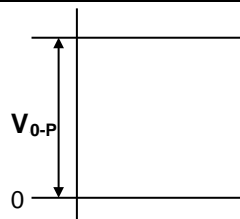
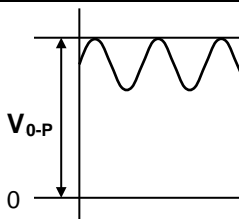
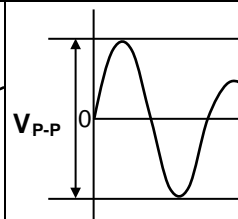
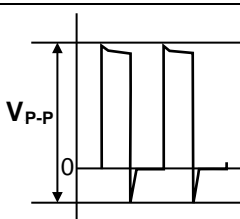
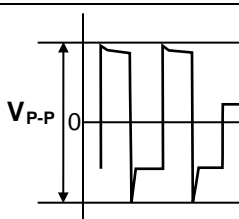
Soldering is limited to Reflow soldering.

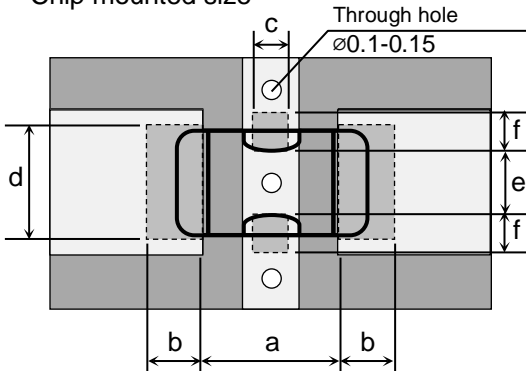
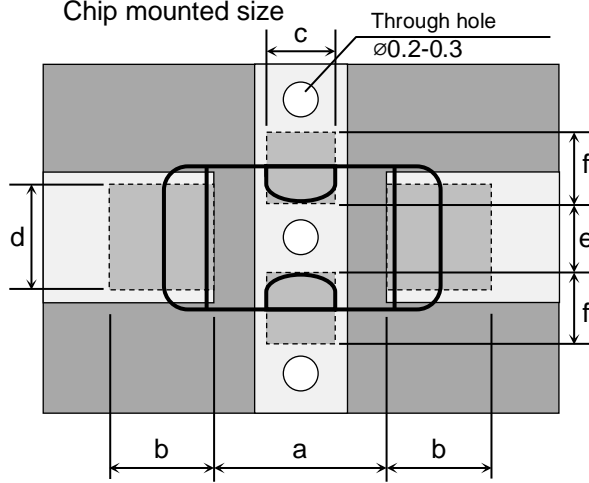
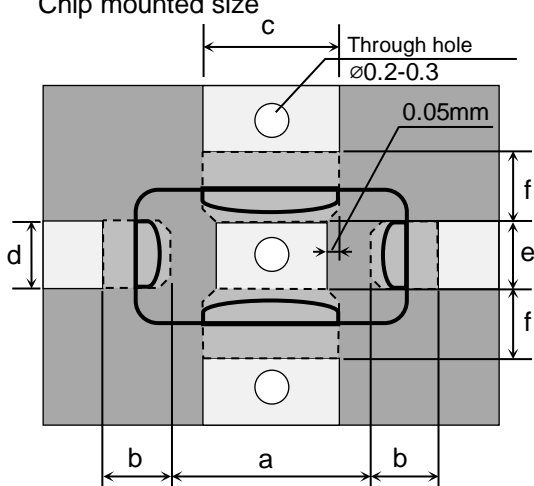
10. EQUIVALENT CIRCUIT DIAGRAM

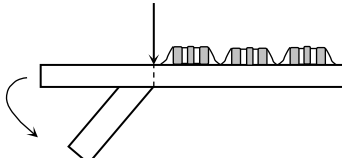
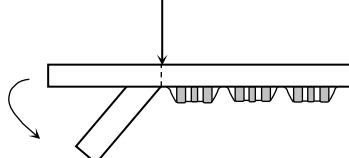
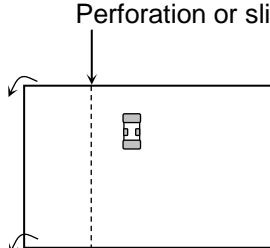
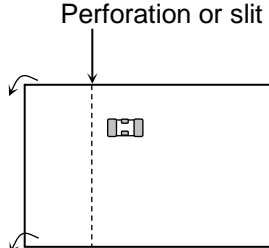
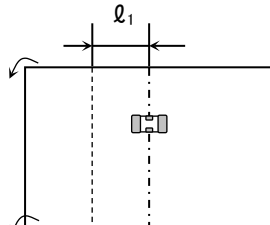
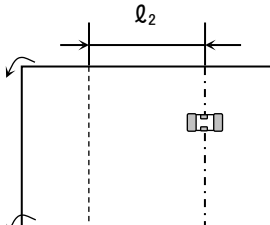


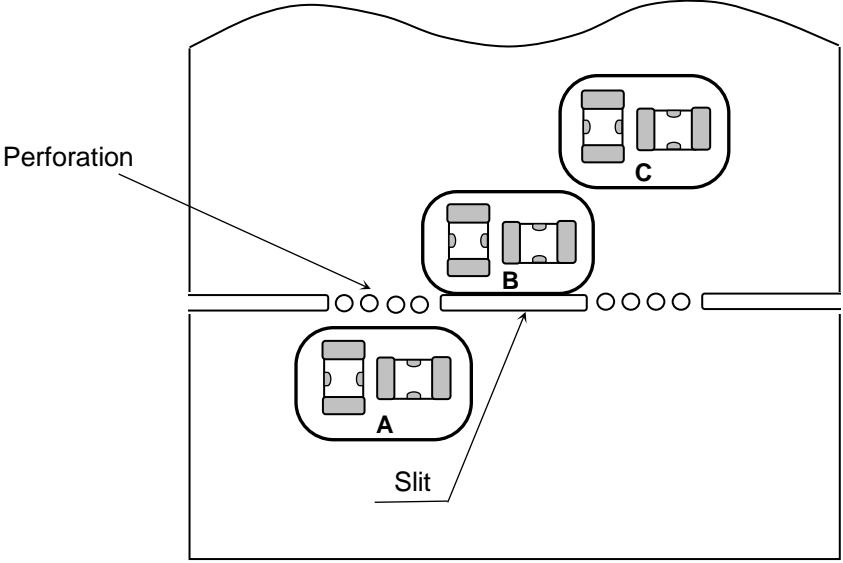
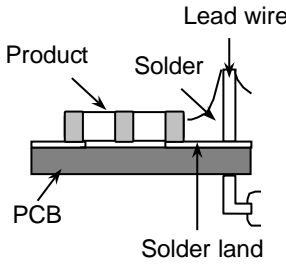
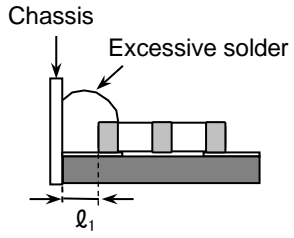
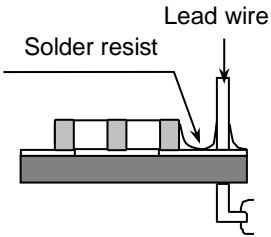
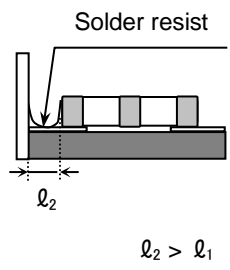
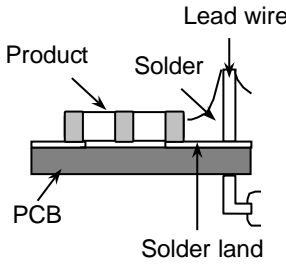
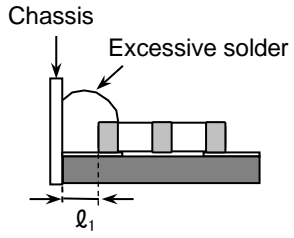
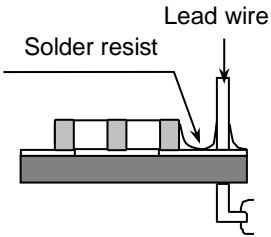
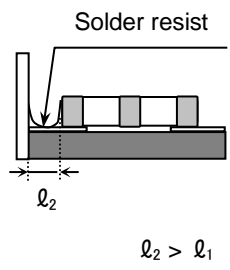
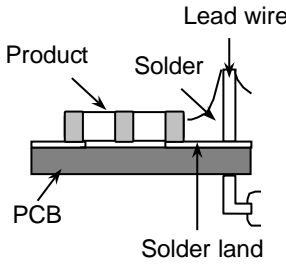
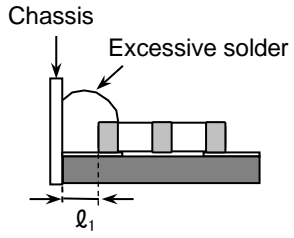
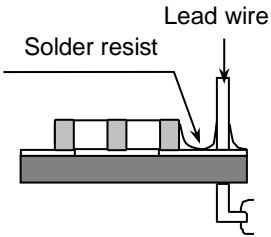
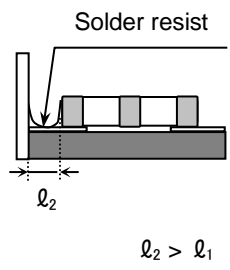
11. CAUTION

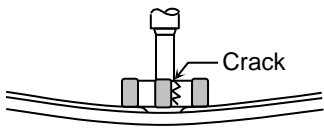
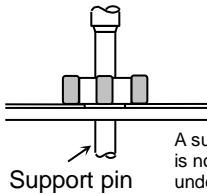
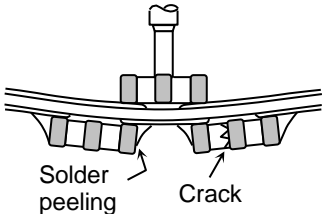
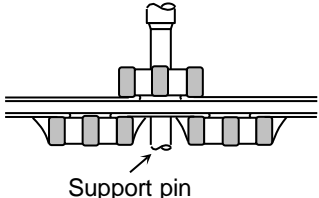
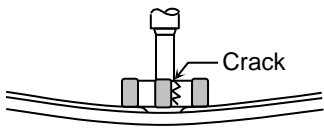
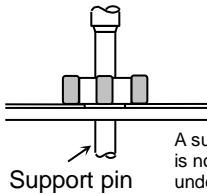
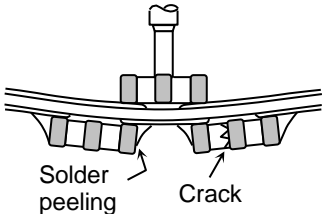
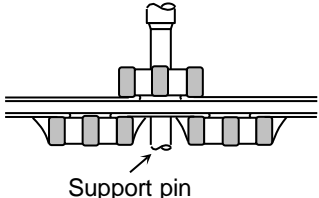
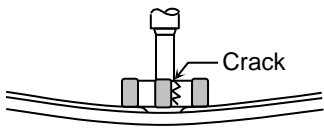
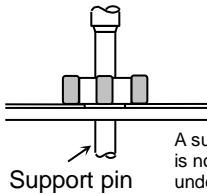
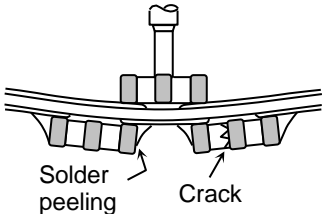
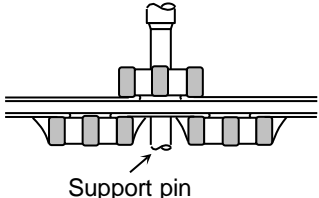
No.	Process	Condition
1	Operating Condition (Storage, Use, Transportation)	<p>1-1. Storage, Use The products must be stored in an ambient temperature of 5 to 40°C with a relative humidity of 20 to 70%RH. JIS C 60721-3-1 Class 1K2 should be followed for the other climatic conditions.</p> <ol style="list-style-type: none"> 1) High temperature and humidity environment may affect a product's solder ability because it accelerates terminal oxidization. They also deteriorate performance of taping and packaging. Therefore, SMD products shall be used within 6 months. For products with terminal electrodes consisting of silver or silver-palladium which tend to become oxidized or sulfurized, use as soon as possible, such as within one month after opening the bag. 2) When products are stored for a longer time period than 6 months, confirm the solderability of the products prior to use. During storage, keep the minimum packaging unit in its original packaging without opening it. Do not deviate from the above temperature and humidity conditions even for a short term. 3) Corrosive gasses in the air or atmosphere may result in deterioration of the reliability, such as poor solderability of the terminal electrodes. Do not store products where they will be exposed to corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine ammonia etc.) 4) Solderability and electrical performance may deteriorate due to photochemical change in the terminal electrode if stored in direct sunlight, or due to condensation from rapid changes in humidity. The products especially which use resin material must be operated and stored in an environment free of dew condensation, as moisture absorption due to condensation may affect the performance. 5) Refer to JIS C 60721-3-1, class 1K2 for other climate conditions. <p>1-2. Handling in transportation In case of the transportation of the product, the performance of the product may be deteriorated depending on the transportation condition. (Refer to JEITA RCR-2335C 9.2 Handling in transportation)</p>
2	Circuit design  Caution	<p>2-1. Operating temperature</p> <ol style="list-style-type: none"> 1) Upper category temperature (maximum operating temperature) is specified. It is necessary to select a product whose rated temperature is higher than the operating temperature. Also, it is necessary to consider the temperature distribution in the equipment and seasonal temperature variation. 2) Surface temperature including self heating should be below maximum operating temperature. Due to dielectric loss, products will heat itself when AC is applied due to ESR. Especially at high frequencies, please be careful that the heat might be so extreme. Also, even if the surface temperature of the product includes self-heating and is the maximum operating temperature or lower, excessive heating of the product due to self-heating may cause deterioration of the characteristics and reliability of the product. The self-heating temperature rise of the product changes depending on the difference in heat radiation due to the mounting method to the device, the ambient temperature, the cooling method of the device and circuit board material and the design, etc. The load should be contained so that the self-heating temperature rise of the product body in a natural convection environment at an ambient temperature of 25°C remain below 20°C. When using in a high-frequency circuit or a circuit in which a product generates heat, such as when a high-frequency ripple current flows, pay attention to the above precautions. (Note that accurate measurement may not be possible with self-heating measurement when the equipment applies cooling other than natural convection such as a cooling fan.) 3) The electrical characteristics of the product will vary depending on the temperature. The product should be selected and designed in taking the temperature into consideration.

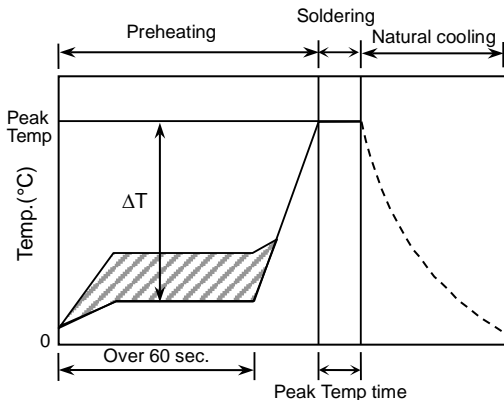
No.	Process	Condition														
2	Circuit design ⚠ Caution	<p>2-2. When overvoltage is applied Applying overvoltage to a product may cause dielectric breakdown and result in a short circuit. The duration until dielectric breakdown depends on the applied voltage and the ambient temperature.</p> <p>2-3. Operating voltage 1) Operating voltage across the terminals should be below the rated voltage. When AC and DC are super imposed, V_{0-P} must be below the rated voltage. — (1) and (2) AC or pulse with overshooting, V_{P-P} must be below the rated voltage. — (3), (4) and (5) When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use the product within rated voltage containing these Irregular voltage.</p> <table><tr><th>Voltage</th><th>(1) DC voltage</th><th>(2) DC+AC voltage</th><th>(3) AC voltage</th></tr><tr><td>Positional Measurement (Rated voltage)</td><td></td><td></td><td></td></tr><tr><th>Voltage</th><th>(4) Pulse voltage (A)</th><th>(5) Pulse voltage (B)</th></tr><tr><td>Positional Measurement (Rated voltage)</td><td></td><td></td></tr></table> <p>2) Even below the rated voltage, if repetitive high frequency AC or pulse is applied, the reliability of the products may be reduced.</p> <p>3) The effective capacitance will vary depending on applied DC and AC voltages. The products should be selected and designed in taking the voltages into consideration.</p> <p>4) Abnormal voltage (surge voltage, static electricity, pulse voltage, etc.) shall not exceed the rated voltage.</p> <p>5) When products are used in a series connection, it is necessary to add a balancing circuit such as voltage dividing resistors in order to avoid an imbalance in the voltage applied to each product.</p> <p>2-4. Frequency When the products are used in AC and/or pulse voltages, the products may vibrate themselves and generate audible sound.</p> <p>2-5. Derating current This product allows DC current to flow inside. Do not use this product above the rated DC current.</p>	Voltage	(1) DC voltage	(2) DC+AC voltage	(3) AC voltage	Positional Measurement (Rated voltage)				Voltage	(4) Pulse voltage (A)	(5) Pulse voltage (B)	Positional Measurement (Rated voltage)		
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3	Designing P.C.board	<p>The amount of solder at the terminations has a direct effect on the reliability of the products.</p> <p>1) The greater the amount of solder, the higher the stress on the products, and the more likely that it will break. When designing a P.C.board, determine the shape and size of the solder lands to have proper amount of solder on the terminations.</p> <p>2) Avoid using common solder land for multiple terminations and provide individual solder land for each terminations.</p>														

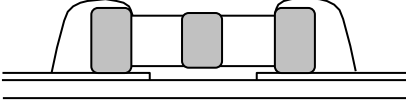
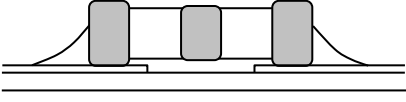
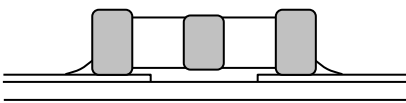
No.	Process	Condition																																										
3	Designing P.C.board	<div>3) Size and recommended land dimensions. <YFF15> Chip mounted size</div> <div></div> <div><YFF18PC/SC/PH, YFF21, YFF31> Chip mounted size</div> <div></div> <div><YFF18PW> Chip mounted size</div> <div></div> <div><div>Resist</div><div>Land pattern</div><div>Land pattern & Resist</div></div> <div><p>YFF Series is having the unique Ground structure and eliminate unnecessary noise at the wide frequency range. The following points should be considered at the pad design to obtain the better performance.</p><ol style="list-style-type: none">1) Ground pattern should be designed as big as possible.2) Make through hole and connect to the ground pattern of the chip mounted side.3) Through hole should be designed as close to GND terminal as possible.4) Connect all the input/output and GND terminals to each land patterns.<p>Notes) *If through hole is too big, solder paste way came into the hole and make bad connection with the ground pattern.</p></div> <div>(Unit : mm)</div> <table><tr><th>Type \ Symbol</th><th>a</th><th>b</th><th>c</th><th>d</th><th>e</th><th>f</th></tr><tr><td>YFF15</td><td>0.70</td><td>0.30</td><td>0.19</td><td>0.60</td><td>0.25</td><td>0.25</td></tr><tr><td>YFF18PC/SC/PH</td><td>1.00</td><td>0.60</td><td>0.40</td><td>0.60</td><td>0.40</td><td>0.40</td></tr><tr><td>YFF18PW</td><td>1.20</td><td>0.40</td><td>0.80</td><td>0.40</td><td>0.40</td><td>0.40</td></tr><tr><td>YFF21</td><td>1.40</td><td>0.60</td><td>0.50</td><td>0.80</td><td>0.60</td><td>0.65</td></tr><tr><td>YFF31</td><td>2.50</td><td>1.20</td><td>1.40</td><td>1.30</td><td>0.80</td><td>0.90</td></tr></table>	Type \ Symbol	a	b	c	d	e	f	YFF15	0.70	0.30	0.19	0.60	0.25	0.25	YFF18PC/SC/PH	1.00	0.60	0.40	0.60	0.40	0.40	YFF18PW	1.20	0.40	0.80	0.40	0.40	0.40	YFF21	1.40	0.60	0.50	0.80	0.60	0.65	YFF31	2.50	1.20	1.40	1.30	0.80	0.90
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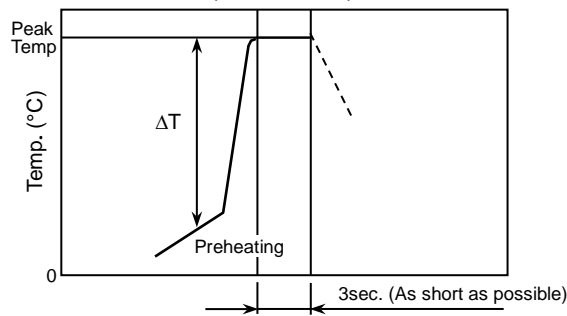
No.	Process	Condition		
3	Designing P.C.board	4) Recommended product layout is as following.		
			Disadvantage against bending stress	Advantage against bending stress
		Mounting face	<p>Perforation or slit</p>  <p>Break P.C.board with mounted side up.</p>	<p>Perforation or slit</p>  <p>Break P.C.board with mounted side down.</p>
		Chip arrangement (Direction)	<p>Mount in parallel with perforation or slit</p> <p>Perforation or slit</p> 	<p>Mount perpendicularly to perforation or slit</p> <p>Perforation or slit</p> 
		Distance from slit	<p>Closer to slit is higher stress</p>  <p>($l_1 < l_2$)</p>	<p>Away from slit is less stress</p>  <p>($l_1 < l_2$)</p>

No.	Process	Condition									
3	Designing P.C.board	<p>5) Mechanical stress varies according to location of product on the P.C.board.</p>  <p>Stress force for location of product $A > B$ $A > C$</p> <p>When dividing printed wiring boards, the intensities of mechanical stress applied to products are different according to each dividing method in the order of : Push-back < Slit < V-groove < Perforation. Therefore consider not only position of products, but also the way of the dividing the printed wiring boards.</p> <p>6) Layout recommendation</p> <table border="1"> <thead> <tr> <th>Example</th><th>Use of common solder land</th><th>Soldering with chassis</th></tr> </thead> <tbody> <tr> <td>Need to avoid</td><td>  </td><td>  </td></tr> <tr> <td>Recommendation</td><td>  </td><td>  <p>$l_2 > l_1$</p> </td></tr> </tbody> </table>	Example	Use of common solder land	Soldering with chassis	Need to avoid			Recommendation		 <p>$l_2 > l_1$</p>
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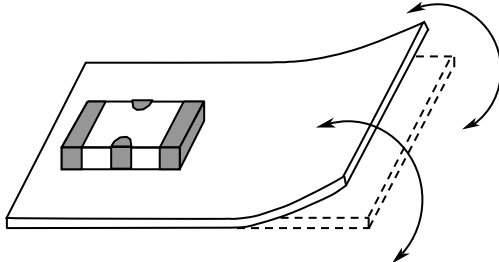
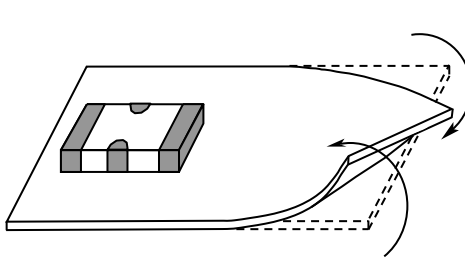
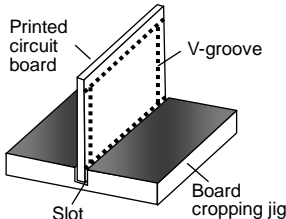
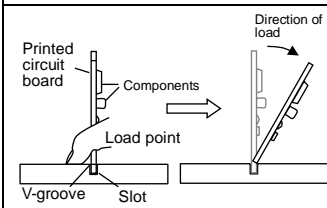
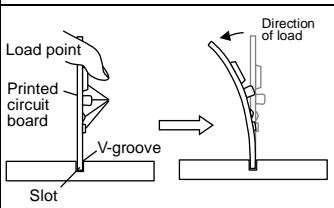
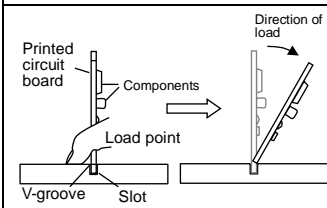
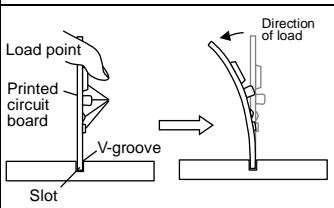
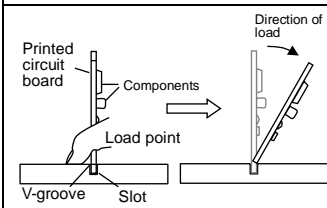
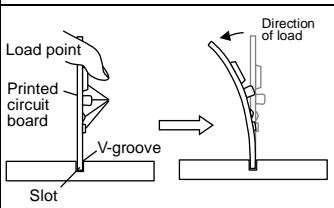
No.	Process	Condition									
4	Mounting	<p>4-1. Stress from mounting head</p> <p>If the mounting head is adjusted too low, it may induce excessive stress in the product to result in cracking. Please take following precautions.</p> <ol style="list-style-type: none"> 1) Adjust the bottom dead center of the mounting head to reach on the P.C.board surface and not press it. 2) Adjust the mounting head pressure to be 1 to 3N of static weight. 3) To minimize the impact energy from mounting head, it is important to provide support from the bottom side of the P.C.board. <p>See following examples.</p> <table border="1"> <thead> <tr> <th></th><th>Not recommended</th><th>Recommended</th></tr> </thead> <tbody> <tr> <td>Single-sided mounting</td><td>  <p>Crack</p> </td><td>  <p>Support pin</p> <p>A support pin is not to be underneath the product.</p> </td></tr> <tr> <td>Double-sides mounting</td><td>  <p>Solder peeling</p> <p>Crack</p> </td><td>  <p>Support pin</p> </td></tr> </tbody> </table> <p>When the centering jaw is worn out, it may give mechanical impact on the product to cause crack. Please control the close up dimension of the centering jaw and provide sufficient preventive maintenance and replacement of it.</p>		Not recommended	Recommended	Single-sided mounting	 <p>Crack</p>	 <p>Support pin</p> <p>A support pin is not to be underneath the product.</p>	Double-sides mounting	 <p>Solder peeling</p> <p>Crack</p>	 <p>Support pin</p>
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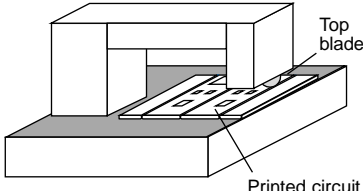
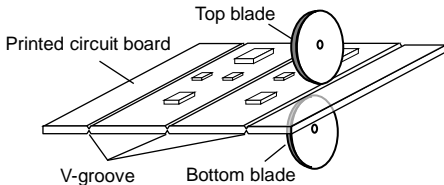
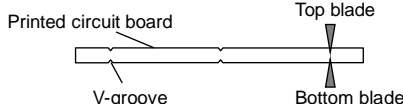
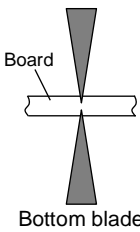
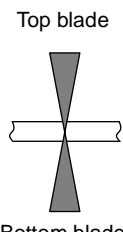
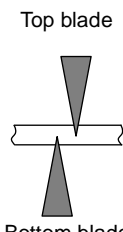
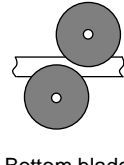
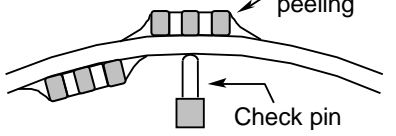
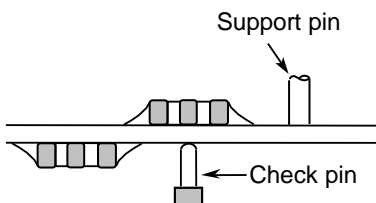
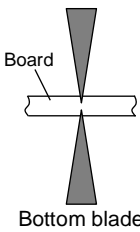
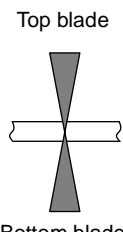
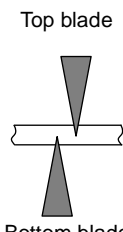
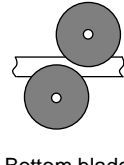
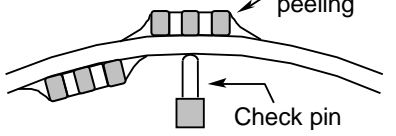
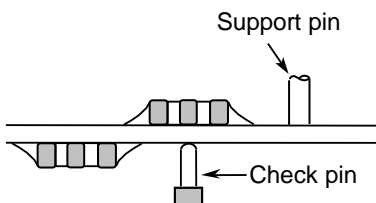
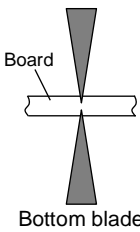
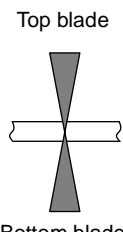
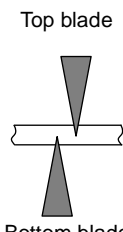
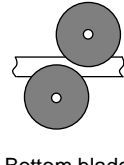
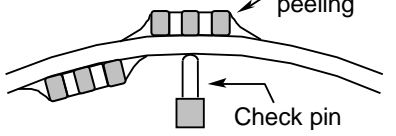
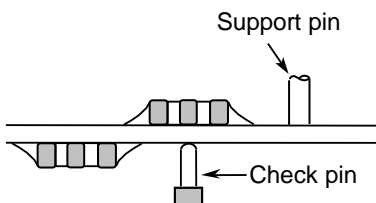
No.	Process	Condition															
5	Soldering	<div>5-1. Flux selection</div> <div>Flux can seriously affect the performance of products. Confirm the following to select the appropriate flux.</div> <div><div>1) It is recommended to use a mildly activated rosin flux (less than 0.1wt% chlorine). Strong flux is not recommended.</div><div>2) Excessive flux must be avoided. Please provide proper amount of flux.</div><div>3) When water-soluble flux is used, enough washing is necessary.</div></div> <div>5-2. Recommended soldering profile : Reflow method</div> <div>Refer to the following temperature profile at Reflow soldering.</div> <div><div>Reflow soldering</div></div> <div>5-3. Recommended soldering peak temp and peak temp duration for Reflow soldering</div> <div>Pb free solder is recommended, but if Sn-37Pb must be used, refer to below.</div> <table><tr><th rowspan="2">Temp./Duration Solder</th><th colspan="2">Reflow soldering</th></tr><tr><th>Peak temp(°C)</th><th>Duration(sec.)</th></tr><tr><td>Lead Free Solder</td><td>260 max.</td><td>10 max.</td></tr><tr><td>Sn-Pb Solder</td><td>230 max.</td><td>20 max.</td></tr></table> <div>Recommended solder compositions</div> <div>Lead Free Solder : Sn-3.0Ag-0.5Cu</div> <div>5-4. Avoiding thermal shock</div> <div><div>1) Preheating condition</div><table><tr><th>Soldering</th><th>Temp. (°C)</th></tr><tr><td>Reflow soldering</td><td>ΔT ≤ 150</td></tr></table></div> <div><div>2) Cooling condition</div><div>Natural cooling using air is recommended. If the product is dipped into a solvent for cleaning, the temperature difference (ΔT) must be less than 100°C.</div></div>	Temp./Duration Solder	Reflow soldering		Peak temp(°C)	Duration(sec.)	Lead Free Solder	260 max.	10 max.	Sn-Pb Solder	230 max.	20 max.	Soldering	Temp. (°C)	Reflow soldering	ΔT ≤ 150
Temp./Duration Solder	Reflow soldering																
	Peak temp(°C)	Duration(sec.)															
Lead Free Solder	260 max.	10 max.															
Sn-Pb Solder	230 max.	20 max.															
Soldering	Temp. (°C)																
Reflow soldering	ΔT ≤ 150																

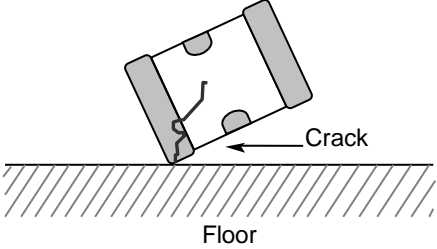
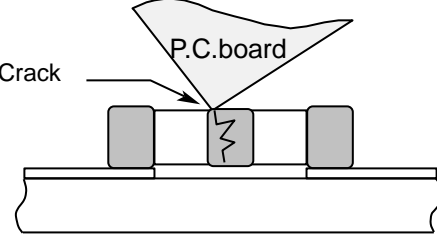
No.	Process	Condition
5	Soldering	<p>5-5. Amount of solder</p> <p>Excessive solder will induce higher tensile force in product when temperature changes and it may result in chip cracking. In sufficient solder may detach the product from the P.C.board.</p>
		<p>Excessive solder</p>  <p>Higher tensile force in product to cause crack</p>
		<p>Adequate</p> 
		<p>Insufficient solder</p>  <p>Low robustness may cause contact failure or product come off the P.C.board.</p>
		<p>5-6. Sn-Zn solder</p> <p>Sn-Zn solder affects product reliability. Please contact TDK in advance when utilize Sn-Zn solder.</p> <p>5-7. Countermeasure for tombstone</p> <p>The misalignment between the mounted positions of the products and the land patterns should be minimized. The tombstone phenomenon may occur especially the products are mounted (in longitudinal direction) in the same direction of the reflow soldering. (Refer to JEITA RCR-2335C Annex A (Informative) Recommendations to prevent the tombstone phenomenon)</p>


No.	Process	Condition												
6	Solder repairing	<p>Solder repairing is unavoidable, refer to below.</p> <p>6-1. Solder repair by solder iron</p> <p>1) Selection of the soldering iron tip</p> <p>Tip temperature of solder iron varies by its type, P.C.board material and solder land size. The higher the tip temperature, the quicker the operation. However, heat shock may cause a crack in the product. Please make sure the tip temp. before soldering and keep the peak temp and time in accordance with following recommended condition.</p> <div><p>Manual soldering (Solder iron)</p></div> <p>Recommended solder iron condition (Sn-Pb Solder and Lead Free Solder)</p> <table><tr><th>Temp. (°C)</th><th>Duration (sec.)</th><th>Wattage (W)</th><th>Shape (mm)</th></tr><tr><td>300 max.</td><td>3 max.</td><td>20 max.</td><td>∅ 3.0 max.</td></tr></table> <p>* Please preheat the chip products with the condition in 6-2 to avoid the thermal shock.</p> <p>2) Direct contact of the soldering iron with ceramic dielectric of products may cause crack. Do not touch the ceramic dielectric and the terminations by solder iron.</p> <p>3) It is not recommended to reuse dismantled products.</p> <p>6-2. Avoiding thermal shock</p> <p>Preheating condition</p> <table><tr><th>Soldering</th><th>Temp. (°C)</th></tr><tr><td>Manual soldering</td><td>ΔT ≤ 150</td></tr></table>	Temp. (°C)	Duration (sec.)	Wattage (W)	Shape (mm)	300 max.	3 max.	20 max.	∅ 3.0 max.	Soldering	Temp. (°C)	Manual soldering	ΔT ≤ 150
Temp. (°C)	Duration (sec.)	Wattage (W)	Shape (mm)											
300 max.	3 max.	20 max.	∅ 3.0 max.											
Soldering	Temp. (°C)													
Manual soldering	ΔT ≤ 150													

No.	Process	Condition
7	Cleaning	<p>1) If an unsuitable cleaning fluid is used, flux residue or some foreign articles may stick to product surface to deteriorate especially the insulation resistance.</p> <p>2) If cleaning condition is not suitable, it may damage the product.</p> <p>2)-1. Insufficient washing</p> <p>(1) Terminal electrodes may corrode by Halogen in the flux.</p> <p>(2) Halogen in the flux may adhere on the surface of product, and lower the insulation resistance.</p> <p>(3) Water soluble flux has higher tendency to have above mentioned problems (1) and (2).</p> <p>2)-2. Excessive washing</p> <p>When ultrasonic cleaning equipment is used, excessive ultrasonic power or direct vibration transfer to a printed wiring board may generate a resonant vibration in the board. This may cause a crack in a capacitor or its solder joints to the board and degradation in the terminal strength of the capacitor. In order to avoid this, the following cleaning conditions are recommended.</p> <p style="text-align: center;">Power : 20 W/ℓ max. Frequency : 40 kHz max. Washing time : 5 minutes max.</p> <p>2)-3. If the cleaning fluid is contaminated, density of Halogen increases, and it may bring the same result as insufficient cleaning.</p>
8	Coating and molding of the P.C.board	<p>1) When the P.C.board is coated, please verify the quality influence on the product.</p> <p>2) Please verify carefully that there is no harmful decomposing or reaction gas emission during curing which may damage the product.</p> <p>3) Please verify the curing temperature.</p>

No.	Process	Condition				
9	Handling after product mounted  Caution	<div><div>1) Please pay attention not to bend or distort the P.C.board after soldering in handling otherwise the product may crack.</div><div><div><div>Bend</div></div><div><div>Twist</div></div></div><div><div>2) Printed circuit board cropping should not be carried out by hand, but by using the proper tooling. Printed circuit board cropping should be carried out using a board cropping jig as shown in the following figure or a board cropping apparatus to prevent inducing mechanical stress on the board.</div><div><div>(1)Example of a board cropping jig</div><div>Recommended example: The board should be pushed from the back side, close to the cropping jig so that the board is not bent and the stress applied to the product is compressive.</div><div>Unrecommended example: If the pushing point is far from the cropping jig and the pushing direction is from the front side of the board, large tensile stress is applied to the product, which may cause cracks.</div></div><div><div><div>Outline of jig</div></div><div><table><tr><th>Recommended</th><th>Unrecommended</th></tr><tr><td></td><td></td></tr></table></div></div></div></div>	Recommended	Unrecommended		
Recommended	Unrecommended					
						

No.	Process	Condition																	
9	Handling after chip mounted  Caution	<p>(2)Example of a board cropping machine</p> <p>An outline of a printed circuit board cropping machine is shown below. The top and bottom blades are aligned with one another along the lines with the V-grooves on printed circuit board when cropping the board.</p> <p>Unrecommended example: Misalignment of blade position between top and bottom, right and left, or front and rear blades may cause a crack in the product.</p> <div><div><p>Outline of machine</p></div><div><p>Principle of operation</p></div><div><p>Cross-section diagram</p></div></div> <table><tr><th rowspan="2">Recommended</th><th colspan="3">Unrecommended</th></tr><tr><th>Top-bottom misalignment</th><th>Left-right misalignment</th><th>Front-rear misalignment</th></tr><tr><td></td><td></td><td></td><td></td></tr></table> <p>3) When functional check of the P.C.board is performed, check pin pressure tends to be adjusted higher for fear of loose contact. But if the pressure is excessive and bend the P.C.board, it may crack the product or peel the terminations off. Please adjust the check pins not to bend the P.C.board.</p> <table><tr><th>Item</th><th>Not recommended</th><th>Recommended</th></tr><tr><td>Board bending</td><td></td><td></td></tr></table>	Recommended	Unrecommended			Top-bottom misalignment	Left-right misalignment	Front-rear misalignment					Item	Not recommended	Recommended	Board bending		
Recommended	Unrecommended																		
	Top-bottom misalignment	Left-right misalignment	Front-rear misalignment																
																			
Item	Not recommended	Recommended																	
Board bending																			

No.	Process	Condition
10	Handling of loose product	<p>1) If dropped the product may crack. Once dropped do not use it. Especially, the large case sized product are tendency to have cracks easily, so please handle with care.</p>  <p>2) Piling the P.C.board after mounting for storage or handling, the corner of the P.C. board may hit the product of another board to cause crack.</p> 
11	Caution during operation of equipment	<p>1) A product shall not be touched directly with bare hands during operation in order to avoid electric shock. Electric energy held by the product may be discharged through the human body when touched with a bare hand. Even when the equipment is off, a product may stay charged. The product should be handled after being completely discharged using a resistor.</p> <p>2) The terminals of a product shall not be short-circuited by any accidental contact with a conductive object. A product shall not be exposed to a conductive liquid such as an acid or alkali solution. A conductive object or liquid, such as acid and alkali, between the terminals may lead to the breakdown of a product due to short circuit</p> <p>3) Confirm that the environment to which the equipment will be exposed during transportation and operation meets the specified conditions. Do not to use the equipment in the following environments.</p> <ol style="list-style-type: none"> (1) Environment where a product is spattered with water or oil (2) Environment where a product is exposed to direct sunlight (3) Environment where a product is exposed to Ozone, ultraviolet rays or radiation (4) Environment where a product exposed to corrosive gas(e.g. hydrogen sulfide, sulfur dioxide, chlorine. ammonia gas etc.) (5) Environment where a product exposed to vibration or mechanical shock exceeding the specified limits. (6) Atmosphere change with causes condensation

No.	Process	Condition
12	Others  Caution	<p>The products listed on this specification sheet are intended for use in general electronic equipment (AV equipment, telecommunications equipment, home appliances, amusement equipment, computer equipment, personal equipment, office equipment, measurement equipment, industrial robots) under a normal operation and use condition.</p> <p>The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require a more stringent level of safety or reliability, or whose failure, malfunction or trouble could cause serious damage to society, person or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below or for any other use exceeding the range or conditions set forth in this specification sheet. If you intend to use the products in the applications listed below or if you have special requirements exceeding the range or conditions set forth in this specification, please contact us.</p> <ul style="list-style-type: none"> (1) Aerospace/Aviation equipment (2) Transportation equipment (cars, electric trains, ships, etc.) (3) Medical equipment (Excepting Pharmaceutical Affairs Law classification Class1, 2) (4) Power-generation control equipment (5) Atomic energy-related equipment (6) Seabed equipment (7) Transportation control equipment (8) Public information-processing equipment (9) Military equipment (10) Electric heating apparatus, burning equipment (11) Disaster prevention/crime prevention equipment (12) Safety equipment (13) Other applications that are not considered general-purpose applications <p>When designing your equipment even for general-purpose applications, you are kindly requested to take into consideration securing protection circuit/device or providing backup circuits in your equipment.</p>

12. TAPE PACKAGING SPECIFICATION

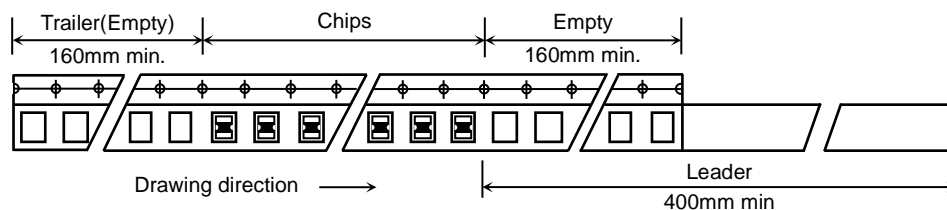
1. CONSTRUCTION AND DIMENSION OF TAPING

1-1. Dimensions of carrier tape

Dimensions of paper tape shall be according to Appendix 3, 4.

Dimensions of plastic tape shall be according to Appendix 5.

1-2. Bulk part and leader of taping



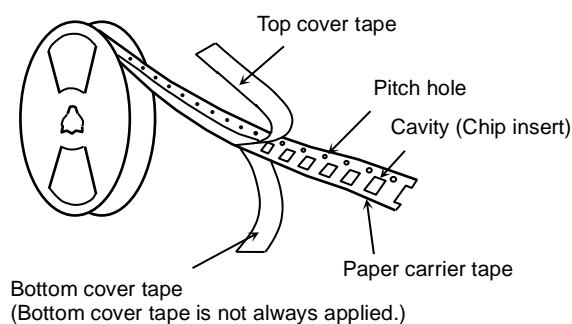
1-3. Dimensions of reel

Dimensions of $\varnothing 178$ reel shall be according to Appendix 6.

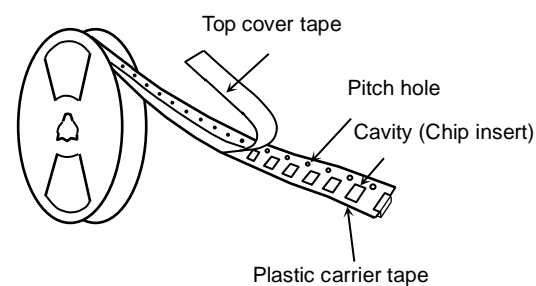
Dimensions of $\varnothing 330$ reel shall be according to Appendix 7.

1-4. Structure of taping

<Paper>



<Plastic>



2. PRODUCT QUANTITY

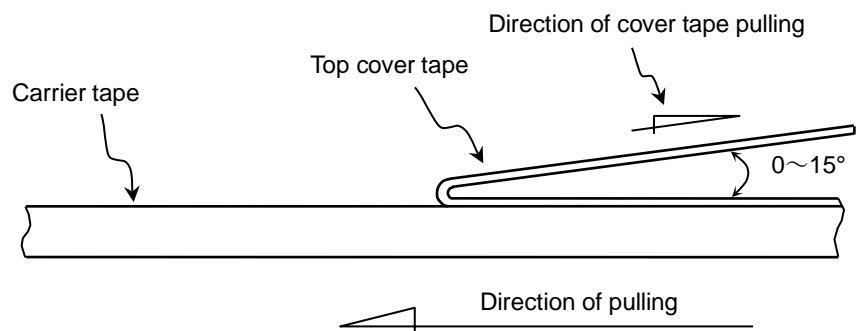
Please refer to detail page on TDK web..

3. PERFORMANCE SPECIFICATIONS

3-1. Fixing peeling strength (top tape)

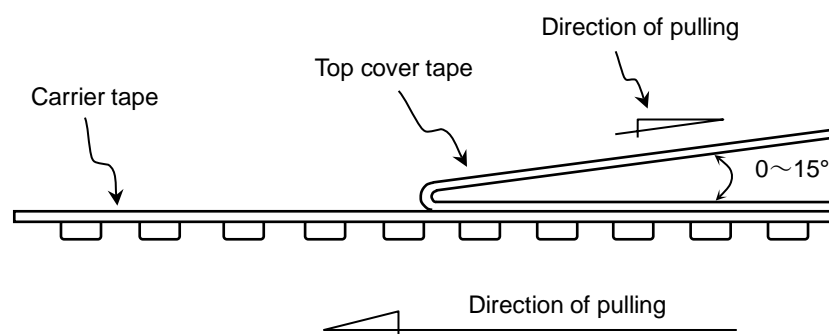
$0.05\text{N} < \text{Peeling strength} < 0.7\text{N}$

<Paper>



[Paper tape should not adhere to top cover tape when pull the cover tape.]

<Plastic>



3-2. Carrier tape shall be flexible enough to be wound around a minimum radius of 30mm with components in tape.

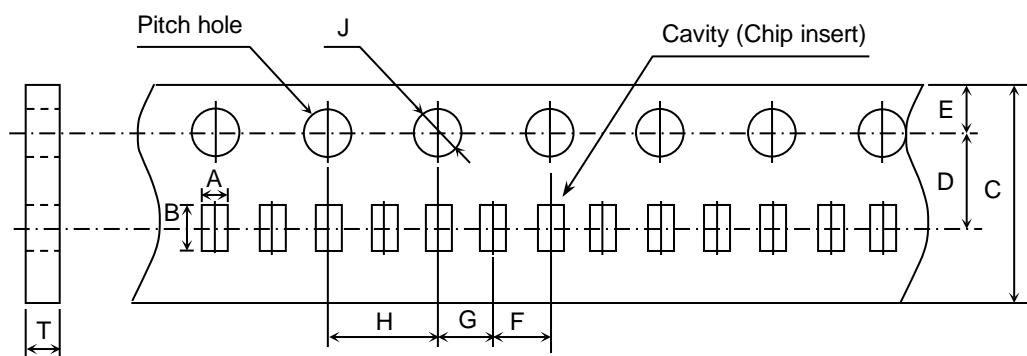
3-3. The missing of components shall be less than 0.1%

3-4. Components shall not stick to fixing tape.

3-5. When removing the cover tape, there shall not be difficulties by unfitting clearance gap, burrs and crushes of cavities. Also the sprocket holes shall not be covered by absorbing dust into the suction nozzle.

Appendix 3

Paper Tape



(Unit : mm)

Symbol	A	B	C	D	E	F
Case size						
YFF15	(0.62) * (0.75)	(1.12) * (1.18)	8.00 ± 0.30	3.50 ± 0.05	1.75 ± 0.10	2.00 ± 0.05

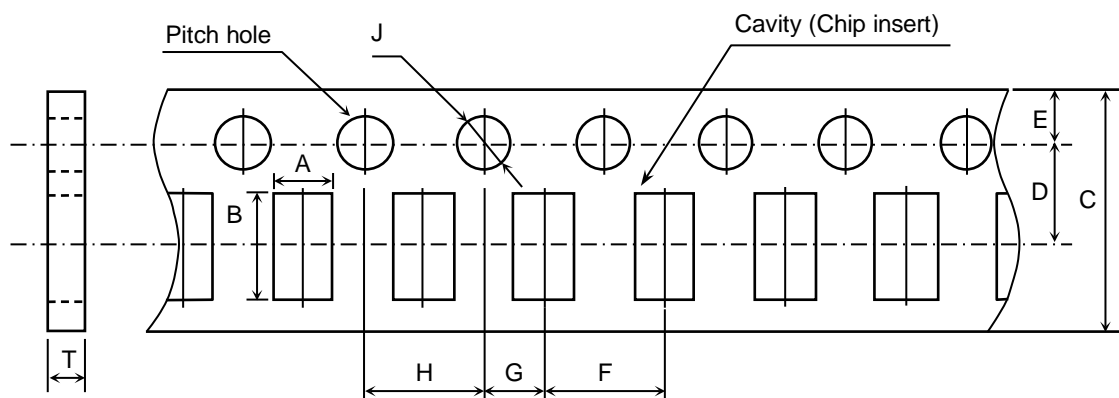
Symbol	G	H	J	T
Case size				
YFF15	2.00 ± 0.05	4.00 ± 0.10	$\varnothing 1.50^{+0.10}_0$	0.70 max.

() Reference value.

* Applied to YFF15PC1V224M, YFF15PC0J105MT*A**, YFF15PC0G435M.

Appendix 4

Paper Tape



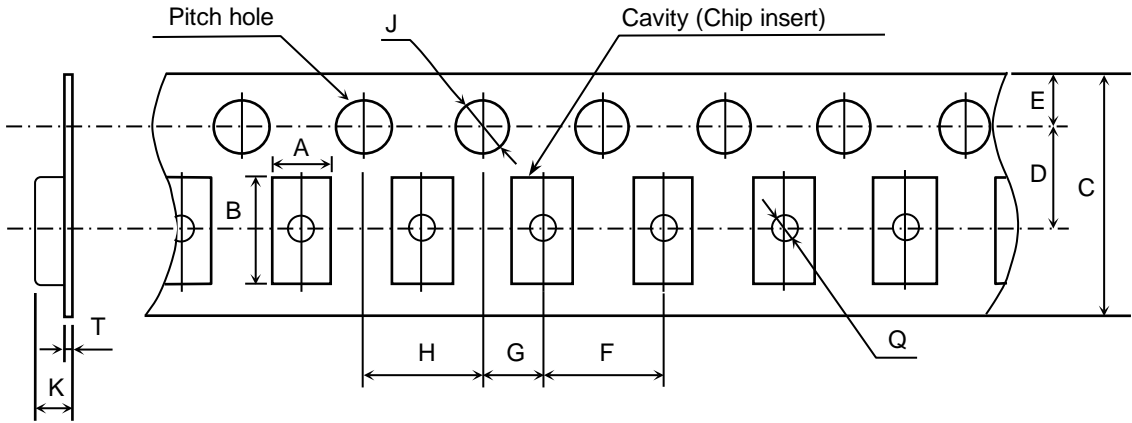
(Unit : mm)

Symbol	A	B	C	D	E	F
Case size						
YFF18	(1.10)	(1.90)	8.00 ± 0.30	3.50 ± 0.05	1.75 ± 0.10	4.00 ± 0.10
YFF21	(1.50)	(2.30)				

Symbol	G	H	J	T
Case size				
YFF18	2.00 ± 0.05	4.00 ± 0.10	$\varnothing 1.50^{+0.10}_0$	1.20 max.
YFF21				

() Reference value.

Appendix 5 Plastic Tape



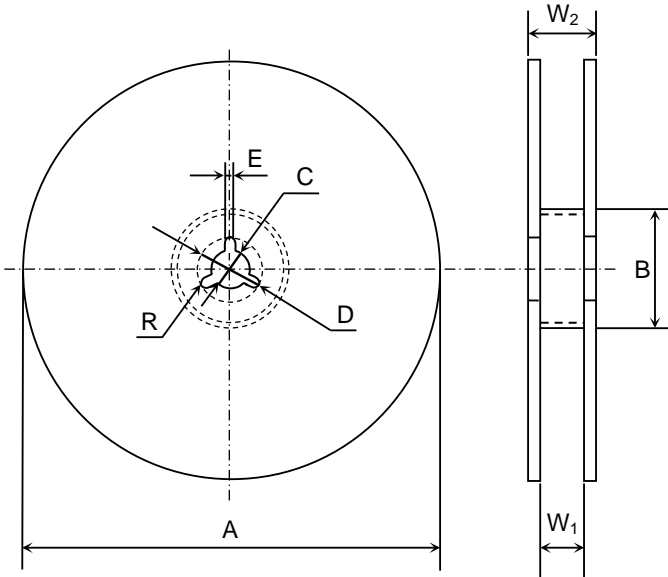
(Unit : mm)

Symbol	A	B	C	D	E	F
Case size						
YFF18 (10μF)	(1.10)	(1.90)	8.00±0.30	3.50±0.05	1.75±0.10	4.00±0.10
YFF31	(1.90)	(3.50)				
Symbol	G	H	J	K	T	Q
Case size						
YFF18 (10μF)	2.00±0.05	4.00±0.10	ø 1.50 ^{+0.10} ₀	1.60 max.	0.30 max.	ø 0.50 min.
YFF31				2.50 max.		

() Reference value.

Appendix 6

Dimensions of reel (Material : Polystyrene)

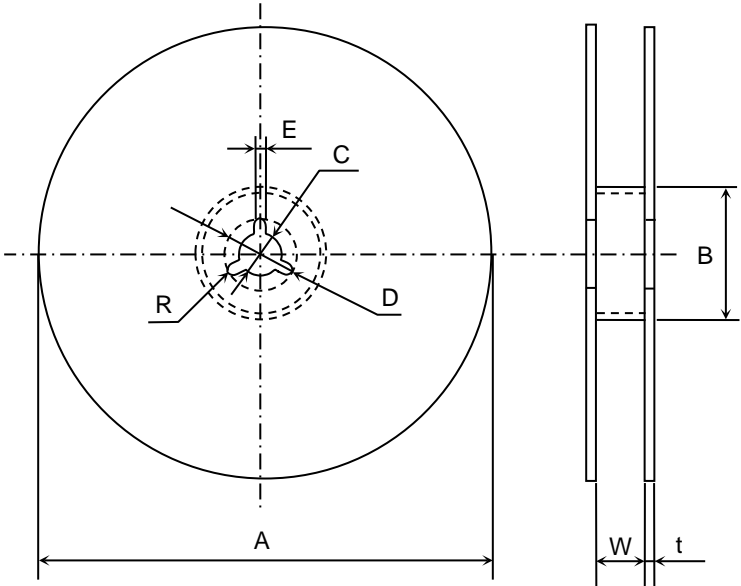


(Unit : mm)

Symbol	A	B	C	D	E	W ₁
Dimension	∅ 178±2.0	∅ 60±2.0	∅ 13±0.5	∅ 21±0.8	2.0±0.5	9.0±0.3
Symbol	W ₂	R				
Dimension	13.0±1.4	1.0				

Appendix 7

Dimensions of reel (Material : Polystyrene)



(Unit : mm)

Symbol	A	B	C	D	E	W
Dimension	∅ 382 max. (Nominal ∅ 330)	∅ 50 min.	∅ 13±0.5	∅ 21±0.8	2.0±0.5	10.0±1.5
Symbol	t	R				
Dimension	2.0±0.5	1.0				