

ACSC03-41SURKWA-F01

Surface Mount Display

DESCRIPTIONS

- The Hyper Red source color devices are made with AlGaInP on GaAs substrate Light Emitting Diode
- Electrostatic discharge and power surge could damage the LEDs
- It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs
- All devices, equipments and machineries must be electrically grounded

FEATURES

- 0.3 inch digit height
- Low current operation
- Excellent character appearance
- Mechanically rugged
- Gray face, white segment
- Package: 550 pcs / reel
- Moisture sensitivity level: 2a
- RoHS compliant

APPLICATIONS

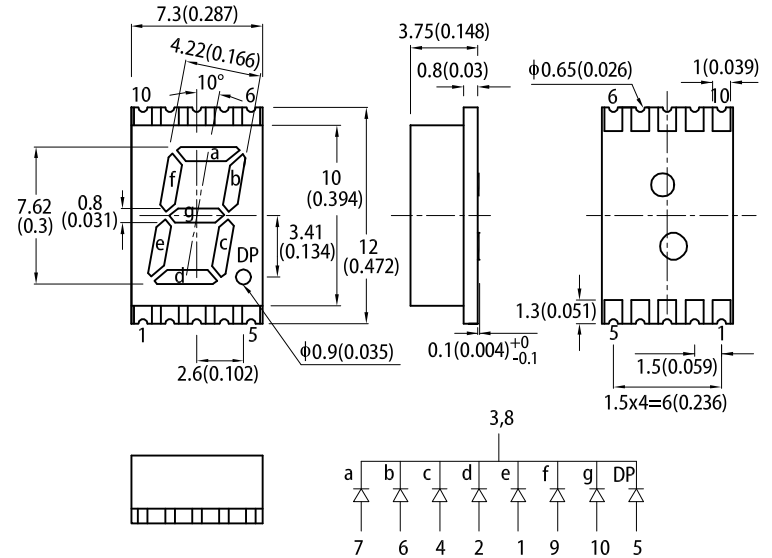
- Home and smart appliances
- Display time and digital combination
- Industrial and instrumental applications
- Numeric status

ATTENTION

Observe precautions for handling electrostatic discharge sensitive devices

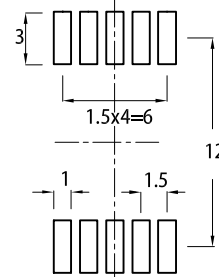


PACKAGE DIMENSIONS



RECOMMENDED SOLDERING PATTERN

(units: mm; tolerance: ± 0.15)



Notes:

1. All dimensions are in millimeters (inches). Tolerance is $\pm 0.25(0.01)$ unless otherwise noted.
2. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.
3. The gap between the reflector and PCB shall not exceed 0.25mm.

SELECTION GUIDE

| Part Number | Emitting Color (Material) | Lens Type | Iv (ucd) @ 10mA ^[1] | | Description |
|---------------------|---------------------------|----------------|--------------------------------|-------|----------------------------------|
| | | | Min. | Typ. | |
| ACSC03-41SURKWA-F01 | ■ Hyper Red (AlGaInP) | White Diffused | 14000 | 27000 | Common Cathode, Rt. Hand Decimal |
| | | | *3600 | *6400 | |

Notes:
 1. Luminous intensity / luminous Flux: $\pm 15\%$.
 * Luminous intensity value is traceable to CIE127-2007 standards.

ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

| Parameter | Symbol | Emitting Color | Value | | Unit |
|---|-------------------------------|----------------|-------|------|------|
| | | | Typ. | Max. | |
| Wavelength at Peak Emission I _F = 10mA | λ_{peak} | Hyper Red | 645 | - | nm |
| Dominant Wavelength I _F = 10mA | $\lambda_{\text{dom}}^{[1]}$ | Hyper Red | 630 | - | nm |
| Spectral Bandwidth at 50% Φ REL MAX I _F = 10mA | $\Delta\lambda$ | Hyper Red | 28 | - | nm |
| Forward Voltage I _F = 10mA | V _F ^[2] | Hyper Red | 1.85 | 2.35 | V |
| Reverse Current (V _R = 5V) | I _R | Hyper Red | - | 10 | μA |

Notes:

1. The dominant wavelength (λ_d) above is the setup value of the sorting machine. (Tolerance λ_d : $\pm 1\text{nm}$.)

2. Forward voltage: $\pm 0.1\text{V}$.

3. Wavelength value is traceable to CIE127-2007 standards.

4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

| Parameter | Symbol | Value | Unit |
|---|--------------------------------|------------|------|
| Power Dissipation | P _D | 75 | mW |
| Reverse Voltage | V _R | 5 | V |
| Junction Temperature | T _j | 115 | °C |
| Operating Temperature | T _{op} | -40 to +85 | °C |
| Storage Temperature | T _{stg} | -40 to +85 | °C |
| DC Forward Current | I _F | 30 | mA |
| Peak Forward Current | I _{FP} ^[1] | 185 | mA |
| Electrostatic Discharge Threshold (HBM) | - | 3000 | V |

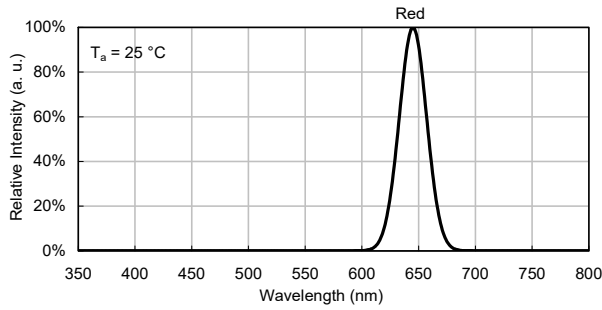
Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.

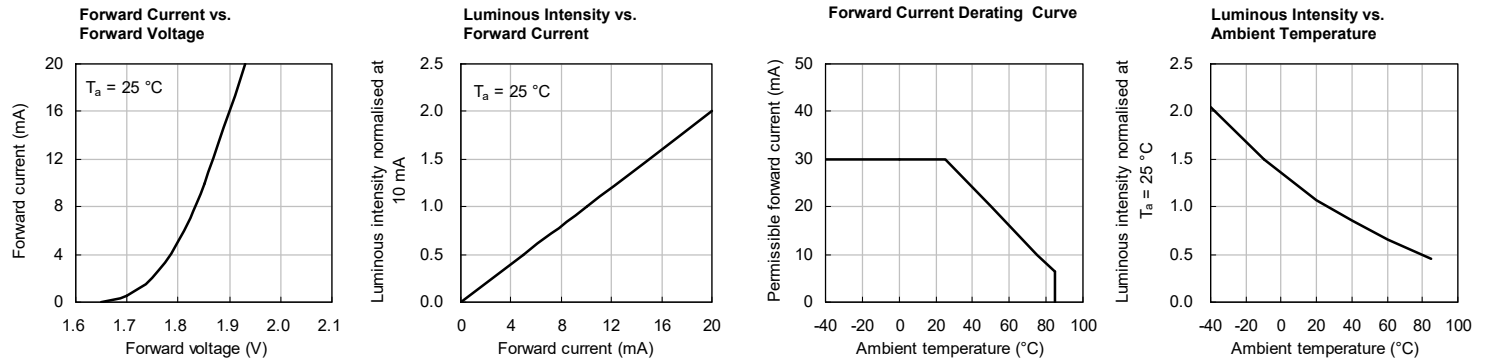
2. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

TECHNICAL DATA

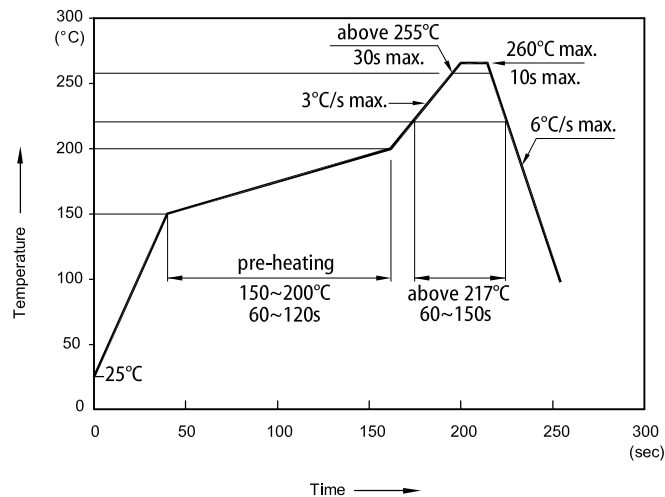
RELATIVE INTENSITY vs. WAVELENGTH



HYPER RED

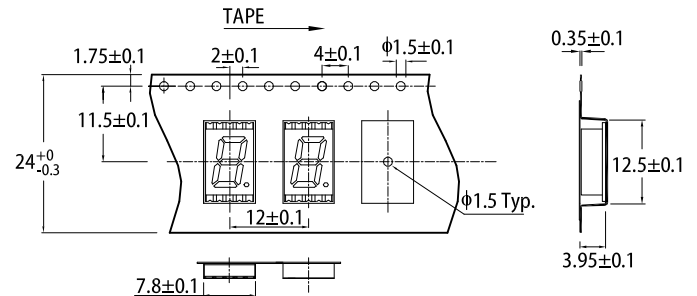


REFLOW SOLDERING PROFILE for LEAD-FREE SMD PROCESS

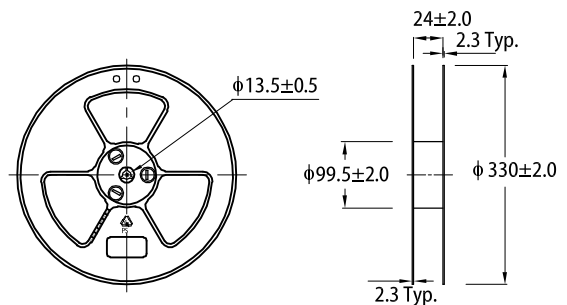


- Notes:
1. Don't cause stress to the LEDs while it is exposed to high temperature.
 2. The maximum number of reflow soldering passes is 2 times.
 3. Reflow soldering is recommended. Other soldering methods are not recommended as they might cause damage to the product.

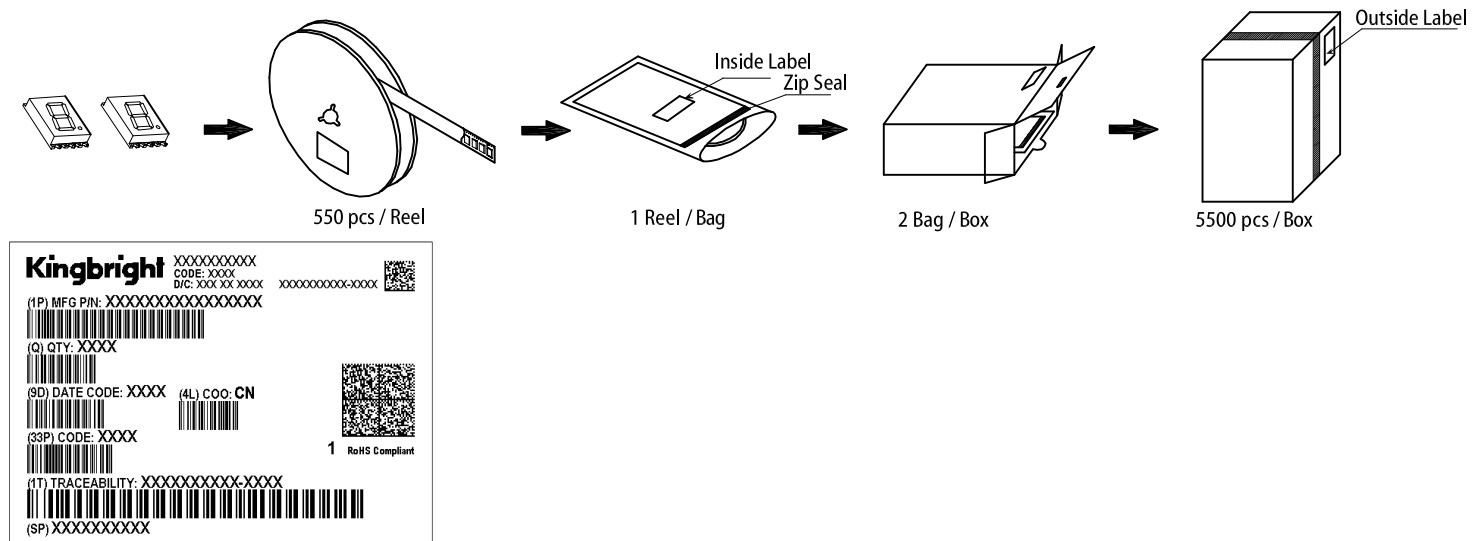
TAPE SPECIFICATIONS (units: mm)



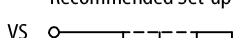
REEL DIMENSION (units: mm)

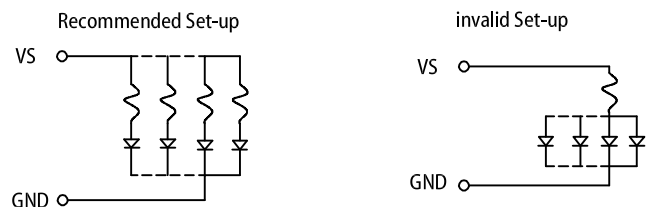


PACKING & LABEL SPECIFICATIONS



CIRCUIT DESIGN NOTES

1. Protective current-limiting resistors may be necessary to operate the LEDs within the specified range.
 2. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor.
 3. The driving circuit should be designed to protect the LED against reverse voltages and transient voltage spikes when the circuit is powered up or shut down.
 4. The safe operating current should be chosen after considering the maximum ambient temperature of the operating environment.
 5. Prolonged reverse bias should be avoided, as it could cause metal migration, leading to an increase in leakage current or causing a short circuit.
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- The diagram, titled "Recommended Set-up", shows a circuit with a positive supply voltage labeled "VS" and a ground connection labeled "GND". Four LEDs are connected in parallel between VS and GND. Each LED is in series with a current-limiting resistor, represented by a zigzag line. The VS line is a solid line that branches into four parallel paths, each containing a resistor and an LED in series. The GND line is a solid line that all four parallel paths converge back to. Dashed lines are used to indicate the parallel connection of the LED branches.



PRECAUTIONARY NOTES

1. The information included in this document reflects representative usage scenarios and is intended for technical reference only.
2. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
3. When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.
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6. When any special process such as potting is required for LED assembly, please consult with Kingbright representative before proceeding.
7. All design applications should refer to Kingbright application notes available at <https://www.KingbrightUSA.com/ApplicationNotes>