

# MIPI DSI, IPS 10.1" LCD TFT DATASHEET

Rev.1.0 2023-11-15

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally Black/IPS	/
Size	10.1	Inch
Viewing Direction	Free	/
Outside Dimensions (W x H x D)	229.46 x 149.10 x 10.82	mm
Active Area (W x H)	216.96 x 135.60	mm
Pixel Pitch (W x H)	0.1695 x 0.1695	mm
Resolution	1280 x 800	/
Brightness	1000	cd/m²
Color Depth	16.7 M	/
Pixel Arrangement	RGB Vertical Stripe	/
Driver IC of Board	SN65DSI83	/
Interface	MIPI DSI	/
EEPROM Memory Size	2-Kbit	Mb
Host Connector	ZIF 34 pins, 0.5mm pitch, down-side contact	/
Supply Voltage for Module	5.0	V
Weight	278	g

Note 1. RoHS compliant

Note 2. LCM weight tolerance: ± 5%.



# **1. REVISION RECORD**

REV NO	. REV DATE	CONTENTS	REMARKS
1.0	2023-11-15	Initial Release	



# 2. CONTENTS

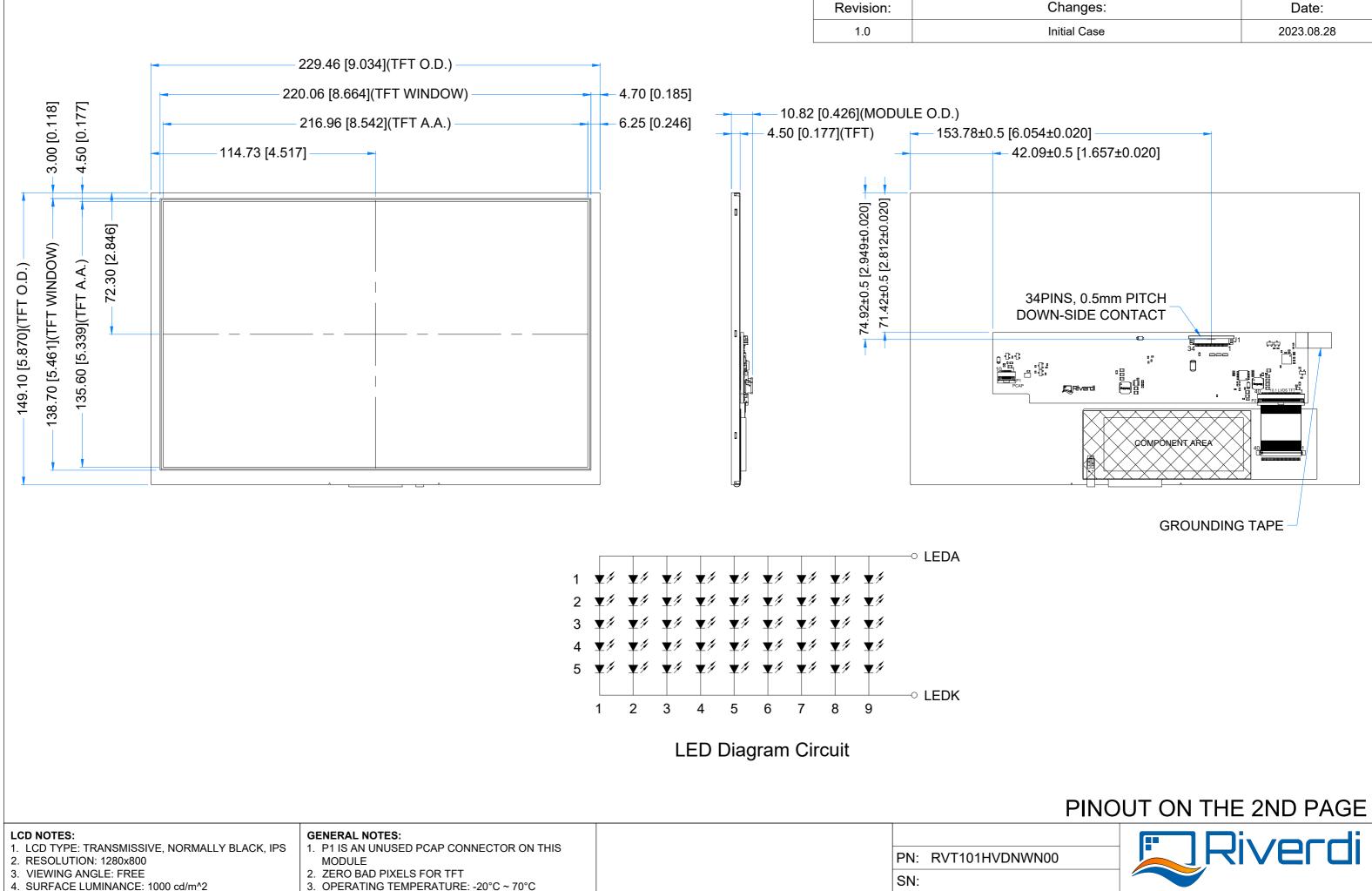
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# **3. MODULE CLASSIFICATION INFORMATION**

		101							
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.

NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	101 – 10.1"
4.	MODEL SERIAL NO.	H – High Brightness, IPS
5.	RESOLUTION	V – 1280 x 800 px
6.	INTERFACE	D – MIPI DSI
7.	FRAME	N – Without Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	N – Without Capacitive Touch Panel
10.	VERSION	00 – (00-99)



- 5. DRIVING IC ON THE BOARD: SN65DSI83ZXHR
- 6. INTERFACE: MIPI DSI
- 7. SUPPLY VOLTAGE FOR MODULE: 5.0 V
- D. OPERATING TEMPERATURE. -20 C ~ 70 C
- 4. STORAGE TEMPERATURE: -30°C ~ 80°C
- 5. WITHOUT INDIVIDUAL TOLERANCE: ±0.3mm[0.012inch]
- 6. RoHS COMPLIANT

PN: RVT101HVDNWN00 SN:		IVe	רםו
DRAWN: M.Natywa	2023.08.28	1:1.68	
CHECKED: Carol Gao	2023.09.04	[mm]	
APPR:		ISO A3	P. 1 of 1



#### **5. ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	MIN	MAX	UNIT	NOTE
Supply Voltage for Module	$V_{DD}$	0	7.4	V	Note 1
Reference Voltage	$V_{REF}$	0	4.6	V	
PWM Input Voltage	$V_{PWM}$	-0.3	7.4		
Operating Temperature	T <sub>OP</sub>	-20	70	°C.	
Storage Temperature	T <sub>ST</sub>	-30	80		
Storage Humidity (@ 25 ± 5°C)	H <sub>ST</sub>	10	-	% RH	
Operating Ambient Humidity (@ 25 ± 5°C)	H <sub>OP</sub>	10	-	70 KH	

**Note 1.** Exceeding maximum values may cause improper operation or permanent damage to the unit.

#### **6. ELECTRICAL CHARACTERISTICS**

PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Supply Voltage	for Module	$V_{DD}$	-	5.0	-	V	
Reference Volt	age	$V_{REF}$	0	-	3.6	v	Note 1
Current drawn	from V <sub>REF</sub>	$IV_{REF}$	-	1.5	-	uA	V <sub>REF</sub> =1.8V
Input High Volt	tage	V <sub>IH</sub>	$0.7V_{REF}$	-	-	V	
Input Low Volt	age	V <sub>IL</sub>	0	-	$0.3V_{REF}$	<b>'</b>	
PWM Logic	High Voltage	VPWMH	1.2	-	$V_{DD}$	V	Note 2
Input Voltage	Low Voltage	Vpwml	0	-	0.4	V	Note 2
PWM Frequence	СУ	FPWM	200	-	1000	Hz	

Note 1. TYP of Reference Voltage is 1.8V or 3.3V which is dependent on the SBC

**Note 2.** PWM input is independent of  $V_{REF}$ . Min of logic high level is 1.2V and max of logic low level is 0.4V.

PARAMETER	SYMBOL	BL 0%	BL 50%	BL 100%	UNIT	NOTE
Current drawn from $V_{DD}$ @5.0V	$IV_{DD}$	210	650	1200	mA	Note 3

Note 3. BL 0%. current was measured with BL brightness set to 0%,

BL 50%.current was measured with BL brightness set to 50%,

BL 100%.current was measured with BL brightness set to 100%.

#### Test condition:

1. Ambient temp 25 °C



#### 7. BACKLIGHT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Lifetime	-	-	50,000	-	hours	Note 1

**Note 1.** Operating life means the period in which the LED brightness goes down to 50% of the initial brightness. Typical operating lifetime is the estimated parameter.

#### 8. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE
Response Time	Tr+Tf		-	25	35	ms	FIG 1.	4
Contrast Ratio	Cr	θ=O°	800	1000	-			1
Luminance Uniformity	δ WHITE	ø=0° Ta=25 °C	-	75	-	%	FIG 2.	3
Surface Luminance	Lv	14-25 C	-	1000	-	cd/m²		2
		ø = 90°	75	85	-	deg		6
Viewing Angle	9	ø = 270° ø = 0°	75	85	-	deg	FIG 3.	
Range			75	85	-	deg		
		ø = 180°	75	85	-	deg		
	Rx		0.22	0.26	0.30	-		
	Ry		0.20	0.24	0.28	-		
	Gx	θ=O°	0.34	0.38	0.42	-		
CIE (x, y)	Gy	Ø=0°	0.50	0.54	0.58	-	FIG 2.	5
Chromaticity	Bx	Ta=25 °C	0.10	0.14	0.18	-	FIU Z.	J
	Ву	1a-25 C	0.09	0.13	0.17	-		
	Wx		0.28	0.32	0.36	-		
	Wy		0.29	0.33	0.37	-		

**Note 1.** Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 3.

Contrast Ratio =  $\frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$ 

**Note 2.** Surface luminance is measured 500mm from the center point of the LCD surface with all pixels displaying white. For more information see Figure 3.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

**Note 3.** The uniformity in surface luminance  $\delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 3.

 $\delta \text{ WHITE } = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$ 

**Note 4.** Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 2. The test equipment is BM-7A.



**Note 5.** CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then calculating the average value.

**Note 6**. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 4.

**Note 7.** Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80). For response time testing, the testing data is based on BM-7A. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, Chromaticity the test data is based on SR-3A.

Figure 1. The definition of response time

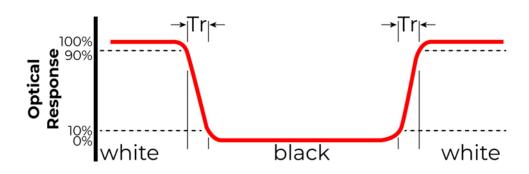
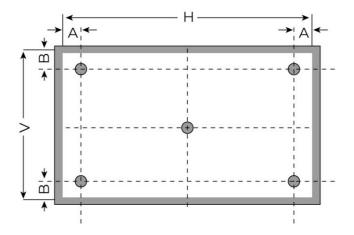


Figure 2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity



A: 5mm

B:5mm

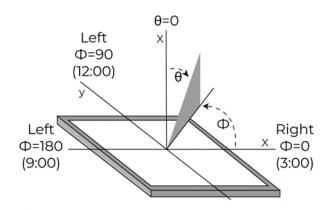
H, V: Active Area

Light spot size Ø=5mm, 500mm distance from the LCD surface to detector lens.

Measurement instrument is SR-3A



Figure 3. The definition of viewing angle



## 9. INTERFACES DESCRIPTION

## 9.1 J1 connector

	COMMICCION			
PIN NO.	CONNECTOR	I/O/P	DESCRIPTION	NOTE
1	GND	Р	Ground	
2	DSI_D0P	1/0	MIDI DCI differential data pair (Data lane 0)	
3	DSI_D0N	1/0	MIPI DSI differential data pair. (Data lane 0)	
4	GND	Р	Ground	
5	DSI_D1P	I	MIPI DSI differential data pair. (Data lane 1)	
6	DSI_D1N	I	MIPI D31 dillereritial data pair. (Data larie 1)	
7	GND	Р	Ground	
8	DSI_CLKP	I	MIPI DSI differential clock pair	
9	DSI_CLKN	I	Wife Dordinerential clock pail	
10	GND	Р	Ground	
11	DSI_D2P	1	MIPI DSI differential data pair. (Data lane 2)	
12	DSI_D2N	I	Wife Doi differential data pair. (Data lane 2)	
13	GND	Р	Ground	
14	DSI_D3P	I	MIPI DSI differential data pair. (Data lane 3)	
15	DSI_D3N	1	MIFT DOI differential data pair. (Data lane 3)	
16	GND	Р	Ground	
17	PWR_DN	I	Power down (With locally generated reset after releasing power-down)	Note 2
			Active Low, display is off when signal is low;	
18	PWM	I	Backlight brightness control	Note 3
19	INT	0	Touch panel Interrupt signal; Open-drain output, active low	Nata 2
20	I2C_SCL	I	I2C clock signal	Note 2
21	I2C_SDA	1/0	I2C data signal	
22	RESET	I	Touch panel reset, active low (touch panel is off)	
23	NC	/	No connection	
24	$V_{REF}$	Р	Reference voltage	
25	NC	/	No connection	



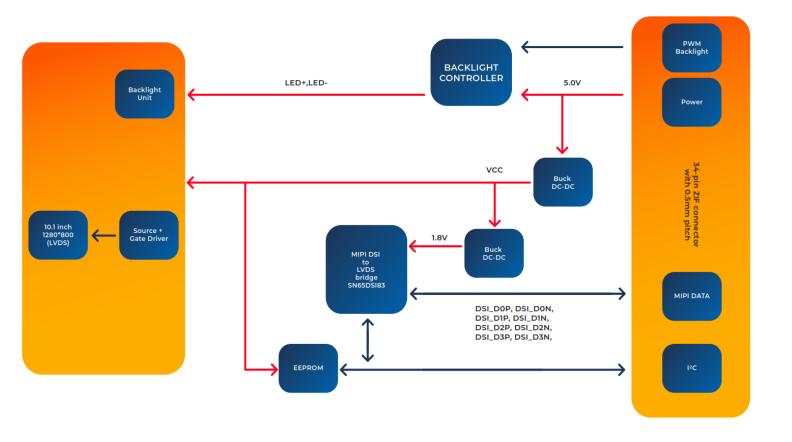
26				
27	5.0V	Р	Power supply $V_{DD}$	
28				
29				
30	GND	Р	Ground	
31				
32	NC	/	No connection	
33	- INC			
34				

Note 1. Matched 34 pins, 0.5 mm pitch, 150mm long FFC accessory: FFC0534150.

Note 2. Internally pull-up with 15K resistor to  $V_{REF}$ .

**Note 3**.Backlight driver is TPS61500. PWM frequency range: 200Hz-1KHz. 0% PWM duty cycle corresponds to minimum brightness. 100% PWM duty cycle corresponds to maximum brightness.

#### 10. DIAGRAM BLOCK





## 11. TFT TIMING CHARACTERISTICS

The TFT of the module applies Riverdi high brightness, IPS, 10.1" TFT: RVT101HVLNWN00 For detailed information of the display, please refer to datasheet of display.

# 11.1 Timing table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Clock Frequency	F <sub>DCLK</sub>	66.3	72.4	78.9	MHz
(Rate=60Hz (LVDS))					
HSYNC Period Time	Тн	1380	1440	1500	DCLK
Horizontal Display area	T <sub>HD</sub>	1280			DCLK
Hsync Pulse Width	T <sub>HPW</sub>	1	-	40	Тс
Hsync Back Porch	Тнвр	88	88	88	DCLK
(With pulse width)					
Hsync Front Porch	$T_{HFP}$	12	72	132	DCLK
VSYNC Period Time	T <sub>V</sub>	824	838	872	
Vertical Display area	T <sub>VD</sub>	800			
Vsync Pulse Width	T <sub>vw</sub>	1	-	20	
Vsync Back Porch	$T_{VBP}$	23	23	23	Н
(With pulse width)					
Vsync Front Porch	T <sub>VFP</sub>	1	15	49	



#### 12. INSPECTION

Standard acceptance/rejection criteria for TFT module

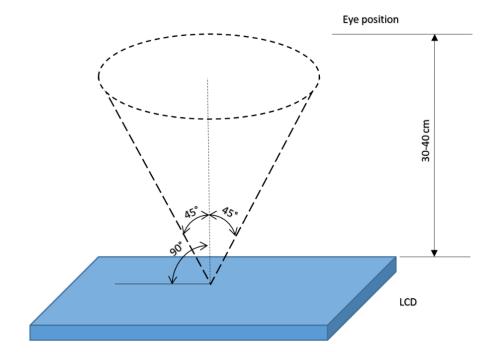
# 12.1 Inspection condition

Ambient conditions:

- Temperature: 25 ± 2°C
- Humidity: (60 ± 10) %RH
- Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

Viewing distance: 35 ± 5cm between inspector bare eye and LCD.

Viewing Angle: U/D: 45°/45°, L/R: 45°/45°





# 12.2 Inspection standard

The LCD TFT has zero bad pixels. Please refer the item "Bright/Dark dots".

ITEM		CRITER	RION				
	x		Size =10.1"				
Black spots, white spots, light leakage, Foreign Particle (round Type)		Average	Average Diameter		Qu	Qualified Qty	
	<b>( )</b>	D ≤ 0.2 m	D ≤ 0.2 mm		lgr	Ignored	
		0.2 mm ·	0.2 mm < D ≤ 0.3 mm		N≤	N≤4	
(	D=(x+y)/2 Spots density: 10 mm	0.5mm <	: D		N =	= O	
	Width		Size =10.1"				
LCD black spots,		Lengt	Length		dth	Qualified Qty	
white spots, light leakage	Length	-	-		0.05	Ignored	
(line Type)	{	L ≤ 5.	L ≤ 5.0		W ≤ 0.1	N ≤ 3	
	Spots density: 10 mm	5.0 <	5.0 < L 0.10 < 5.0 <			N = 0	
	S		Size =10.1"				
	Item			Qualified Qty			
Bright/Dark	Bright dots			0			
Dots	Dark dots		0				
	Cluster Bright Dots or D						
	Total Bright and Dar		<b>-</b> "		0		
	Size ≥ 5"				<b>N</b> +		
	Average Diameter D < 0.2 mm			Qualified Qty Ignored			
Clear spots	0.2 mm < D < 0.3 mm			ignored 4			
Cieai Spots	0.3 mm < D < 0.5 mm			2			
	0.5 mm < D			0			
	Spots density: 10 mm						
	Size ≥ 5"						
Taylah mamal	Average Diameter			Qualified Qty			
Touch panel spot	D < 0.25 mm			Ignored			
	0.25 mm < D < 0.5 mm			4			
	0.5 mm < D			0			
	Size ≥ 5"						
Touch panel	Length Width					ualified Qty	
White line	- W < 0.03		Ignored				
Scratch	L < 5.0 0.03 < W < 9		).05				
	- 0.05 < W 0						



## **13. RELIABILITY TEST**

NO.	TEST ITEM	TEST CONDITION	NOTE			
1	High Temperature Storage	80°C/120 hours				
2	Low Temperature Storage	-30°C/120 hours				
3	High Temperature Operating	70 °C /120 hours	Note 1			
4	Low Temperature Operating	-20°C/120 hours				
5	High Temperature and High Humidity	40°C, 90%RH, 120Hrs				
6	Thermal Cycling Test (No operation)	-20°C for 30min, 70°C for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note 2			
7	Vibration Test	Frequency: 10 ÷ 55 Hz. Stroke: 1.5 mm. Sweep: 10Hz ÷ 55Hz ÷ 10 Hz. 2 hours for each direction of X, Y, Z (Total 6 hours)				
8	Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces				

**Note 1.** Sample quantity for each test item is  $5 \div 10$  pcs.

**Note 2.** Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.



#### 14. LEGAL INFORMATION

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