

111 Corning Rd, Suite 116 • Cary, NC 27518

LCDK185CTL1ART02 & LCDK185NTL1NCT02

LCD185 with Adapter for TI SK-AM62 Starter Kit

Revision		
Model Numbers LCDK185CTL1ART02		
	LCDK185NTL1NCT02	
Datasheet Revision	1.3	
Drawing Revision	c	

	Customer Approval	
Approved by:		Date:

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Revision History

Document Revision

Date	Version #	Description	Created By	Checked By	Approved By
9/28/2022	R1.0	Initial release	DA	КВ	JH
9/30/2022	R1.1	Updated schematics R303 is now 100K	ZH	JH	JH
2/17/2023	R1.2	Updated mechanical drawing, updated ordering information table, removed PCB schematics, updated PCB pictorials, PCB-L0136R1.2 was PCB-L0136R1.1, PCB-L0152R1.1 was PCB-L0152R1.0		DA	JH
03/27/2023	R1.3	Updated USB-C Charger to USB-A, and updated the part number to a 5V 2A part	ZH	DA	JH

Hardware Revision

Date	Version #	Description
09/28/2022	R1.0	Initial Release
2/17/2023	R1.1	Added PCB stacking hardware
3/27/2023	R1.2	Charger part number AQ10A-050BP-H was VEL05US050-US-BB

Product Overview

This LCD kit supports connecting LTS LCD185 to the TI SK-AM62 starter kit. These 2 platforms may also be referred to as AM62x platforms. This kit predominantly consists of LCD185, PCB-L0136, and PCB-L0152.

LCD185 is a high-brightness, high-resolution (WUXGA) 10.1" LCD that requires the following 3 connections.

- LVDS (45-pin flex)
- Touch Screen (8-pin flex)
- Backlight (cabled 2-pin connectors)

PCB-L0136 and PCB-L0152 converts the 2 flex connections to the single 40-pin flex used on the TI SK-AM62 starter kit. PCB-L0136 also provides the LCD185 backlight driver which the starter kit can control over the same 40-pin flex. Finally, PCB-L0136 supports a USB Type C connector to be powered from commodity USB chargers. Since these chargers can support various maximum currents, PCB-L0136 has a rotary switch to adjust the maximum backlight power.

Ordering Information

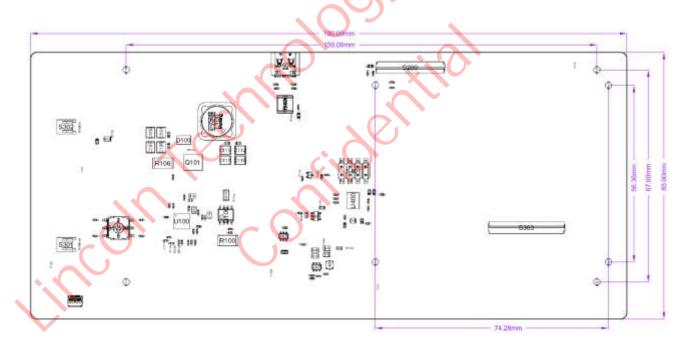
This document refers to parts listed below either by part number or name indicated in the following table. Part revisions are only specifically indicated in this table and may be excluded in other sections of this document.

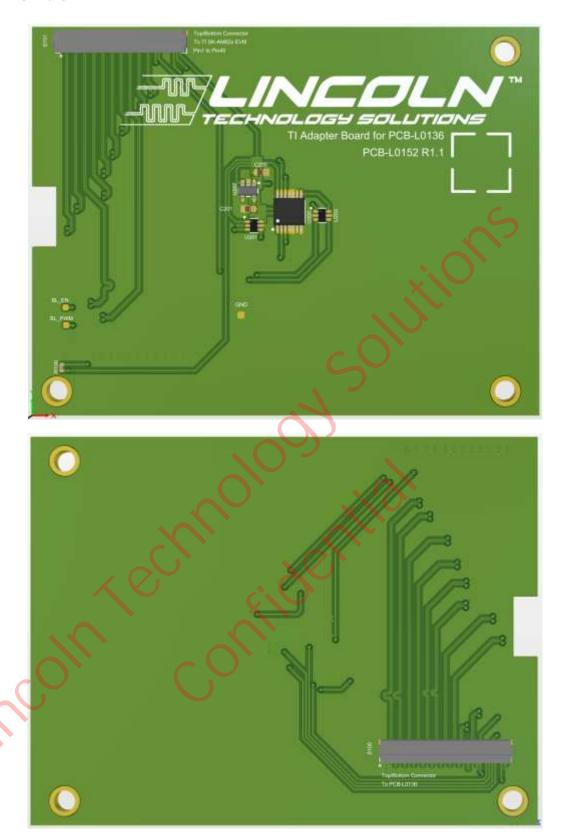
LTS Part #	Parts in Kit	Description
	PCB-L0136R1.2	AM62x_SOC to LCD185 Adapter Board
	PCB-L0152R1.1	TI Starter Kit Adapter Board
	LCD185-101CTL1ARNTTR1.0	10.1" HBWG Cap Touch
	0151661165	LCD Flex (2" 0.5mm pitch 45 POS FFC cable)
LCDK185CTL1ART02R1.2	0151660431	AM62x Flex (4" 0.5mm pitch 40 POS FFC cable)
	AQ10A-050BP-H	USB-A Charger
	DH-20M50052	USB-A to USB-C cable
	A14040700UX0339	M3 X 9 mm + 4 mm Hex Standoff
	93640A125	M3 x 6 mm Nylon Socket Head Screw
	PCB-L0136R1.2	AM62x_SOC to LCD185 Adapter Board
	PCB-L0152R1.1	TI Starter Kit Adapter Board
	LCD185-101NTL1NCNTTR1.0	10.1" HBWG No Touch with CG
100	0151661165	LCD Flex (2" 0.5mm pitch 45 POS FFC cable)
LCDK185NTL1NCT02R1.2	0151660431	AM62x Flex (4" 0.5mm pitch 40 POS FFC cable)
	AQ10A-050BP-H	USB-A Charger
	DH-20M50052	USB-A to USB-C cable
	A14040700UX0339	M3 X 9 mm + 4 mm Hex Standoff
	93640A125	M3 x 6 mm Nylon Socket Head Screw

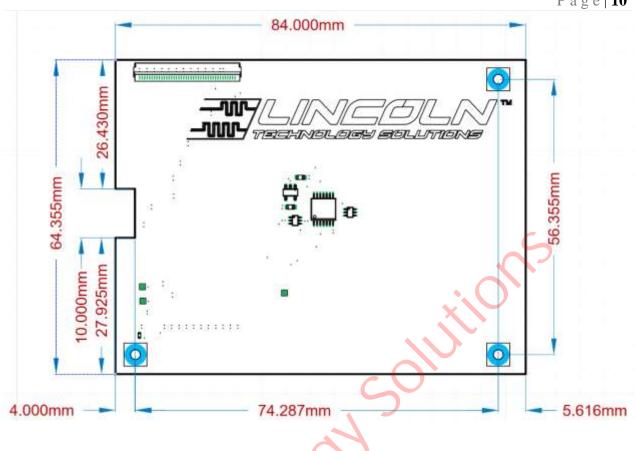
PCB-L0136

The following shows a 3D rendering as well as a dimensioned mechanical drawing.









Required Connections

PCB-L0136 requires the following 3 "types" of connections.

NOTE: Full details on the connectors is provided in <u>Connector Summary</u> and <u>Connector Pinouts</u>.

LCD

PCB-L0136 should be positioned so that the Touch I2C and LCD185 connectors are aligned with the corresponding flex and connector on LCD185.

- The Touch I2C connector supports the Touch flex on LCD185.
- The LCD185 connector supports the flex connector on LCD185 using the LCD Flex.
- The Backlight connectors support the cabled backlight connectors on LCD185.

Power

The VBUS connector supports a standard USB-C cable from a USB charger or power bank.

TI SK-AM62 Starter Kit

The 40-pin FFC connector on PCB-L0136 supports the flex connector on the TI SK-AM62 Starter Kit through the adapter board, PCB-L0152. The 40-pin FFC connectors on both PCB-L0136 and PCB-L0152 have both top and bottom contacts to maximize flex orientation support. A flex cable sample and part number are provided, but the required flex will be defined by the final orientation of the TI SK-AM62 Starter Kit relative to PCB-L0152. At a minimum it must support 40 pins and 0.5mm pitch.

Specifications

Optical and Mechanical (LCD185 + PCB-L0136 + PCB-L0152)

Item	Specification	Unit
Outline Dimensions (typ) ¹	228 x 154 (LCD185)	mm
	Thickness: 26 (with stacked PCB configuration)	
LCD Size (diagonal)	10.1	inches
Active Area	216.8x135.5	mm
Resolution	1920x1200	pixels
Backlight Type	Edge lit	-

⁽¹⁾ The outline dimensions do not include any AM62x platform or associated cabling.

Environmental

Item	Symbol	Value	Unit
Operating Temperature	T _{OPR}	0 ~ 70	°C
Storage Temperature	T _{STG}	-20 ~ 70	°C

Connector Summary

Connector Type	MPN	Description
AM62x SOC (PCB L0136 S200,	FH34SRJ-40S-0.5SH(50)	40 positions
PCBL0152 S100 & S101)		0.5mm pitch
		Connector between PCB-L0136
The same of the sa		and PCB-L0152. Connector to
No. of Concession of the Conce		TI Starter Kit.
Touch I2C (S300)	5034800600	6 positions
		0.5mm pitch
	,50	Connector to Touch I2C
Backlight (S301, S302)	S2B-ZR(LF)(SN)	Connector to LCD backlight
A. The second second	1003	2 positions
	io, gentilo	1.5mm pitch
LCD185 (S303)	FH34SRJ-45S-0.5SH(50)	45 positions
	0,	0.5mm pitch
The state of the s		Connector to LCD185
No Cartesian Control		

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USB-C (S400)	TYPE-C-31-M-12	16 positions
		5V Power
EEPROM (S401)	TSM-104-01-L-DV	8 positions
MAM		2.54mm pitch Optional connector for
		EEPROM programming.
-19	50,	

Connector Pinouts (PCB-L0136)

To PCB-L0152 (S200)

		_	
Number	Pin Name	I/O	Description
1	GND	Р	Ground
2	VSYS) i	TI Starter Kit power-up indicator
3	GND	Р	Ground
4	TP_SCL_1V8	I	Touch Panel and EEPROM I2C Clock
5	TP_SDA_1V8	I/O	Touch Panel and EEPROM I2C Data
6	TP_INT_1V8	0	Touch Panel Interrupt
7	MSTR_RST_3V3	I	Reset
8	BL_PWM_3V3	I	Pulse Width Modulation Back light brightness control
9	BL_EN_3V3	I	Backlight Enable

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10	GND	Р	Ground
11	OLDI0_CLK1N	I	LVDS clock pin
12	OLDI0_CLK1P	I	LVDS clock pin
13	GND	Р	Ground
14	OLDI0_CLK0N	I	LVDS clock pin
15	OLDIO_CLK0P	I	LVDS clock pin
16	GND	Р	Ground
17	OLDI0_A7N	I	LVDS data pin
18	OLDI0_A7P	I	LVDS data pin
19	GND	Р	Ground
20	OLDI0_A6N	I	LVDS data pin
21	OLDIO_A6P	I	LVDS data pin
22	GND	Р	Ground
23	OLDI0_A5N	I	LVDS data pin
24	OLDI0_A5P	I	LVDS data pin
25	GND	Р	Ground
26	OLDIO_A4N	1	LVDS data pin
27	OLDI0_A4P	I	LVDS data pin
28	GND	Р	Ground
29	OLDIO_A3N	I	LVDS data pin
30	OLDI0_A3P	I	LVDS data pin
31	GND	Р	Ground
32	OLDI0_A2N	I	LVDS data pin
33	OLDI0_A2P	I	LVDS data pin

34	GND	Р	Ground
35	OLDI0_A1N	I	LVDS data pin
36	OLDI0_A1P	I	LVDS data pin
37	GND	Р	Ground
38	OLDI0_A0N	I	LVDS data pin
39	OLDI0_A0P	I	LVDS data pin
40	GND	Р	Ground

Touch I2C (S300)

Number	Pin Name	I/O	Description
1	GND	Р	Ground
2	TP_SDA_1V8	I/O	Touch Panel I2C Data
3	TP_SCL_1V8	0	Touch Panel I2C Clock
4	TP_INT_1V8	0	Touch Panel Interrupt
5	TP_RST_1V8	I	Touch Panel Reset
6	VDD	Р	Touch Panel Power

Backlight (S301, S302)

Number	Pin Name	I/O	Description
1	V_{BL_A}	Р	Backlight Anode
2	V_{BL_C}	Р	Backlight Cathode

LCD185 (S303)

Number	Pin Name	I/O	Description
1	VDD	Р	LCD185 Power
2	VDD	Р	LCD185 Power
3	VDD	Р	LCD185 Power
4	VDD	Р	LCD185 Power
5	VDD	Р	LCD185 Power
6	NC	-	No Connection
7	NC	-	No Connection
8	NC	-	No Connection
9	NC	-	No Connection
10	GND	Р	Ground
11	O_LVDS_0_N	0	LVDS Odd Channel, Pair 0
12	O_LVDS_0_P	0	LVDS Odd Channel, Pair 0
13	GND	Р	Ground
14	O_LVDS_1_N	0	LVDS Odd Channel, Pair 1
15	O_LVDS_1_P	0	LVDS Odd Channel, Pair 1
16	GND	Р	Ground
17	O_LVDS_CLK_N	0	LVDS Odd Channel, Clock
18	O_LVDS_CLK_P	0	LVDS Odd Channel, Clock
19	GND	Р	Ground
20	O_LVDS_2_N	0	LVDS Odd Channel, Pair 2
21	O_LVDS_2_P	0	LVDS Odd Channel, Pair 2
22	GND	Р	Ground

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23	O_LVDS_3_N	0	LVDS Odd Channel, Pair 3
24	O_LVDS_3_P	0	LVDS Odd Channel, Pair 3
25	GND	Р	Ground
26	E_LVDS_0_N	0	LVDS Even Channel, Pair 0
27	E_LVDS_0_P	0	LVDS Even Channel, Pair 0
28	GND	Р	Ground
29	E_LVDS_1_N	0	LVDS Even Channel, Pair 1
30	E_LVDS_1_P	0	LVDS Even Channel, Pair 1
31	GND	Р	Ground
32	E_LVDS_CLK_N	0	LVDS Even Channel, Clock
33	E_LVDS_CLK_P	0	LVDS Even Channel, Clock
34	GND	Р	Ground
35	E_LVDS_2_N	0	LVDS Even Channel, Pair 2
36	E_LVDS_2_P	0	LVDS Even Channel, Pair 2
37	GND	Р	Ground
38	E_LVDS_3_N	0	LVDS Even Channel, Pair 3
39	E_LVDS_3_P	0	LVDS Even Channel, Pair 3
40	GND	Р	Ground
41	NC	-	No Connection
42	- NC		No Connection
43	NC	-	No Connection
44	NC	-	No Connection
45	NC	-	No Connection
	I .		

USB-C Power (S400)

Pin	Name	I/O	Description
A1 / B12	GND	Р	Ground
A2 / B11	NC	-	No Connection
A3 / B10	NC	-	No Connection
A4 / B9	VBUS	Р	Input from USB Charger or Power Bank
A5 / B8	NC	-	No Connection
A6 / B7	NC	-	No Connection
A7 / B6	NC	-	No Connection
A8 / B5	NC	-	No Connection
A9 / B4	VBUS	Р	5V VBUS
A10 / B3	NC	-	No Connection
A11 / B2	NC	-	No Connection
A12 / B1	GND	Р	Ground

EEPROM (S401)

This connector is provided to program an on-board EEPROM. This can be used for Linux to identify PCB-L0136, if required by the end customer.

Pin	Name	I/O	Description
1	EE_WP	I	EEPROM Write Protect
2	VSYS	0	TI Starter Kit Power-Up Indicator
3	EE_VIO	0	EEPROM I/O Voltage Reference (1.8V)
4	EE_SCL	I	EEPROM I2C Clock
5	GND	Р	Ground
6	EE_SDA	I/O	EEPROM I2C Data
7	GND	Р	Ground
8	GND	Р	Ground

Connector Pinouts (PCB-L0152)

To PCB-L0136 (S100)

Number	Pin Name	1/0	Description
1	GND	Р	Ground
2	O_LVDS_0_P	0	LVDS Data
3	O_LVDS_0_N	0	LVDS Data
4	GND	Р	Ground
5	O_LVDS_1_P	0	LVDS Data
6	O_LVDS_1_N	0	LVDS Data
7	GND	Р	Ground
8	O_LVDS_2_P	0	LVDS Data

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9	O_LVDS_2_N	0	LVDS Data
10	GND	Р	Ground
11	O_LVDS_3_P	0	LVDS Data
12	O_LVDS_3_N	0	LVDS Data
13	GND	Р	Ground
14	E_LVDS_0_P	0	LVDS Data
15	E_LVDS_0_N	0	LVDS Data
16	GND	Р	Ground
17	E_LVDS_1_P	0	LVDS Data
18	E_LVDS_1_N	0	LVDS Data
19	GND	Р	Ground
20	E_LVDS_2_P	0	LVDS Data
21	E_LVDS_2_N	0	LVDS Data
22	GND	Р	Ground
23	E_LVDS_3_P	0	LVDS Data
24	E_LVDS_3_N	0	LVDS Data
25	GND	P	Ground
26	O_LVDS_CLK_P	0	LVDS Clock
27	O_LVDS_CLK_N	0	LVDS Clock
28	GND	Р	Ground
29	E_LVDS_CLK_P	0	LVDS Clock
30	E_LVDS_CLK_N	0	LVDS Clock
31	GND	Р	Ground
32	BL_EN_3V3	0	Backlight Enable (To test point for debug)

33	BL_PWM_3V3	0	Backlight PWM (To test point for debug)
34	TP_RST_3V3	0	Touch Panel Reset
35	TP_INT_1V8	I	Touch Panel Interrupt
36	SDA_TP_1V8	I/O	Touch Panel & EEPROM I2C Data
37	SCL_TP_1V8	0	Touch Panel & EEPROM I2C Touch
38	GND	Р	Ground
39	VCC_3v3_SYS	0	Power enable pin for PCB-L0136
40	GND	Р	Ground

To TI Starter Kit (S101)

Number	Pin Name	I/O	Description
1	GND	Р	Ground
2	E_LVDS_3_P	I	LVDS Data
3	E_LVDS_3_N	I	LVDS Data
4	GND	Р	Ground
5	E_LVDS_2_P	I	LVDS Data
6	E_LVDS_2_N	I	LVDS Data
7	GND	Р	Ground
8	E_LVDS_CLK_P	I	LVDS Clock
9	E_LVDS_CLK_N	I	LVDS Clock
10	GND	Р	Ground
11	E_LVDS_1_P	I	LVDS Data
12	E_LVDS_1_N	I	LVDS Data
13	GND	Р	Ground

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14	E_LVDS_0_P	I	LVDS Data
15	E_LVDS_0_N	I	LVDS Data
16	GND	Р	Ground
17	O_LVDS_3_P	I	LVDS Data
18	O_LVDS_3_N	I	LVDS Data
19	GND	Р	Ground
20	O_LVDS_2_P	I	LVDS Data
21	O_LVDS_2_N	I	LVDS Data
22	GND	Р	Ground
23	O_LVDS_CLK_P	I	LVDS Clock
24	O_LVDS_CLK_N	I	LVDS Clock
25	GND	Р	Ground
26	O_LVDS_1_P	I	LVDS Data
27	O_LVDS_1_N	I	LVDS Data
28	GND	Р	Ground
29	O_LVDS_0_P		LVDS Data
30	O_LVDS_0_N	7	LVDS Data
31	GND	Р	Ground
32	TP_INT_3V3	0	Touch Panel Interrupt
33	TP_RST_3V3	I	Touch Panel Reset
34	GND	Р	Ground
35	GND	Р	Ground
36	NC	-	No connection
37	NC	-	No connection

38	SDA_TP_3V3	I/O	Touch Panel & EEPROM I2C Data
39	SCL_TP3V3	I	Touch Panel & EEPROM I2C Touch
40	VCC_3V3_SYS	I	Power enable pin for PCB-L0136

Electrical – Absolute Maximum

Item	Symbol	Va	lue	Unit	
		Min.	Max.		
Touch Panel and LCD185	VDD	-0.3	3.47	V	
PCB-L0136 Voltage	VBUS	-0.3	6.0	V	
Power-Up Indicator Voltage	VSYS	-0.3	6.0	V	
Backlight Voltage – Anode	V_{BL_A}	-0.3	50	V	
Backlight Voltage – Cathode	V_{BL_C}	-0.3	50	V	
Signal names ending in 3V3	V_{IO3V3}	-0.3	VDD+0.3	V	
Signal names ending in 1V8	V _{IO1V8}	-0.3	2.0	V	
All other signals (LVDS)	V _{IOLVDS}	-0.3	VDD+0.3	V	

Electrical – Typical

NOTE: Positive currents are *into* PCB-L0136 through PCB-L0152 except for VBUS which is to be provided directly to PCB-L0136.

NOTE: LVDS Differential Mode voltages are measured relative to the negative signal within each LVDS pair. All other voltages are measured relative to GND.

Item	Symbol	Min.	Тур.	Max.	Unit
Touch Panel and LCD185 Voltage	VDD	3.0		3.4	V
Touch Panel and LCD185 Current	I_{VDD}	-0.36		n/a	А
PCB-L0136 Voltage	VBUS	4.5		5.5	V
PCB-L0136 Current	I_{VBUS}	n/a	1	3.0	Α
Power-Up Indicator Voltage	VSYS	3.23	5	5.5	V
Power-Up Indicator Current	I _{VSYS}	0		0.02	Α
Backlight Voltage – Anode (100% PWM)	V_{BL_A}	25		30	V
Backlight Voltage – Cathode (100% PWM)	V_{BL_C}	0.7		5.4	V
Backlight PWM Frequency	f _{BL_PWM_3V3}	0.1		20	kHz
Backlight PWM ON Time	t _{ON(BL_PWM_3V3)}	0.22		n/a	us
Backlight Current (each connector)	I _{VBL_C}	0.04		0.2	Α
Signal names ending in 3V3	V _{IL_3V3}	-0.3		0.4	V
	V _{IH_3V3}	2.0		VDD+0.3	V
	V _{IL_1V8}	-0.3		0.5	V
Signal names ending in 1V8	V _{IH_1V8}	1.5		2.0	V
	V _{OL_1V8}	0.0		0.25	V
	V _{OH_1V8}	1.4		1.9	V
All other signals (LVDS Common Mode)	V_{CM_LVDS}	1.0		1.4	V
All other signals (LVDS Differential Mode)	$V_{\text{IL_LVDS}}$	-0.3		-0.1	V
J ,	V _{IH_LVDS}	0.1		0.3	V

Power-Up

In preparation for power-up, the following electromechanical connections are required.

TI Starter Kit

- Ideally PCB-L0136 is attached to the back of LCD185, allowing the following three connections to be consistently maintained with good mechanical stability. NOTE: The Backlight connectors are through-hole. To prevent any possible shorting of these connectors to the LCD185 metal housing, it is recommended that the through-hole pins be covered by tape that offers notable insulation. Additionally, it is recommended to use a double-sided tape (at least 1.0mm thick) to adhere PCB-L0136 to LCD185.
- 2. Touch I2C connector on PCB-L0136 is connected to LCD185 flex.
- 3. LCD185 connector on PCB-L0136 is connected to LCD185 using provided 45-pin FFC.
- Backlight connectors on PCB-L0136 are connected to LCD185 cabled connectors.
- 5. S200 on PCB-L0136 is connected to S100 on PCB-L0152 using provided 40-pin FFC.
- 6. S101 on PCB-L0152 is connected to TI Starter Kit with the other provided 40-pin FFC. Also, it is required that the boot code ensures that *neither* MSTR_RST_3V3 or TP_INT_1V8 to PCB-L0136 is configured as Output-High during and immediately after boot. Configuring them as Input is preferred.

With those connections and boot code configuration, the following power-up sequence is recommended.

- 1. Apply VBUS to PCB-L0136 through PCB-L0152. Adjust SW500 if needed based on VBUS supply current rating.
- 2. Apply VSYS to PCB-L0136 through PCB-L0152 by applying power to the TI board and initiating the boot process.
- 3. After 10~50ms, TI board changes MSTR_RST_3V3 to Output-High and starts the LVDS interface.
- 4. After 200ms, TI board changes BL_EN_3V3 to Output-High.
- 5. TI board controls BL_PWM_3V3 as needed. Options are either static high (for full brightness) or PWM with required frequency and minimum on time (for dimming).

BeaglePlay or Phytec PhyCore platforms

PCB-L0152 is not used for this connection.

- Ideally PCB-L0136 is attached to the back of LCD185, allowing the following three connections to be
 consistently maintained with good mechanical stability. NOTE: The Backlight connectors are
 through-hole. To prevent any possible shorting of these connectors to the LCD185 metal housing, it
 is recommended that the through-hole pins be covered by tape that offers notable insulation.
 Additionally, it is recommended to use a double-sided tape (at least 1.0mm thick) to adhere PCBL0136 to LCD185.
- 2. Touch I2C connector on PCB-L0136 is connected to LCD185 flex.
- 3. LCD185 connector on PCB-L0136 is connected to LCD185 using a flex.
- 4. Backlight connectors on PCB-L0136 are connected to LCD185 cabled connectors.
- 5. Beagle connector on PCB-L0136 is connected to the Beagle board using a flex.

Also, it is required that the Beagle boot code ensures that *neither* MSTR_RST_3V3 or TP_INT_1V8 to PCB-L0136 is configured as Output-High during and immediately after boot. Configuring them as Input is preferred.

With those connections and boot code configuration, the following power-up sequence is recommended.

- 1. Apply VBUS to PCB-L0136. Adjust SW500 if needed based on VBUS supply current rating.
- 2. Apply VSYS to PCB-L0136 by applying power to the Beagle board and initiating the boot process.
- 3. After 10~50ms, Beagle board changes MSTR_RST_3V3 to Output-High and starts the LVDS interface.
- 4. After 200ms, Beagle board changes BL_EN_3V3 to Output-High.
- 5. Beagle board controls BL_PWM_3V3 as needed. Options are either static high (for full brightness) or PWM with required frequency and minimum on time (for dimming).

Interfaces

LCD185 requires the following two industry standard interfaces. Specific requirements are summarized in this section.

I2C

This interface is required to read touch panel data and read/write to a EEPROM. It supports the Fast-mode implementation of the I2C standard.

- I2C 8-bit slave address of the Touch Panel is 0xBA/0xBB.
- I2C 8-bit slave address of the EEPROM is 0xAE/0xAF.

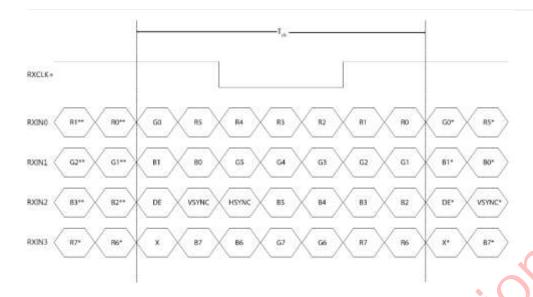
LVDS

This interface is required to send video data. The required timing parameters are summarized in the following table.

Item	Symbol	Min	Тур	Max	Unit
CLK Frequency	f _{CLK}	74.5	77.56	85	MHz
CLK Period	t _{CLK}	11.76	12.89	13.42	ns
Horizontal Display Area ¹	t _{HD}		960		t _{CLK}
HSYNC Period	t _H	989	1040	1248	t _{CLK}
Horizontal Blank	t _{HB}	29	80	288	t _{CLK}
HSYNC Pulse Width	t _{HP}	2	10	255	t _{CLK}
HSYNC Back Porch	t _{HBP}	3	6	255	t _{CLK}
HSYNC Front Porch	t _{HFP}	24	64	260	t _{CLK}
Vertical Display Area	t _{VD}	1	1200		t _H
VSYNC Period	t _V	1243	1243	1560	t _H
Vertical Blank	t _{VB}	43	43	360	t _H
VSYNC Pulse Width	t _{VP}	4	4	20	t _H
VSYNC Back Porch	t _{VBP}	20	20	255	t _H
VSYNC Front Porch	t _{VFP}	19	19	260	t _H
Refresh Frequency	f _{REFRESH}		60		Hz

⁽¹⁾ Two pixels are sent in parallel each t_{CLK} period. The number of active horizontal pixels is still 1920 as specified in optical characteristics, and all other Horizontal specs are double their listed values when counted as pixels.

Additionally, the LVDS interface uses the VESA (Format 2) RGB bit mapping, which is summarized in the following figure. This shows a single lane but LCD185 requires two lanes, which are identified as Odd (OLDIO_A0~3) and Even (OLDIO_A4~7).



Bring-Up Support

To assist with any bring-up concerns, PCB-L0136 supports the following.

- LVDS signals are accessible using test points placed near the LCD185 connector.
- Input voltages (VBUS, VSYS) and generated voltages (VDD=3V3, 1V8) are accessible using test points. Nearby GND test points are also provided.
- Backlight boost signals (VBOOST_IN, VBOOST_OUT, Switch Node, etc.) are accessible using various test points.
- LCB-L0152 additionally has test points for Backlight Enable (BL_EN_3V3, TP101) and Backlight PWM (BL_PWM_3V3, TP100)

Appendix 1: Mechanical Drawing

