SPEC

Spec No.	TQ3C-8EAF0-E1YAG35-01
Date	October 15, 2014

TYPE: TCG104VGLABANN-AN30

<10.4 inch VGA transmissive color TFT with LED backlight and constant current circuit for LED backlight>

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KYOCERA DISPLAY CORPORATION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Designed by:	Engineering de	pt.	Confirmed by: QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved	
July 25, 2013	X. Janimuka	y Yamazaki	W. Yano	O. Sato	1-Hamars	

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Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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Revision record

Revision record								
	Date	Design				Confirmed by	Confirmed by : QA dept.	
Date		Prep	ared	Checked	Approved	Checked	Approved	
Octob	per 15, 2014	X. Ja	nimuta	y Yamazaki	W. Yano	O. Sato	1- Hamars	
Rev.No.	Date	Page			Description			
01	Oct 15,2014	4	5-2. C	onstant current	t circuit for LE	D Backlight		
			chang	e Operating life	e time Typ70,0	00h →100,000	n	



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1. Application

This document defines the specification of TCG104VGLABANN-AN30. (RoHS Compliant)

2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Anti-Glare treatment

Additional circuit : Timing controller, Power supply (3.3V input)

With constant current circuit for LED Backlight (12V)

3. Mechanical specifications

Item	Specification	
Outline dimensions 1)	240.7(W)×(180.2)(H)×9(D)	mm
Active area	211.2(W)×158.4(H) (26.4cm/10.4 inch(Diagonal))	mm
Dot format	640×(R,G,B)(W)×480(H)	
Dot pitch	t pitch 0.11(W)×0.33(H)	
Base color 2)	Normally White	-
Mass	470	g

- 1) Projection not included. Please refer to outline for details.
- 2) Due to the characteristics of the LCD material, the color varies with environmental temperature.



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4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltage(+3.3V)		$V_{ m DD}$	0	4.0	V
Supply voltage(+12V)		$V_{\rm IN}$	-0.3	14.0	V
1)		V_{I1}	-0.3	6.0	V
Input signal voltage	2)	V_{I2}	-0.3	$V_{\rm IN}$	V

1) Input signal: CK, R0~R5, G0~G5, B0~B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D

2) Input signal: BLBRT, BLEN

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	Тор	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	2)	Tsto	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	3)	${ m H}_{ m OP}$	10	4)	%RH
Storage humidity	3)	Нѕто	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Temp. = -30°C < 48h, Temp. = 80°C < 168h Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)
- 3) Non-condensing
- 4) Temp. ≤ 40°C, 85%RH Max. Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

5)

Frequency	10∼55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

6) Acceleration: 490 m/s², Pulse width: 11 ms 3 times in each direction: ±X, ±Y, ±Z EIAJ ED-2531



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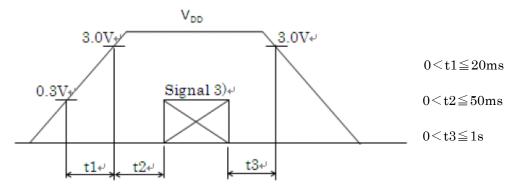
5. Electrical characteristics

5-1. LCD

Temp. = $-20 \sim 70$ °C

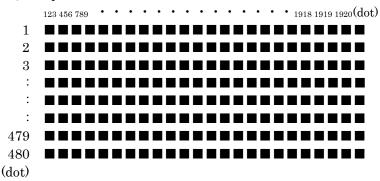
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption	I_{DD}	2)	_	150	185	mA
Permissive input ripple voltage	V_{RP}	-	-	_	_	100
I	V_{IL}	"Low" level	0	_	0.8	V
Input signal voltage 3)	V_{IH}	"High" level	2.0	_	$V_{ m DD}$	V

1) V_{DD}-turn-on conditions



2) Display pattern:

$$V_{DD} = 3.3V$$
, Temp. = 25°C



3) Input signal : CK, R0 \sim R5, G0 \sim G5, B0 \sim B5, Hsync, Vsync, ENAB, R/L, U/D



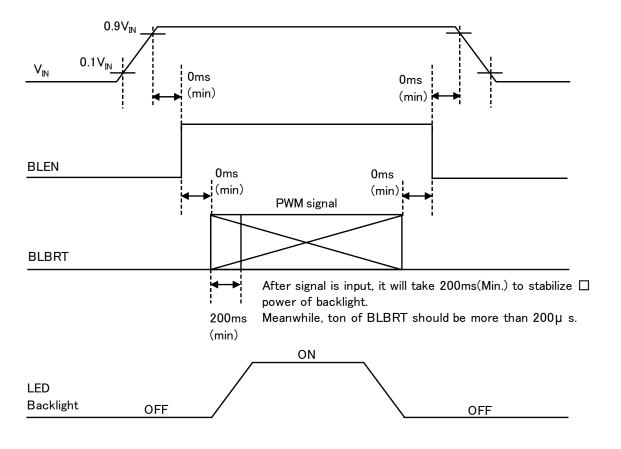
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5-2. Constant current circuit for LED Backlight

Temp. = $-20 \sim 70$ °C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	$V_{\rm IN}$	-	10.8	12.0	13.2	V
Current consumption	$I_{\rm IN}$	2)	-	(375)	(490)	mA
Permissive input ripple voltage	$V_{\mathrm{RP_BL}}$	$V_{IN} = 12.0V$	i	-	100	mVp-p
DI DDT Innut signal veltage	$V_{\rm IL_BLBRT}$	"Low" level	0	1	0.8	V
BLBRT Input signal voltage	V _{IH_BLBRT}	"High" level	2.3	-	V_{IN}	V
BLBRT Input pull-down resistance	R _{IN_BLBRT}	•	100	300	500	$k\Omega$
DI EN Lange signal reality as	$V_{\rm IL_BLEN}$	"Low" level	0	-	0.8	V
BLEN Input signal voltage	V _{IH_BLEN}	"High" level	2.3	-	V_{IN}	V
BLEN Input pull-down resistance	R _{IN_BLEN}	•	100	300	500	$k\Omega$
PWM Frequency 3)	f_{PWM}	-	200	-	10k	$_{ m Hz}$
		$ m f_{PWM}$ =200Hz	1	-	100	%
PWM Duty ratio 3)	$\mathrm{D}_{\mathrm{PWM}}$	f _{PWM} =2kHz	10		100	%
		f _{PWM} =10kHz	50		100	%
Operating life time 4), 5)	Т	Temp.=25°C	-	100,000	-	h

1) V_{IN}-turn-on conditions

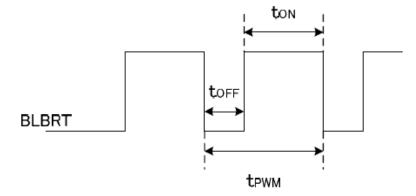


2) $V_{IN} = 12V$, Temp. = 25°C, $D_{PWM} = 100\%$



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3) PWM Timing Diagram



ton, toff $\geq 50 \,\mu$ s.

In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

- 4) When brightness decrease 50% of minimum brightness.

 The average life of a LED will decrease when the LCD is operating at higher temperatures.
- 5) Life time is estimated data.(Condition : IF=60mA, Ta=25 $^{\circ}$ C in chamber).



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6. Optical characteristics

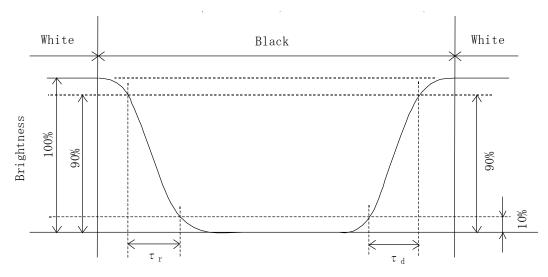
Measuring spot = ϕ 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
D	Rise	Τr	$\theta = \phi = 0$ °	_	10	_	ms	
Response time	Down	τd	$\theta = \phi = 0$ °	_	20	_	ms	
T7: . 1		θ upper		_	60	_	J	
Viewing angle View direction	range	θ lower	CD > 10	_	70	_	deg	
: 6 o'cloc		ϕ LEFT	CR≧10	_	70	_	J	
(Gray inversion)		ф RIGHT		_	70	_	deg	
Contrast ratio		CR	$\theta = \phi = 0$ °	350	500	_	_	
Brightness	Brightness		IF=60mA/Line	560	800	_	cd/m²	
	Red	X	$\theta = \phi = 0$ °	0.555	0.605	0.655		
		У		0.285	0.335	0.385		
	a	X	$\theta = \phi = 0^{\circ}$	0.290	0.340	0.390		
Chromaticity	Green	У	$\theta - \phi = 0$	0.490	0.540	0.590		
coordinates	Dl	X	$\theta = \phi = 0^{\circ}$	0.105	0.155	0.205	_	
	Blue	У	σ – φ –υ	0.055	0.105	0.155		
	Wilsian	X	0 - 1 -00	0.255	0.305	0.355		
	White	У	$\theta = \phi = 0^{\circ}$	0.265	0.315	0.365		

6-1. Definition of contrast ratio

 $CR(Contrast ratio) = \frac{Brightness with all pixels "White"}{Brightness with all pixels "Black"}$

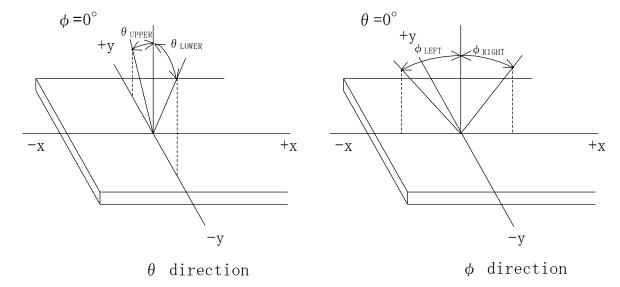
6-2. Definition of response time



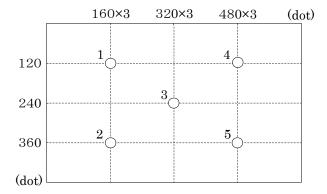


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6-3. Definition of viewing angle



6-4. Brightness measuring points



- 1) Rating is defined as the white brightness at center of display screen(3).
- 2) 5 minutes after LED is turned on. (Ambient Temp.= 25° C)

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7. Interface signals

7-1. LCD

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	GND	GND	-	
3	GND	GND	-	
4	BLBRT	PWM signal(Brightness adjustment)	-	
5	BLEN	ON/OFF terminal voltage		
6	$V_{\rm IN}$	+12V power supply	-	
7	$V_{\rm IN}$	+12V power supply	-	
8	Vin	+12V power supply	-	
9	NC	No connect	-	
10	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	I	2)
11	R/L	Horizontal display mode select signal L: Normal, H: Left / Right reverse mode	I	2)
12	$V_{ m DD}$	3.3V power supply	-	
13	$V_{ m DD}$	3.3V power supply	-	
14	ENAB	Signal to settle the horizontal display position (positive)	I	1)
15	GND	GND	-	
16	B5	BLUE data signal (MSB)	I	
17	B4	BLUE data signal	I	
18	В3	BLUE data signal	I	
19	B2	BLUE data signal	I	
20	B1	BLUE data signal	I	
21	В0	BLUE data signal (LSB)	I	
22	GND	GND	-	
23	G5	GREEN data signal (MSB)	I	
24	G4	GREEN data signal	I	
25	G3	GREEN data signal	I	
26	G2	GREEN data signal	I	
27	G1	GREEN data signal	I	
28	G0	GREEN data signal (LSB)	I	
29	GND	GND	-	
30	R5	RED data signal (MSB)	I	
31	R4	RED data signal	I	
32	R3	RED data signal	I	
33	R2	RED data signal	I	
34	R1	RED data signal	I	
35	R0	RED data signal (LSB)	I	
36	GND	GND	-	
37	$V_{ m SYNC}$	Vertical synchronous signal (negative)	I	
38	Hsync	Horizontal synchronous signal (negative)	I	
39	CK	Clock signal for sampling each data signal	I	
40	GND	GND	-	

LCD connector : 04 6240 040 023 846+ (KYOCERA Connector Products)

Recommended matching FFC or FPC $\,:\,\,$ 0.5mm pitch



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The horizontal display start timing is settled in accordance with a rising timing of ENAB signal.
 In case ENAB is fixed "Low", the horizontal start timing is determined.
 Don't keep ENAB "High" during operation.

2)



R/L = LU/D = H



R/L = HU/D = H



$$R/L = L$$
$$U/D = L$$



$$R/L = H$$

 $U/D = L$

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8. Input timing characteristics

8-1. Timing characteristics

	Item	Symbol	Min	Тур	Max	Unit	Note
CI I	Frequency	1/Te	22.66	25.18	27.69	MHz	
Clock	Duty ratio	Tch/Tc	40	50	60	%	
D. /	Set up time	Tds	5	_	_	ns	
Data	Hold time	Tdh	10	_	_	ns	
	G 1	mii	30.0	31.8	_	μ s	
Horizontal sync. signal	Cycle	TH	770	800	850	clock	
Signai	Pulse width	ТНр	2	96	200	clock	
Vertical sync.	Cycle	TV	515	525	560	line	
signal	Pulse width	TVp	2	_	34	line	
Horizontal displa	ny period	THd		640		clock	
Hsync,-Clock phase difference		ТНс	10	_	Tc-10	ns	
Hsync-Vsync. phase difference		TVh	2Tc	_	TH-THp-1	ns	
Vertical sync. signal start position		TVs	34		•	line	
Vertical display p	period	TVd	480			line	

- 1) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.
- 2) CK count of each Horizontal Scanning Time should be always the same. Vertical invalid data period should be "n" X "Horizontal Scanning Time". (n: integer) Frame period should be always the same.

8-2. Horizontal display position

Item		Symbol	Min	Тур	Max	Unit	Note
	Set up time	Tes	5	_	Tc-10	ns	
Enable signal	Pulse width	Тер	2	640	TH-10	clock	
H _{SYNC} – Enable signal phase difference		The	44	_	TH-664	clock	

- 1) The horizontal display position is determined by ENAB signal.
- 2) When ENAB is fixed at "Low", the display starts from the data of C104(clock) as shown in 8-5.

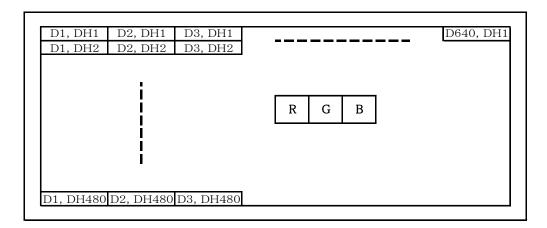
8-3. Vertical display position

- 1) The vertical display position (TVs) is 34th line.
- 2) ENAB signal is independent of vertical display position.

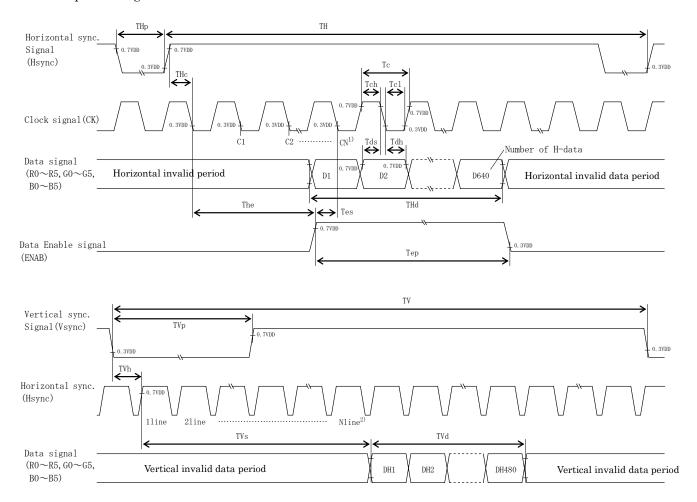


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8-4. Input Data Signals and Display position on the screen



8-5. Input timing characteristics



- 1) When ENAB is fixed at "Low", the display starts from the data of C104(Clock).
- 2) The vertical display position(TVs) is fixed at 34th line.



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9. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2013	2014	2015	2016	2017	2018
Code	3	4	5	6	7	8

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

10. Warranty

10-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

10-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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11. Precautions for use

11-1. Installation of the LCD

- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) Please ground either of the mounting (screw) holes located at each corner of an LCD, in order to stabilize brightness and display quality.
- 3) The LCD shall be installed so that there is no pressure on the LSI chips.
- 4) The LCD shall be installed flat, without twisting or bending.
- 5) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

11-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

11-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Please select the best display pattern based on your evaluation because flicker, lines or nonuniformity or unevenness can be visible depending on display patterns.

11-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified.

 Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

11-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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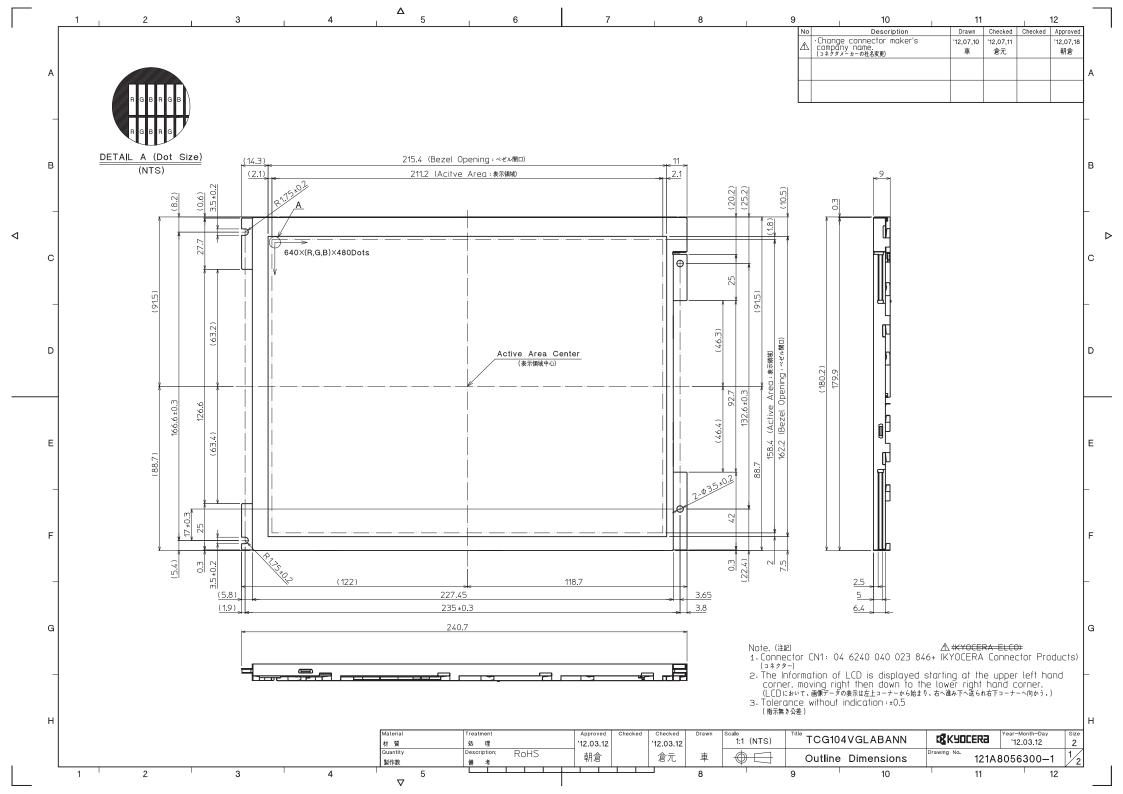
12. Reliability test data

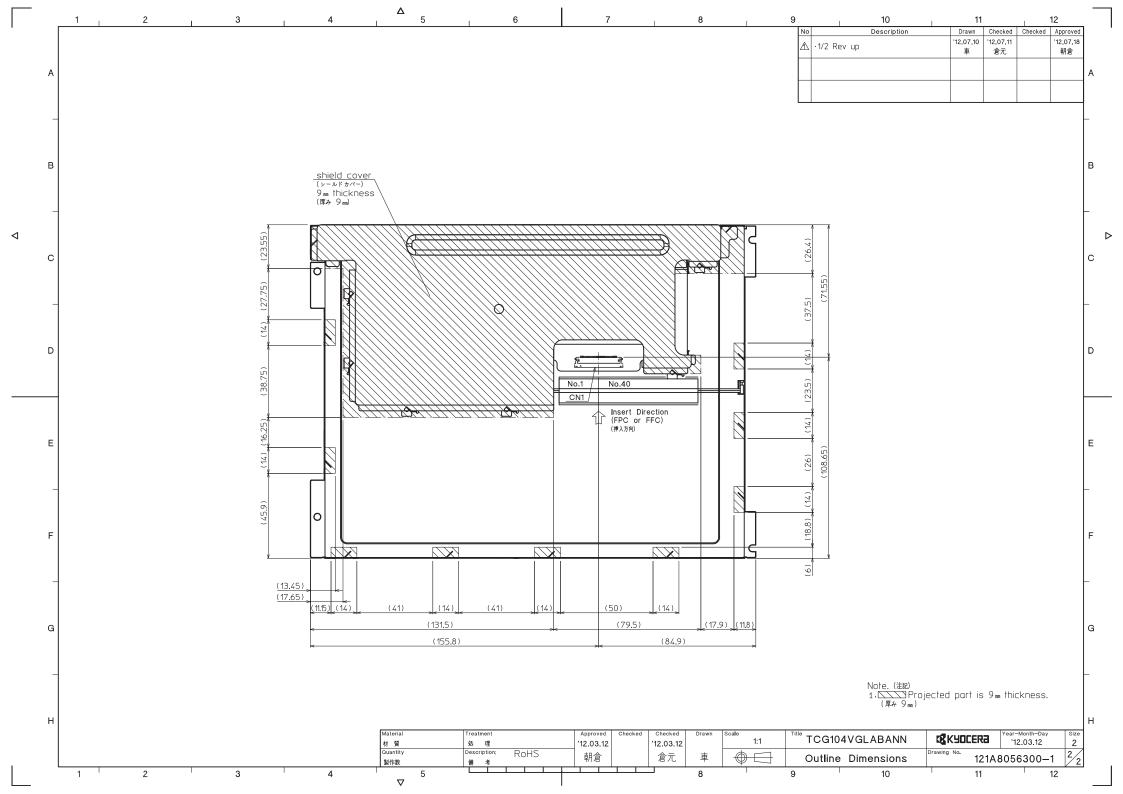
Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect: No defect: No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	No defectNo defectNo defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

 The reliability test is conducted only to examine the LCD's capability.







ı	Spec No.	TQ3C-8EAF0-E2YAG35-00
	Date	July 25, 2013

KYOCERA INSPECTION STANDARD

TYPE: TCG104VGLABANN-AN30

KYOCERA DISPLAY CORPORATION

Original	Designed by : Engineering dept.			Confirmed by : QA dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
July 25, 2013	X. Mori	Y. Yamazaki	M.FijiTani	O. Sato	T. OKamoto



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Revision record

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Visuals specification

1) Note

1) Note	1					
		Note				
General	 Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. This inspection standard about the image quality shall be applied to any defect within 					
	the active area and shall not be applicable to outside of the area.					
	3. Inspecti	on conditions ance	: 500 Lux min. : 300 mm.			
	Inspect	ion distance				
	Temper	rature	: 25 ± 5℃			
	Direction	on	: Directly above			
Definition of inspection item	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter. Don't count dot: If the dot is not visible through the			
			filter. RGBRGBRGB RGBRGB RGBRGB RGBRGBRGB			
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "White" data sent to the screen.			
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects. RGBRGBRGB			
			R G B R G B R G B			
	External inspection	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixels "Black" or "White") and non operating.			
		Appearance inspection	Does not satisfy the value at the spec.			
	Definition of size	Definition of circle size Definition of linear size				



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2) Standard

2) Standard								
Classification		Inspection item		Judgement standard				
Defect	Dot	Bright dot defect		Acceptable number : 4				
(in LCD	defect	-		Bright dot spacing : 5 mm c			or more	
glass)		Black dot	defect	Acceptable number : 5				
							or more	
		2 dot join Bright dot		Adalla a dec				
		defect		Acceptable number :			: 2	
			Black dot defect	Acceptable number		: 3		
		3 or more dots join		Acceptable number : 0				
		Total dot d	lefects	Acceptable number : 5 Max			X	
	Others	White dot,	Dark dot					
		(Circle)		Size (mm) Ad		Ac	ceptable number	
	(Oncie)			d ≤ 0.2			(Neglected)	
				$0.2 < d \le 0.4$		5		
				$0.4 < d \le 0.5$		3		
				0.5 < d		0		
External	inspection	Polarizer (Scratch)					
(Defect or	_	1 olalizei (octateli)		Width (mm)	Length (mm) A		Acceptable number	
Polarizer				$W \leq 0.1$			(Neglected)	
between I				$0.1 < W \le 0.3$	L	≦ 5.0	(Neglected)	
and LCD				$0.1 < W \ge 0.3$	5.0 < L		0	
	8-0000			0.3 < W		0		
		Polarizer (Bubble)					
				Size (mm)		Acceptable number		
				d ≦ 0.2		(Neglected)		
				$0.2 < d \le 0.3$		5		
				$0.3 < d \le 0.5$		3		
				0.5 < d		0		
		Foreign pa	ırticle					
		(Circular shape)		Size (mm)		Acceptable number		
				d ≤ 0.2		(Neglected)		
				$0.2 < d \le 0.4$		5		
				$0.4 < d \le 0.5$		3		
				0.5 < d		0		
		Foreign particle (Linear shape) Scratch						
				Width (mm) Length		n (mm) Acceptable number		
				W ≤ 0.03		- (Neglecte		
						≤ 2.0	(Neglected)	
				$0.03 < W \le 0.1$	2.0 < L		3	
					4.0 < L		0	
				0.1 < W	_		(According to	
							circular shape)	

