## 1. General description

Low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode in a very small SOD323 (SC-76) Surface-Mounted Device (SMD) plastic package designed to protect one signal line from the damage caused by ESD and other transients.

## 2. Features and benefits

- · Bidirectional ESD protection of one line
- Low diode capacitance: C<sub>d</sub> = 2.9 pF
- Ultra low leakage current: I<sub>RM</sub> = 5 nA
- · ESD protection of up to 10 kV
- IEC 61000-4-2, level 4 (ESD)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

- Computers and peripherals
- · Audio and video equipment
- · Cellular handsets and accessories
- 10/100/1000 Ethernet
- Local Area Network (LAN) equipment
- Communication systems
- Portable electronics
- · SIM card protection
- High-speed data lines

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C	-	-	5	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	2.9	3.5	pF
		f = 1 MHz; V <sub>R</sub> = 5 V; T <sub>amb</sub> = 25 °C	-	1.9	-	pF



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	1 2	[54 51]
2	K2	cathode (diode 2)		K1 K2
			SOD323	sym045

# 6. Ordering information

### **Table 3. Ordering information**

3					
Type number	Package		Version		
	Name	Description	Version		
PESD5V0U1BA-Q	SOD323	plastic, surface-mounted package; 2 leads; 1.3 mm pitch; 1.7 mm x 1.25 mm x 0.95 mm body	SOD323		

# 7. Marking

### Table 4. Marking codes

Type number	Marking code
PESD5V0U1BA-Q	AA

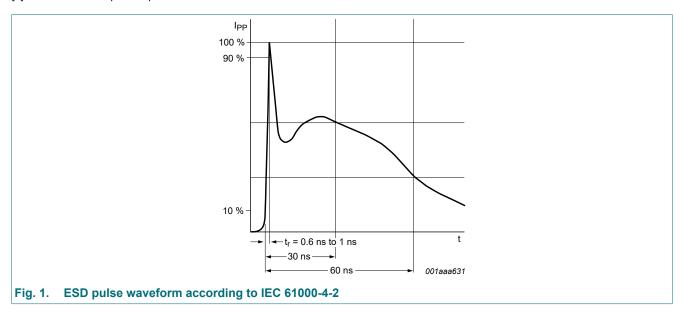
# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximu	ım ratings					
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2 (contact discharge)	[1] [2]	-	10	kV
	voltage	MIL-STD-883 (human body model)		-	8	kV

- [1] Device stressed with ten non-repetitive ESD pulses.
- [2] Measured from pin 1 to pin 2.



## 9. Characteristics

#### **Table 6. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C	-	-	5	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C	5.5	7	9.5	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 5 V; T <sub>amb</sub> = 25 °C	-	5	100	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	2.9	3.5	pF
		f = 1 MHz; V <sub>R</sub> = 5 V; T <sub>amb</sub> = 25 °C	-	1.9	-	pF
R <sub>diff</sub>	differential resistance	I <sub>R</sub> = 1 mA; T <sub>amb</sub> = 25 °C	-	-	100	Ω

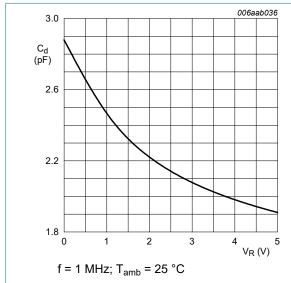


Fig. 2. Diode capacitance as a function of reverse voltage; typical values

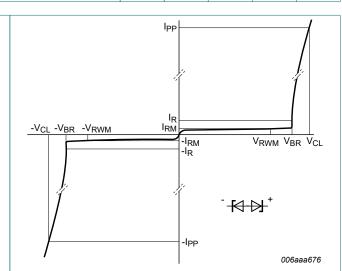
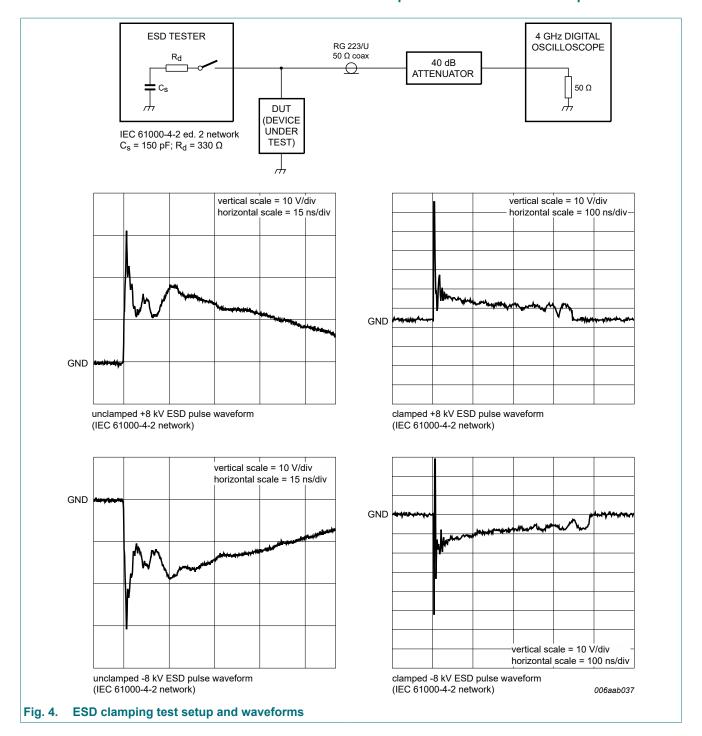
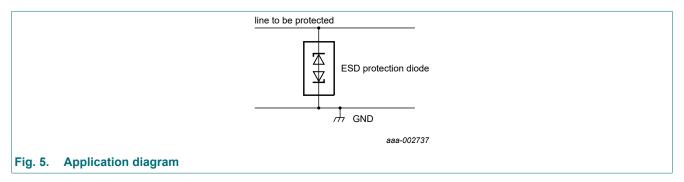


Fig. 3. V-I characteristics for a bidirectional ESD protection diode



## 10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

### 11. Test information

#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline

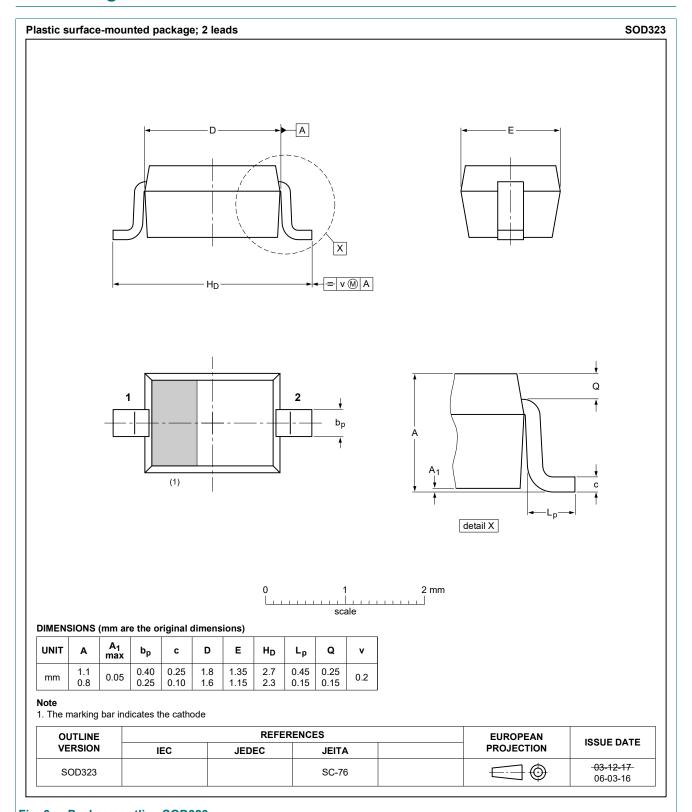
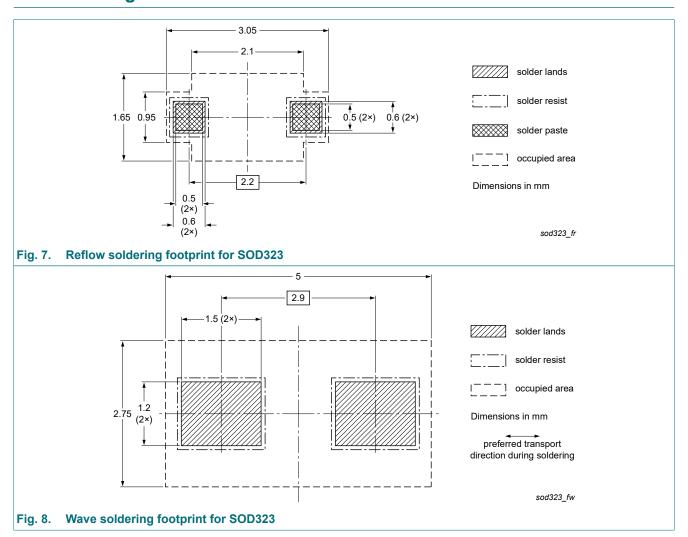


Fig. 6. Package outline SOD323

# 13. Soldering



# 14. Revision history

### **Table 7. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD5V0U1BA-Q v.1	20220503	Product data sheet	-	-

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <a href="https://www.nexperia.com">https://www.nexperia.com</a>.

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#### Low capacitance bidirectional ESD protection diode

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