

Radiation-Tolerant Isolated DC-to-DC Converter

LE50-28 Single Series



Introduction

The LE50-28 is an isolated DC-to-DC converter capable of delivering up to 50W of output power in a small size design. The LE family provides a radiation-tolerant option with top class TID and SEE performance for space and military applications. With forward converter topology and a patented magnetic feedback, the LE50-28 is optimized for applications where isolated DC voltage conversion is required. The discrete surface mount design facilitates customization with reasonable lead time and modest NRE cost.

To achieve MIL-STD-461 EMI compliance, an external filter is required. Off-the-shelf filters, such as Microchip's SF100-28-28S, are available.

As a non-hybrid space-grade DC-DC power converter module in the market, the LE50-28 series excels in its robustness in applications with 1.00×10^6 hours of MTBF.

The LE50-28 is available in a 3.055" x 2.055" x 0.5" package.

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1. Benefits and Features

- Up to 50W Output Power (Parallel Up to Four for Higher Power)
- 22 VDC to 38 VDC Input Range
- Five Output Configurations Available

Table 1-1. Output Configurations

Output	Base Part Number
3.3V	LE50-28-3R3S
5V	LE50-28-5S
12V	LE50-28-12S
15V	LE50-28-15S
28V	LE50-28-28S

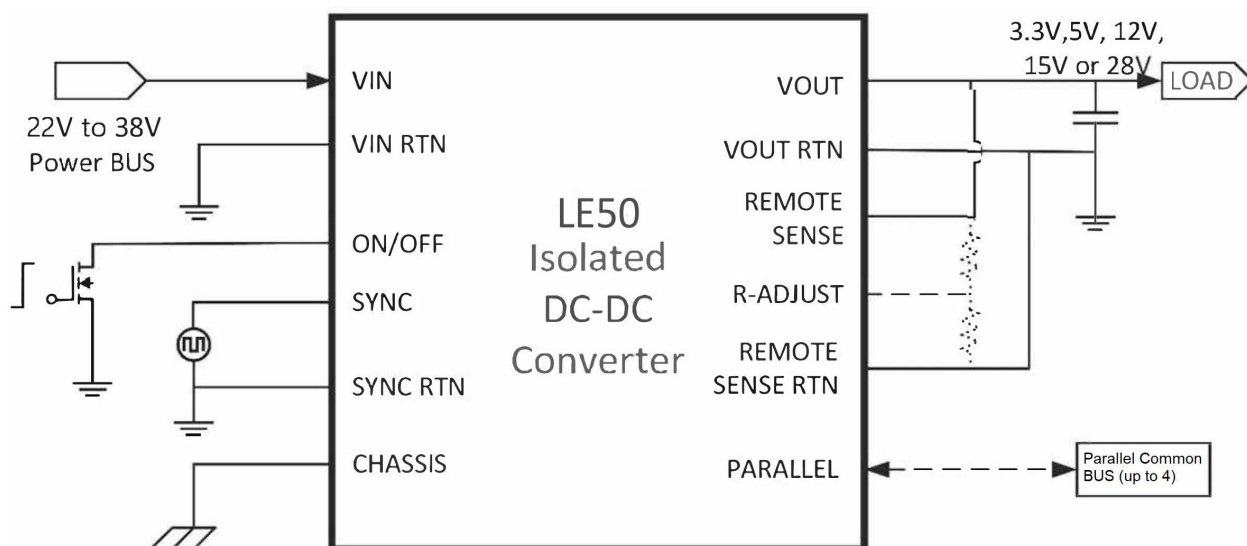
- Up to 80% Efficiency @ Full Load
- <1% Output Ripple
- Forward Topology
- Patented Magnetic Feedback
- Adjustable Output with Remote Adjust
- Inhibit Pin for Electrical ON/OFF
- Capable of Paralleling Up to Four Identical Units
- Isolated Synchronization Input
- Low Mass 120g
- Flight Proven Technology with $>1 \times 10^6$ Hours of MTBF
- Product is Classified as EAR99
- Customization of Input/Output Voltages Available Upon Request

Radiation Performance

- TID >50 krad (Si) and 30 krad (Si) ELDRS (<10 mrad/s) per MIL-STD-883 Method 1019
- SEE (SEGR, SEB, SET, SEL) Immunity >37 MeV·cm²/mg

2. Typical Application Circuit

Figure 2-1. LE50-28 Single Typical Application Circuit



3. Absolute Maximum Ratings

Table 3-1. Absolute Maximum Ratings

Rating	Value
V_{IN} range	-0.5 VDC to 60 VDC
Output power	50W
Lead temperature	300 °C for 10s
Operating temperature	-55 °C to 100 °C
Storage temperature	-55 °C to 125 °C
Shock	1500 gpk, 0.5 ms, ½ sine
Constant acceleration	50g
Random vibration	24.06 grms, 50 Hz to 2000 Hz

4. Electrical Parameters

This section shows the electrical parameters of the LE50-28 Single Series device under the following conditions unless otherwise specified:

Table 4-1. Electrical Parameters

Parameter	Output	Conditions	Min	Nom	Max	Units
Input voltage						
(V _{IN})	—	Note 2	22	28	38	V
Output voltage						
(V _{OUT})	28V	I _{OUT} = 100% rated load	27.73	28.00	28.27	V
	15V		14.85	15.00	15.15	
	12V		11.88	12.00	12.12	
	5V		5.05	5.10	5.15	
	3.3V		3.27	3.30	3.33	
Output Voltage Adjust						
(V _{ADJ})	—	—	5	—	—	%
Output power						
(P _{OUT})	28V	Note 12 In all cases Output power must be kept within P _{out} rating.	0	—	50	W
	15V				50	
	12V				50	
	5V				50	
	3.3V				33	
Output current						
(I _{OUT})	28V	—	0	—	1.8	A
	15V				3.3	
	12V				4.2	
	5V				10	
	3.3V				10	

.....continued

Parameter	Output	Conditions	Min	Nom	Max	Units
Line regulation						
(VR _{LINE})	28V	V _{IN} = 22V, 28V, 38V, I _{OUT} = 100% rated Note 11	-120	—	120	mV
	15V		-60		60	
	12V		-50		50	
	5V		-20		20	
	3.3V		-20		20	
Load regulation						
(VR _{LOAD})	28V	V _{IN} = 28V, I _{OUT} = 10%, 100% rated Note 10	-280	—	280	mV
	15V		-150		150	
	12V		-120		120	
	5V		-50		50	
	3.3V		-50		50	
Input current						
(I _{IN})	—	I _{OUT} =0, pin3 open	—	100	150	mA
		Pin 3 shorted to pin 2	—	2	5	
Output ripple						
(V _{RIP})	28V	V _{IN} = 28V I _{OUT} = 100% rated, Note 3	—	100	280	mV p-p
	15V		—	75	150	
	12V		—	60	120	
	5V		—	25	50	
	3.3V		—	25	50	
Switching frequency						
(FS)	—	Sync input (pin 4) open	200	220	240	kHz
Efficiency						
(EFF)	28V	I _{OUT} = 100% rated load	78	80	—	%
	15V		77	80	—	
	12V		76	80	—	
	5V		71	79	—	
	3.3V		62	77	—	

.....continued

Parameter	Output	Conditions	Min	Nom	Max	Units
Inhibit input						
Inhibit input: ON Threshold	—	Note 1	3.3	—	—	V
Inhibit input: OFF drive current (sink)	—	Note 1	1000	—	—	μA
Inhibit input: OFF Threshold	—	Note 1	—	—	1.5	V
Current limit point						
(% rated output)	—	When V_{OUT} = 90% of nominal set point	105	—	145	%
Synchronization						
frequency range	—	The external clock on sync input (pin 4)	500	—	600	kHz
Synchronization pulse-high level	—	Note 1	4.0	—	10.0	V
Synchronization pulse-low level	—	Note 1	-0.5	—	0.5	V
Synchronization pulse-transition rate	—	Note 1	200	—	—	V/μs
Synchronization pulse-duty cycle	—	Note 1	10	—	80	%
Power dissipation, load fault						
(PD)	—	Short circuit, overload Note 5	—	—	24	W
Output response to step load changes						
(V _{TLD})	28V	(50% to/from 100%) rated load Note 6	-2200	—	2200	mV peak
	15V		-1200		1200	
	12V		-900		900	
	5V		-500		500	
	3.3V		-300		300	

.....continued

Parameter	Output	Conditions	Min	Nom	Max	Units
Recovery time, step load changes						
(T _{TLD})	—	(50% to/from 100%) rated load Notes 6, 7	—	200	2000	μs
Output response to step line changes						
(V _{TLN})	28V	22V to/from 38V I _{OUT} = 100% rated load Note 8	-1000	—	1000	mV peak
	15V		-600		600	
	12V		-480		480	
	5V		-300		300	
	3.3V		-300		300	
Recovery time, step line changes						
(T _{TLN})	—	22V to/from 38V I _{OUT} = 100% rated load Notes 7, 8	—	200	2000	μs
Turn-on response: overshoot						
(V _{OS}) (main)	28V	(0% to 100%) rated load Notes 4, 9	—	—	2800	mV
	15V		—	—	1500	
	12V		—	—	1200	
	5V		—	—	500	
	3.3V		—	—	500	
Turn-on response: turn-on delay						
(T _{DLY})	—	Note 9	0.1	—	10	ms
Capacitive load						
(CL)	28V	Note 4	—	—	200	μF
	15V		—	—	350	
	12V		—	—	450	
	5V		—	—	1000	
	3.3V		—	—	1000	

.....continued

Parameter	Output	Conditions	Min	Nom	Max	Units
Line rejection						
—	—	DC to 50 kHz, $I_{OUT} = 100\%$ rated load	30	60	—	dB
Insulation resistance						
—	—	50V @25 °C 1. Input (1-3) to All (4-12) 2. Sync (4-5) to All (1-3, 6-12) 3. Chassis (6) to All (1-5, 7-12)	100	—	—	MΩ
Mass						
—	—	Standard case style A, B	—	120	—	g
MTBF						
—	—	MIL-HDBK-217F2, SF, 35 °C	—	1.00×10^6	—	hrs
Isolation voltage						
—	—	—	—	100	—	V

5. Radiation Specification

Table 5-1. Radiation Specification

Environment	Conditions	Min	Unit
TID (gamma)	MIL-STD-883, method 1019 The operating bias applied during exposure	50	krad (Si)
SEE SEU, SEL, SEGR, SEB	Heavy ions [LET] The operating bias applied during exposure	37	MeV•cm ² /mg

Notes:

1. Parameter not 100% tested, and only assured by design.
2. Parameter verified during line and load regulation tests. Regulation is specified for 10% to 100% loading on all outputs.
3. Tested and verified using a 20 kHz to 10 MHz bandwidth. Ripple is measured across a 50 Ohms termination with a 10 nF Cap in series. Results applicable for DC to 20 MHz bandwidth.
4. The capacitive load may be any value from 0 to the maximum limit without compromising DC performance. A capacitive load exceeding the maximum limit may interfere with the proper operation of the converter's overload protection, potentially causing erratic behavior during turn-on.
5. Overload power dissipation is defined as the device power dissipation with the load set such that $V_{OUT} = 90\%$ of nominal.
6. The load step transition time is $\geq 10 \mu s$.
7. Recovery time is measured from the initiation of the transient to where V_{OUT} returned to within $\pm 1\%$ of its steady-state value.
8. The line step transition time is $\geq 100 \mu s$.
9. Turn-on delay time from either a step application of input power or a logic low to a logic high transition on the inhibit pin (pin 3) to the point where $V_{OUT} = 90\%$ of nominal.
10. Load regulation relative to the output voltage at 50% of nominal-rated load.
11. Line regulation relative to the output voltage at 28 VDC input.
12. For operation at temperatures between 70 °C and 100 °C, de-rate power linearly from 50 W (or rated maximum) to zero. Parameter limits are not guaranteed.

6. Parallel Operation

The output terminals of up to four modules may be connected in parallel. The expected current sharing accuracy is 10% at maximum load. To ensure current sharing, the Parallel terminal of every Power Supply module must be connected to form a common bus. It is recommended that these connections be made relatively short.

The remote sense terminals may remain unconnected. However, for the best output voltage regulation, it is recommended that the remote sense terminal of each of the paralleled set of power supplies be connected to a single point, as close as possible to the positive load terminal or the point where the voltage regulation needs to be maintained. Similarly, it is recommended that the remote sense return terminal of each power supply be connected to a single point, as close as possible to the negative load terminal.

The R-ADJUST function can be used in a system of paralleled modules. The sync function is described in the application notes. The specified sync input signal can be applied to each of the paralleled modules.

For best performance, phase shift the sync signal between modules. The sync functionality remains the same for a system of paralleled modules. The use of the sync function is optional for single and or paralleled operation. The specified sync input signal can be applied to any one of the paralleled modules.

7. Sample Electrical Waveforms

These waveforms are for reference only.

Figure 7-1. LE50-28 Efficiency

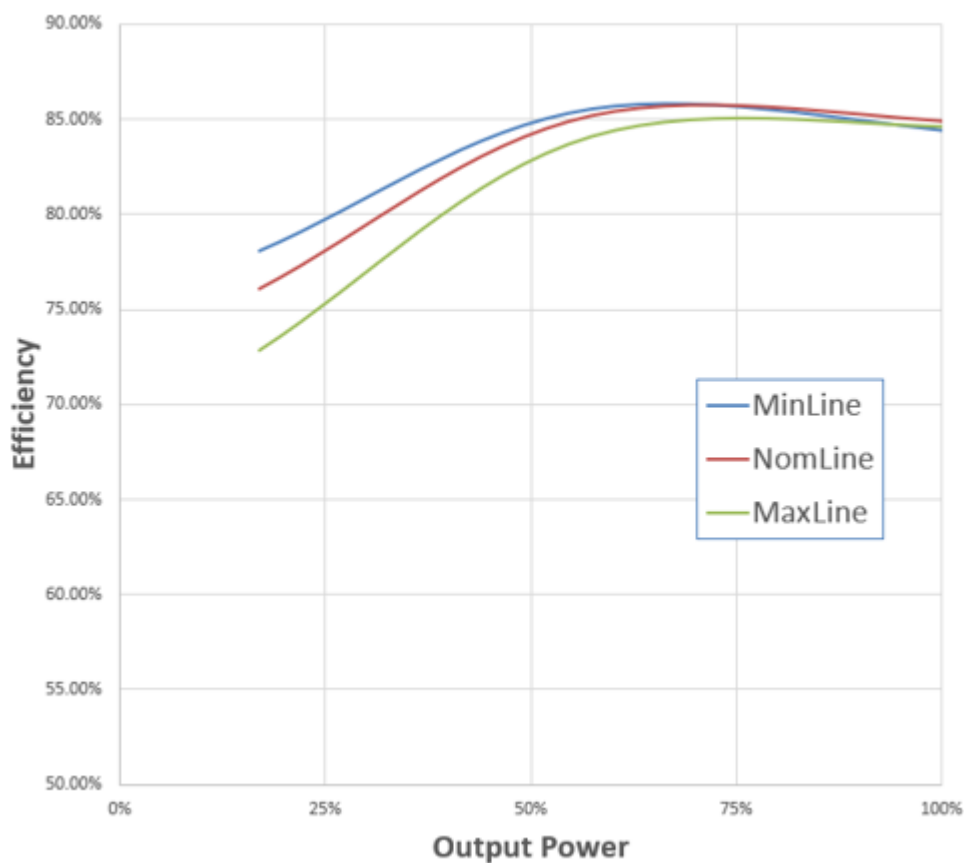


Figure 7-2. LE50-28 EMI

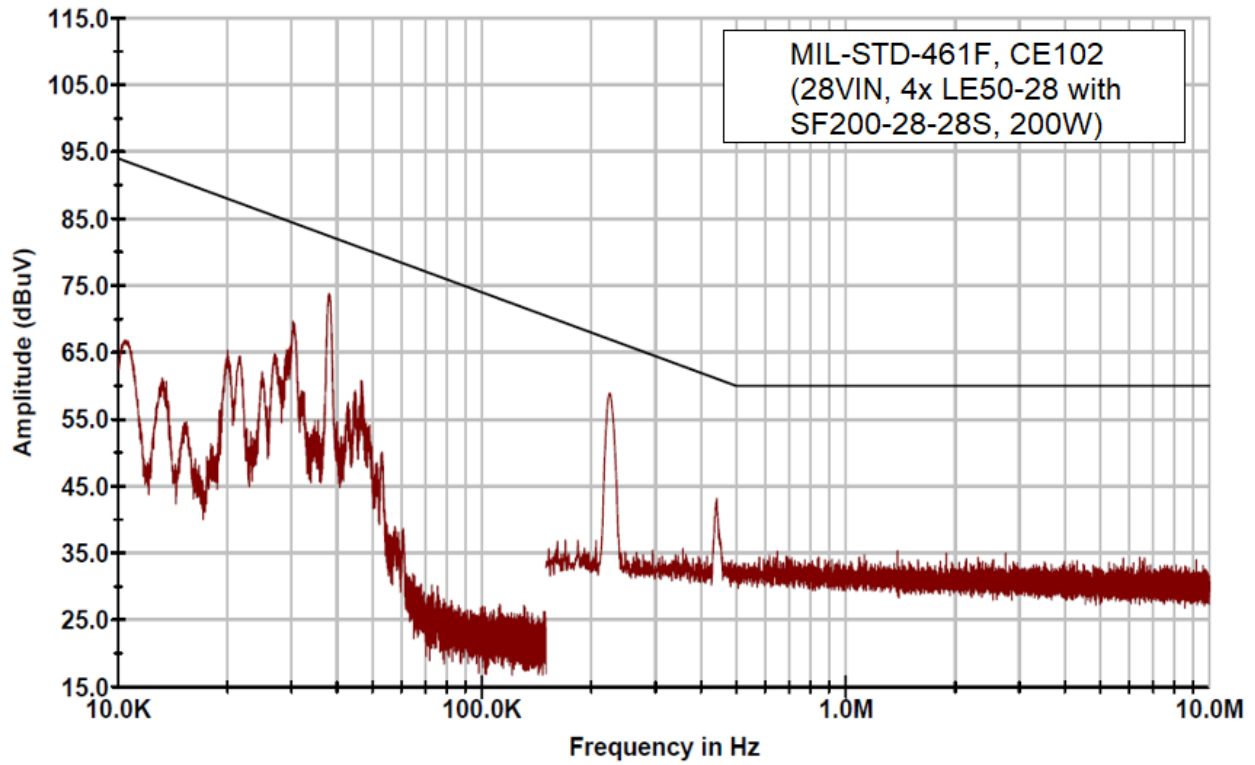


Figure 7-3. Typical Output Ripple

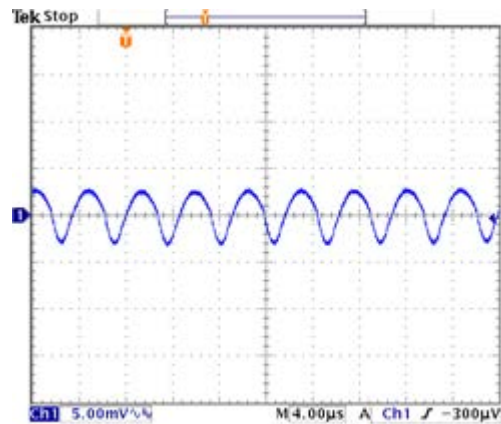
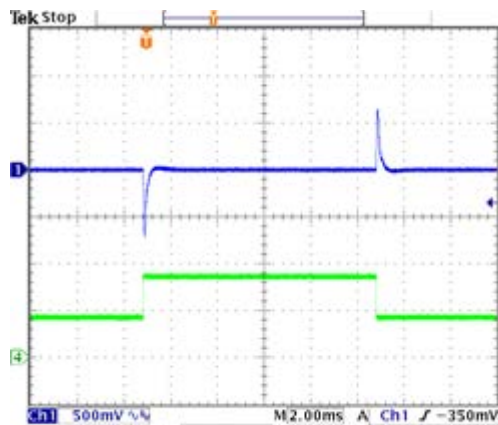


Figure 7-4. Typical 50% Load Step Transient Response



8. Pin Configuration

Figure 8-1. LE50-28 Single Pin Configuration



9. Pin Description

Table 9-1. Pin Description

PIN	NAME	Description
1	VIN	Input Voltage
2	VIN RTN	Input Voltage Return/Ground
3	ON/OFF (INHIBIT)	Power Supply ON/OFF, ON(OPEN/HIGH), OFF(SHORT/LOW)
4	SYNC	External Clock Signal Input
5	SYNC RTN	External Clock Signal Return
6	CHASSIS	Chassis Pin
7	R-ADJUST	Remote Adjust Pin to Adjust Output Voltage
8	PARALLEL	Parallel Bus Pin to use Multiple Devices for Higher Power
9	RMT SNS RTN	Load Voltage Remote Sense Return
10	RMT SNS	Load Voltage Remote Sense
11	VOUT	Output Voltage
12	VOUT RTN	Output Voltage Return/Ground

10. Radiation Performance (-T) Tolerant

- TID >50 krad (Si) and 30 krad (Si) ELDRS (<10 mrad/s) per MIL-STD-883 Method 1019
- SEE (SEGR, SEB, SET, SEL) immunity >37 MeV·cm²/mg

11. Mechanical Outline (Axial Pins) Package

Figure 11-1. Axial Pins and Through-Hole Tabs Package (-A)

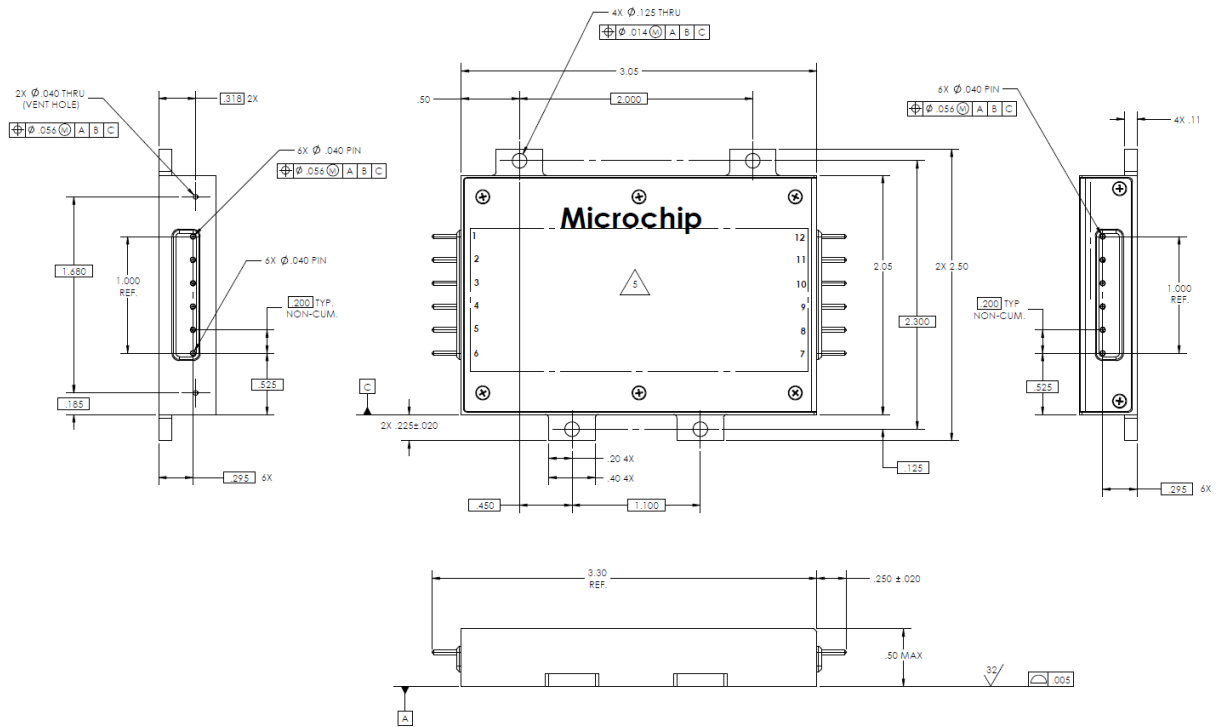
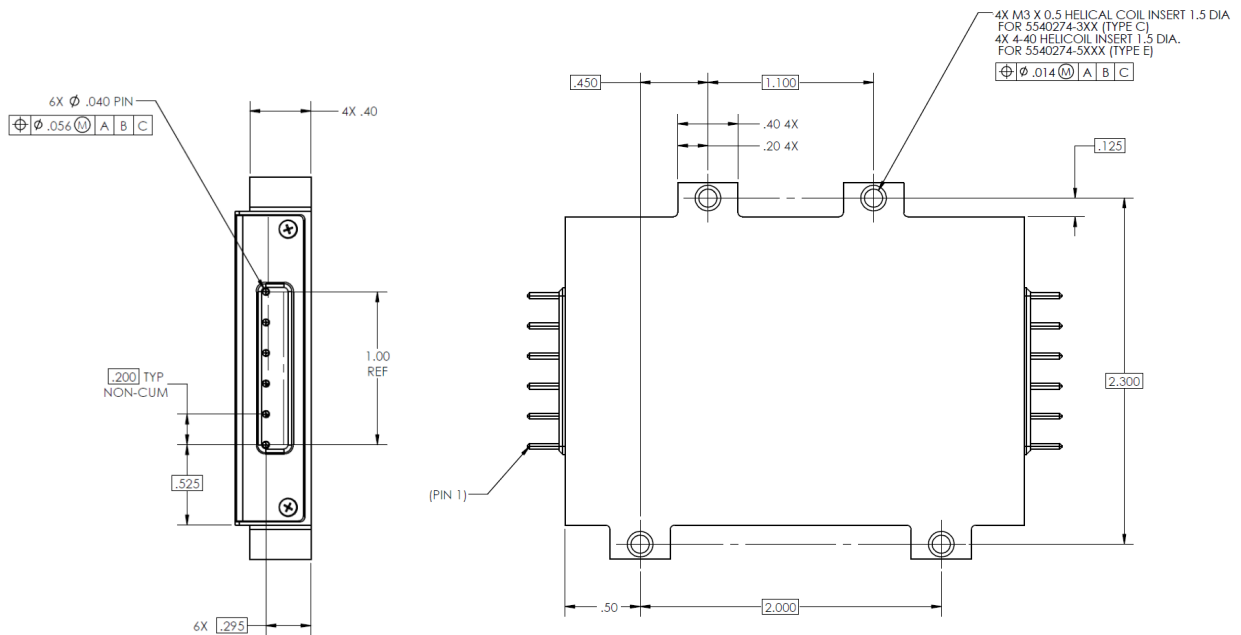


Figure 11-2. Axial Pins and Threaded Tabs Package Bottom View (-C or -E)



12. Mechanical Outline (Radial Pins) Package

Figure 12-1. Radial Pins and Through-Hole Tabs Package (-D)

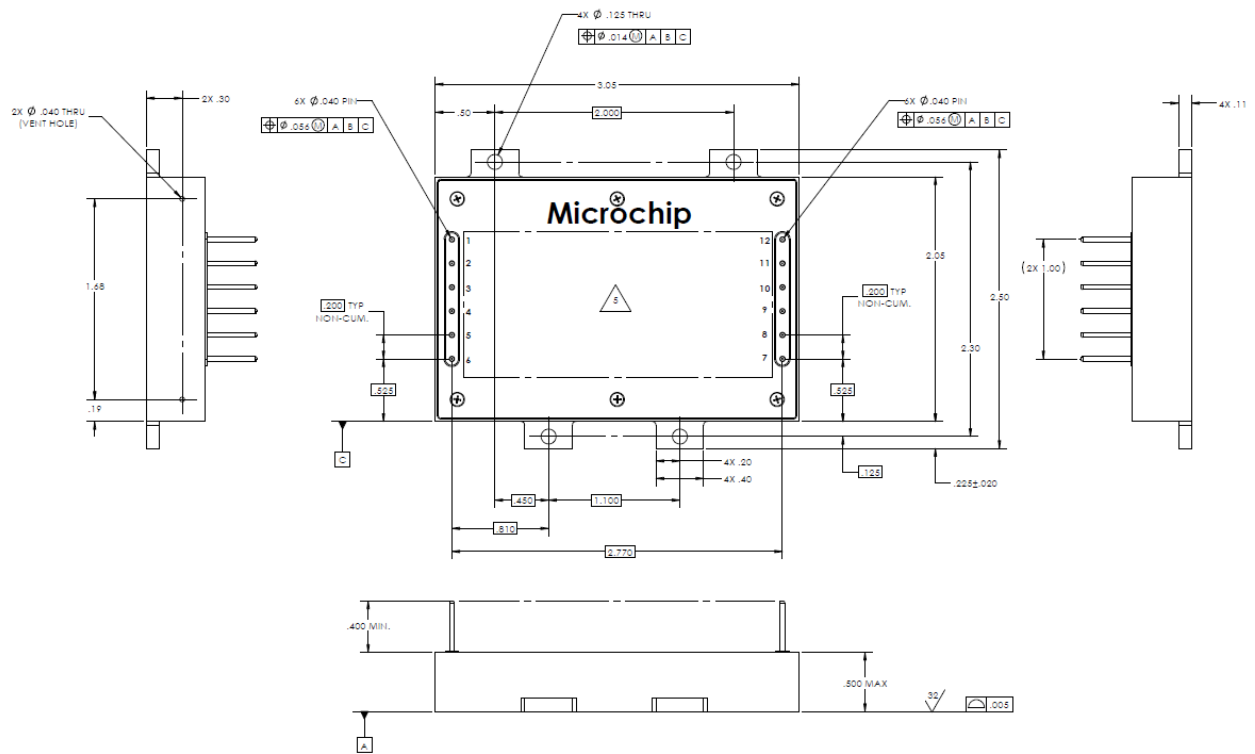
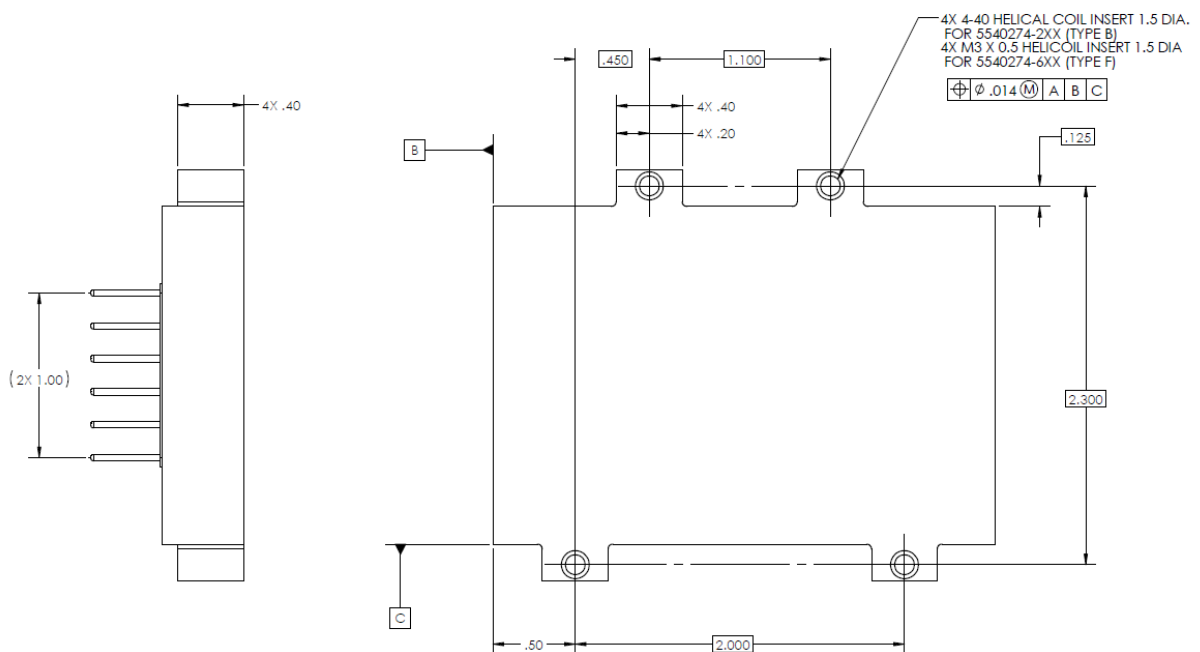


Figure 12-2. Radial Pins and Threaded Tabs Package Bottom View (-B or -F)



13. Qualification Test

Table 13-1. Qualification Test

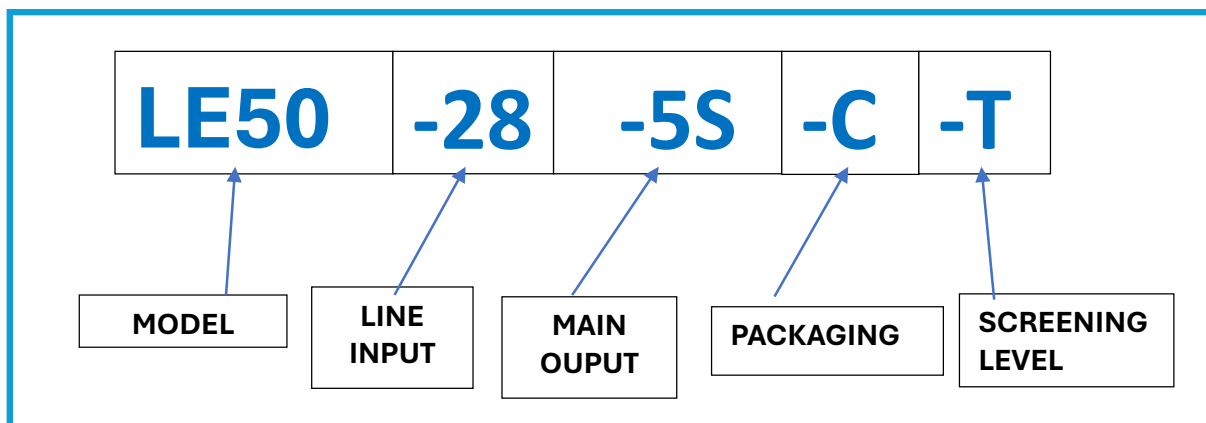
Test	Conditions
External Visual	Per O&M – Dimensions, and mass or MIL-STD-883 Method 2009
Electrical	Read and record (-55 °C, 25 °C, 70 °C)
Shock, Non-Operating	MIL-STD-202, method 213, test condition F, 1500 gpk, 0.5 ms ½ sine pulse. Three pulses in each direction of each axis, 18 pulses total.
Vibration, Operating	MIL-STD-202, method 214, condition II-F, 24.06 grms random vibrations, 50 Hz – 2000 Hz, 3 min/axis (9 min total) Outputs monitored.
Temperature Cycling	10 cycles from base plate temperature, MIL-STD-883, method 1010.9, condition A
EMI (with companion EMI filter)	CE101, CE102, CS101, RE101, RE102, RS101, RS103 per MIL-STD-461 with setup per MIL-STD-462
External Visual Inspection	No damage
Steady State Life Test	1000 hrs at Tc = 85 °C, 50% of rated load
End-Point Electricals	Read and record (-55 °C, 25 °C, 70 °C)

14. ATP Screening Test (-T) Tolerant

Table 14-1. ATP Screening Test (-T) Tolerant

Requirement	Test Method/Condition
External Visual	O&M – Dimensions and mass
Initial Electrical	Full performance at +25 °C
Vibration (Optional)	Workmanship non-operating vibration MIL-STD-202, Method 214, Condition II-A, 6.21 grms random vibration, 50-2000 Hz, 1-minute perpendicular to the board
Post Vibration Electrical	Full performance at +25 °C
Temperature Cycle (Optional)	MIL-STD-883, Method 1010, Condition A, 1 cycle, +70 °C to -55 °C, operating. Outputs monitored during thermal cycles
Burn-in (at 25 °C or Optional)	40 hrs @ 70 °C, 50% of rated load (outputs monitored)
Final Electrical (Optional)	Full performance at +25 °C (deliverable data)
External Visual	No damage

15. Ordering Information



Model	LE50	Standard Applications 50W, 28V input modules.		
Line Input	-28	28.0V	Line input voltage. Nominal input line	
Main	-3R3S	3.3V	Main output voltage	
	-5S	5V		
	-12S	12V		
	-15S	15V		
	-28S	28V		
Mechanical Package	-A	Axial	0.125 in through-hole	Mechanical packaging options. Electrical connections are either Axial or Radial. Mounting holes are drilled through-hole or threaded.
	-B	Radial	4-40 thread	
	-C	Axial	M3 thread	
	-D	Radial	0.125 in through-hole	
	-E	Axial	4-40 thread	
	-F	Radial	M3 thread	
Radiation Tolerance	-T	Tolerant	—	

Note: Other input voltage and output voltage combinations are available. Contact your local sales representative.

Microchip also offers a thermal interface, the ST-2X3; this is a non-silicon, space-approved thermal interface. The data sheet is available upon request.

16. Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

Revision	Date	Description
A	04/2024	Initial Revision

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