

Important notice

Dear Customer,

On 7 February 2017 the former NXP Standard Product business became a new company with the tradename **Nexperia**. Nexperia is an industry leading supplier of Discrete, Logic and PowerMOS semiconductors with its focus on the automotive, industrial, computing, consumer and wearable application markets

In data sheets and application notes which still contain NXP or Philips Semiconductors references, use the references to Nexperia, as shown below.

Instead of http://www.nxp.com, http://www.nxp.com, http://www.nexperia.com, http://www.nexperia.com,

Instead of sales.addresses@www.nxp.com or sales.addresses@www.semiconductors.philips.com, use salesaddresses@nexperia.com (email)

Replace the copyright notice at the bottom of each page or elsewhere in the document, depending on the version, as shown below:

- © NXP N.V. (year). All rights reserved or © Koninklijke Philips Electronics N.V. (year). All rights reserved

Should be replaced with:

- © Nexperia B.V. (year). All rights reserved.

If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

Product data sheet

1. General description

NPN switching transistor in a medium power flat lead SOT89 (SC-62/TO-243) Surface-Mounted Device (SMD) plastic package.

PNP complement: PXT2907A

2. Features and benefits

High current: max. 600 mA Low voltage: max. 40 V

3. Applications

Switching and linear amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	40	V
I _C	collector current		-	-	600	mA
h _{FE}	DC current gain	V_{CE} = 10 V; I_{C} = 150 mA; T_{amb} = 25 °C; $\delta \le 0.02$; $t_{p} \le 300 \ \mu s$; pulsed	100	-	300	

5. Pinning information

Table 2. Pinning information

Table 2.	Filling	IIIIOIIIIatioii		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		C
2	С	collector		в
3	В	base	3 2 1	_ N
			SOT89	sym123





NPN switching transistors

6. Ordering information

Table 3. Ordering information

Type number	Package	kage					
	Name	Description	Version				
PXT2222A	SOT89	plastic surface-mounted package; die pad for good heat transfer; 3 leads	SOT89				

7. Marking

Table 4. Marking codes

Type number	Marking code [1]
PXT2222A	%1P

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	600	mA
I _{CM}	peak collector current	t _p ≤ 1 ms; single pulse		-	800	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.5	W
			[2]	-	0.8	W
			[3]	-	1.1	W
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

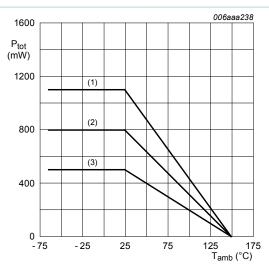
^[1] Transistor mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

PXT2222A

^[2] Transistor mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm².

^[3] Transistor mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm².

NPN switching transistors



- (1) FR4 PCB; 6 cm² mounting pad for collector.
- (2) FR4 PCB; 1 cm² mounting pad for collector.
- (3) FR4 PCB; standard footprint.

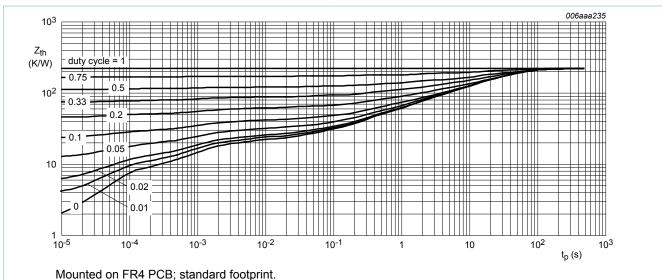
Fig. 1. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance	in free air	[1]	-	-	250	K/W
	from junction to ambient		[2]	-	-	156	K/W
	ambient		[3]	-	-	113	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	30	K/W

- [1] Transistor mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Transistor mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm².
- [3] Transistor mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm².



Mounted on Titl Tob, dandard lootprint.

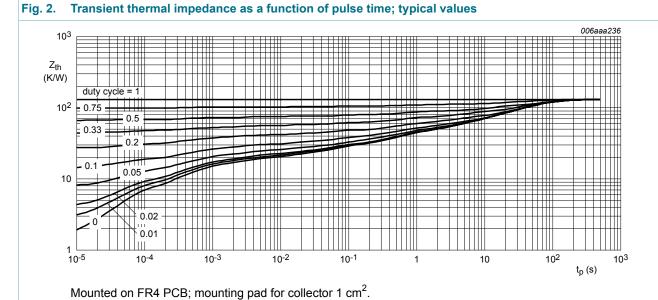
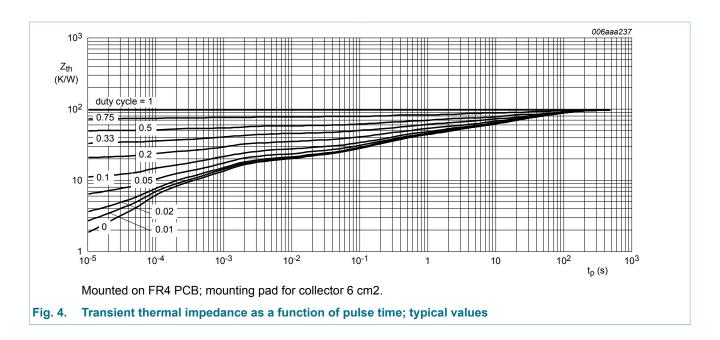


Fig. 3. Transient thermal impedance as a function of pulse time; typical values

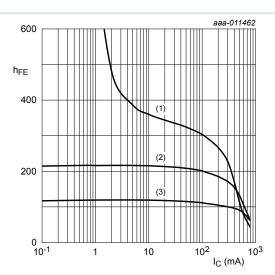


NPN switching transistors

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = 60 V; I _E = 0 A; T _{amb} = 25 °C	-	-	10	nA
	current	$V_{CB} = 60 \text{ V}; I_E = 0 \text{ A}; T_j = 125 ^{\circ}\text{C}$	-	-	10	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-	-	10	nA
h _{FE}	DC current gain	V_{CE} = 10 V; I_{C} = 0.1 mA; T_{amb} = 25 °C	35	-	-	
		V _{CE} = 10 V; I _C = 1 mA; T _{amb} = 25 °C	50	-	-	
		V_{CE} = 10 V; I_{C} = 10 mA; T_{amb} = 25 °C	75	-	-	
		V_{CE} = 10 V; I_{C} = 10 mA; T_{j} = -55 °C	35	-	-	
		V_{CE} = 1 V; I_{C} = 150 mA; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C; pulsed	50	-	-	
		V_{CE} = 10 V; I_{C} = 150 mA; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C; pulsed	100	-	300	
		V_{CE} = 10 V; I_{C} = 500 mA; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C; pulsed	40	-	-	
V _{CEsat}	collector-emitter	I_C = 150 mA; I_B = 15 mA; T_{amb} = 25 °C	-	-	300	mV
	saturation voltage	I_C = 500 mA; I_B = 50 mA; T_{amb} = 25 °C	-	-	1	V
V _{BEsat}	base-emitter saturation voltage	I_C = 150 mA; I_B = 15 mA; T_{amb} = 25 °C	0.6	-	1.2	V
		I_C = 500 mA; I_B = 50 mA; T_{amb} = 25 °C	-	-	2	V
t _d	delay time	I _C = 150 mA; I _{Bon} = 15 mA;	-	-	15	ns
t _r	rise time	I_{Boff} = -15 mA; T_{amb} = 25 °C	-	-	20	ns
t _{on}	turn-on time		-	-	35	ns
t _s	storage time		-	-	200	ns
t _f	fall time		-	-	60	ns
t _{off}	turn-off time		-	-	250	ns
C _C	collector capacitance	V_{CB} = 10 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; f_{amb} = 25 °C	-	-	8	pF
C _E	emitter capacitance	V_{EB} = 500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	-	25	pF
f _T	transition frequency	V_{CE} = 10 V; I_{C} = 20 mA; f = 100 MHz; T_{amb} = 25 °C	300	-	-	MHz
NF	noise figure	V_{CE} = 5 V; I_{C} = 200 μ A; R_{S} = 2 $k\Omega$; f = 1 kHz; B = 200 Hz; T_{amb} = 25 °C	-	-	4	dB



$$V_{CE} = 10 V$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55$$
 °C

Fig. 5. DC current gain as a function of collector current; typical values

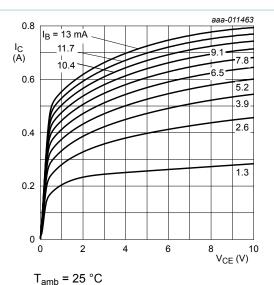
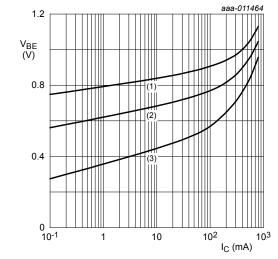


Fig. 6. Collector current as a function of collectoremitter voltage; typical values



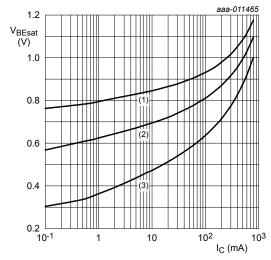
$$V_{CE} = 1 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



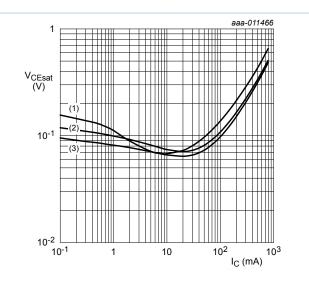
$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values



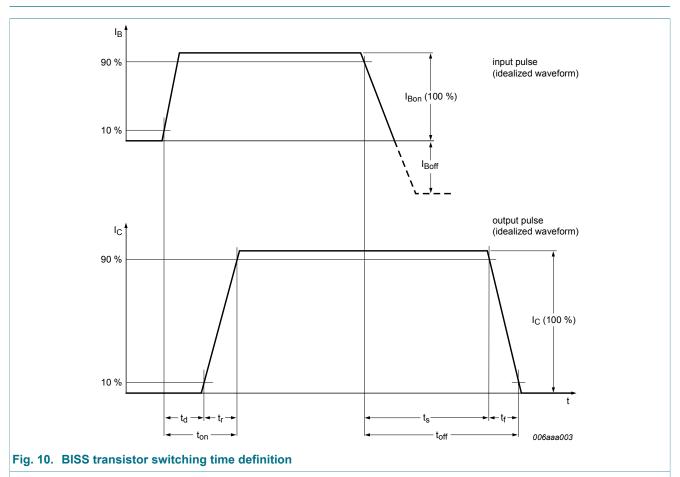
$$I_C/I_B = 10$$

- (1) $T_{amb} = 150 \, ^{\circ}C$
- (2) T_{amb} = 25 °C
- (3) $T_{amb} = -55 \, ^{\circ}C$

Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

NPN switching transistors

11. Test information



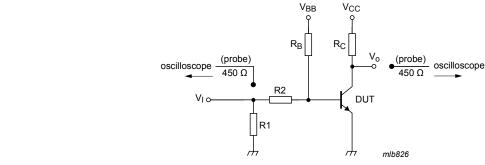
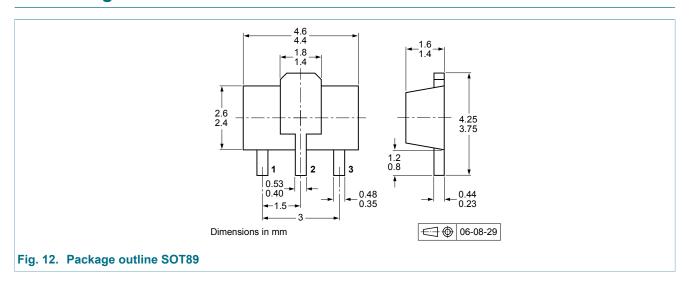


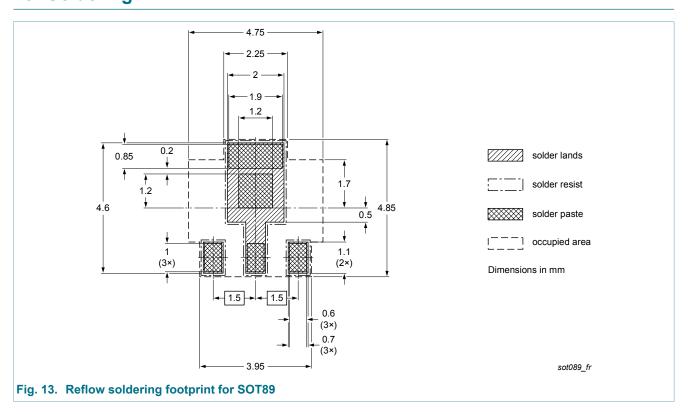
Fig. 11. Test circuit for switching times

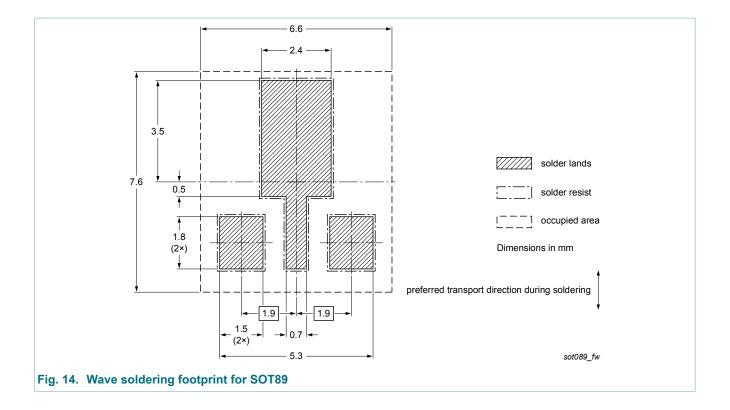
NPN switching transistors

12. Package outline



13. Soldering





NPN switching transistors

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PXT2222A v.5	20140402	Product data sheet	-	PXT2222A v.4	
Modifications:	of NXP Semiconduct Legal texts have be General description Quick refernce data Thermal characteris	en adapted to the new control : updated. : updated. : added. : added. : tics: Figure 2 to 4 updatedues of I _C , I _{CM} and I _{BM} paraures 5 to 9 added.			
PXT2222A v.4	20041122	Product specification	-	PXT2222A v.3	
PXT2222A v.3	19990414	Product specification	-	PXT2222A v.2	
PXT2222A v.2	19970505	Product specification	-	PXT2222A v.1	
PXT2222A v.1	19940901	Product specification	-	-	

NPN switching transistors

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

15.2 Definitions

Preview — The document is a preview version only. The document is still subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

15.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the

PXT2222A

All information provided in this document is subject to legal disclaimers.

© NXP Semiconductors N.V. 2014. All rights reserved

NPN switching transistors

grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

15.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Adelante, Bitport, Bitsound, CoolFlux, CoReUse, DESFire, EZ-HV, FabKey, GreenChip, HiPerSmart, HITAG, I²C-bus logo, ICODE, I-CODE, ITEC, Labelution, MIFARE, MIFARE Plus, MIFARE Ultralight, MoReUse, QLPAK, Silicon Tuner, SiliconMAX, SmartXA, STARplug, TOPFET, TrenchMOS, TriMedia and UCODE — are trademarks of NXP Semiconductors N.V.

HD Radio and **HD Radio** logo — are trademarks of iBiquity Digital Corporation.

NPN switching transistors

16. Contents

General description	1
Features and benefits	1
Applications	1
Quick reference data	1
Pinning information	1
Ordering information	2
Marking	2
Limiting values	2
Thermal characteristics	3
Characteristics	6
Test information	9
Package outline	10
Soldering	10
Revision history	12
Legal information	13
Data sheet status	13
Definitions	13
Disclaimers	13
Trademarks	14
	Features and benefits Applications Quick reference data Pinning information Ordering information Marking Limiting values Thermal characteristics Characteristics Test information Package outline Soldering Revision history Legal information Data sheet status Definitions Disclaimers

© NXP Semiconductors N.V. 2014. All rights reserved

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com Date of release: 2 April 2014