1.0A SURFACE-MOUNT SCHOTTKY BARRIER RECTIFIER
PowerDI123

Product Summary

V_R (V)	I_F (A)	$V_{F\ MAX}$ (V) @ +25°C	$I_{R\ MAX}$ (mA) @ +25°C
40	0.5	0.51	0.02

Features and Benefits

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- Low-Forward Voltage Drop
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DFLS140Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.**
  https://www.diodes.com/quality/product-definitions/

Description and Applications

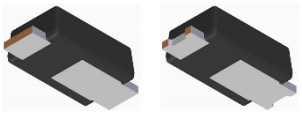
This Schottky Barrier Rectifier is designed to meet the stringent requirements of automotive applications. It is ideally suited to be used as:

- Polarity protection diodes
- Re-circulating diodes
- Switching diodes

Mechanical Data

- Package: PowerDI®123
- Package Material: Molded Plastic; UL "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Polarity: Cathode Band
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.01 grams (Approximate)

PowerDI123



Bottom View

Ordering Information (Note 4)

Orderabe Part Number	Package	Packing	
		Qty.	Carrier
DFLS140Q-7	PowerDI123	3,000	Tape & Reel

Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

PowerDI123

F04 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: M = 2025)
M = Month (ex: 9 = September)

Date Code Key

Year	2014	-	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	B	-	M	N	P	R	S	T	U	V	W	X

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	40	V
RMS Reverse Voltage	$V_{R(RMS)}$	28	V
Average Forward Current @ T_T = +119°C	$I_{F(AV)}$	1.1	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	I_{FSM}	40	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_D	1.67	W
Power Dissipation (Note 6)	P_D	556	mW
Thermal Resistance Junction to Ambient (Note 5)	$R_{\theta JA}$	60	°C/W
Thermal Resistance Junction to Ambient (Note 6)	$R_{\theta JA}$	180	°C/W
Thermal Resistance Junction to Soldering (Note 7)	$R_{\theta JS}$	10	°C/W
Operating Temperature Range	T_J	-55 to +125	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	$V_{(BR)R}$	40	—	—	V	I_R = 20μA
Forward Voltage	V_F	— —	0.45 0.53	0.51 0.61	V	I_F = 0.5A I_F = 1.1A
Leakage Current (Note 8)	I_R	— —	— —	20 6.0	μA mA	V_R = 40V, T_J = +25°C V_R = 40V, T_J = +100°C
Total Capacitance	C_T	—	28	—	pF	V_R = 10V, f = 1.0MHz

Notes:
5. Part mounted on 50.8mm x 50.8mm GETEK board with 25.4mm x 25.4mm copper pad, 25% anode, 75% cathode. T_A = +25°C.
6. Part mounted on FR-4 board with 1.8mm x 2.5mm cathode and 1.8mm x 1.2mm anode, 1oz. copper pads. T_A = +25°C.
7. Theoretical $R_{\theta JS}$ calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
8. Short duration pulse test used to minimize self-heating effect.

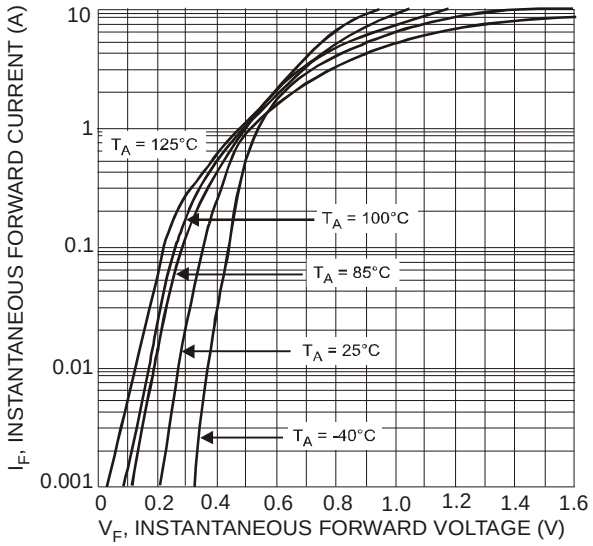


Fig. 1 Typical Forward Characteristics

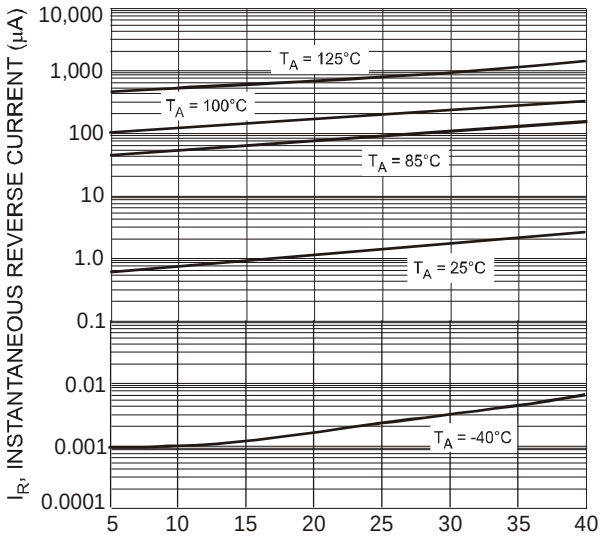


Fig. 2 Typical Reverse Characteristics

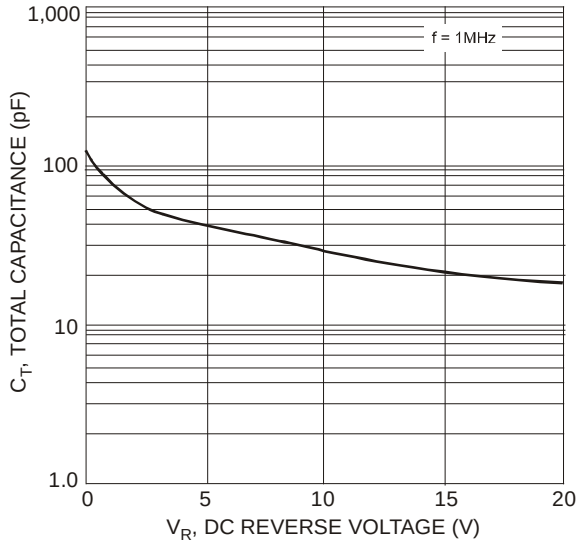


Fig. 3 Total Capacitance vs. Reverse Voltage

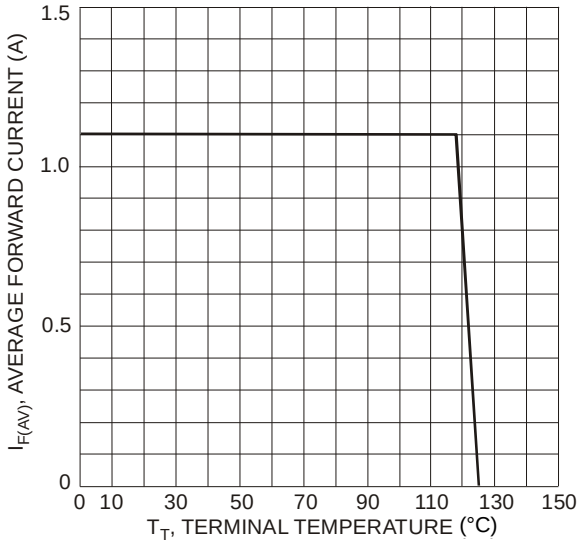


Fig. 4 Forward Current Derating Curve

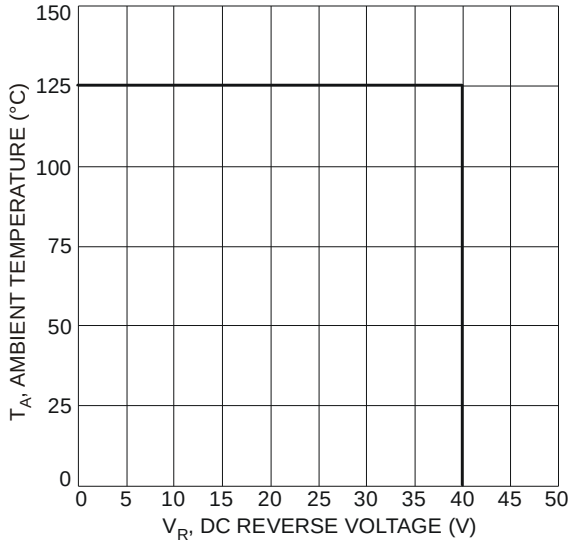


Fig. 5 Operating Temperature Derating

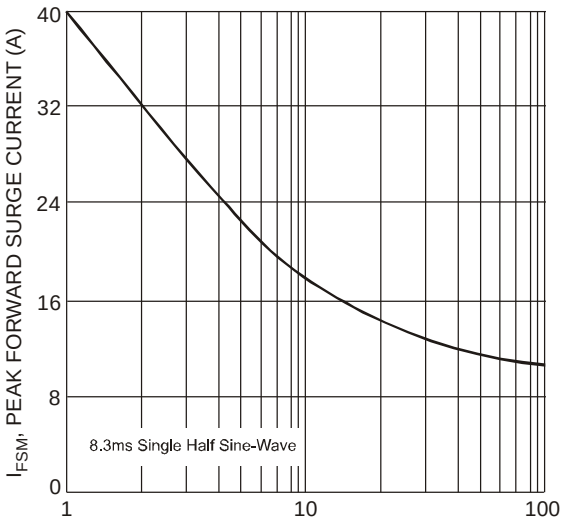
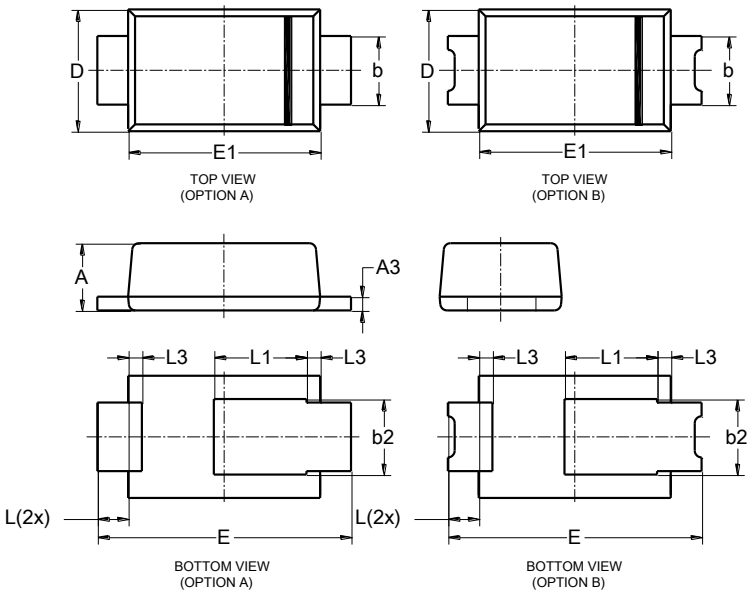


Fig. 6 Maximum Non-Repetitive Peak Forward Surge Current

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123

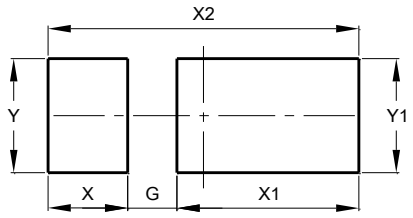


PowerDI123			
Dim	**Min**	**Max**	**Typ**
A	0.93	1.00	0.98
A3	0.15	0.25	0.20
b	0.85	1.25	1.00
b2	1.025	1.125	1.10
D	1.63	1.93	1.78
E	3.50	3.90	3.70
E1	2.60	3.00	2.80
L	0.40	0.50	0.45
L1	1.25	1.40	1.35
L3	0.125	0.275	0.20
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123



Dimensions	Value (in mm)
G	0.65
X	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50

IMPORTANT NOTICE

1. DIODES INCORPORATED (Diodes) AND ITS SUBSIDIARIES MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes' products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes' products. Diodes' products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of Diodes' products for their intended applications, (c) ensuring their applications, which incorporate Diodes' products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.

3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.

4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.

5. Diodes' products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.

6. Diodes' products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes' products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.

7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.

8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

9. This Notice may be periodically updated with the most recent version available at https://www.diodes.com/about/company/terms-and-conditions/important-notice

www.diodes.com