**Product data sheet** 

## 1. General description

NPN high-voltage transistor in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Low current (max. 300 mA)
- High voltage (max. 160 V)

# 3. Applications

Switching and amplification in high voltage applications such as telephony.

## 4. Quick reference data

### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	140	V
I <sub>C</sub>	collector current		-	-	300	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C	60	-	-	

# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	
2	Е	emitter		C
3	С	collector		B—————————————————————————————————————
			1	sym123



### NPN high-voltage transistor

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PMST5550	SC-70	plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	SOT323			

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
PMST5550	%1F

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	160	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	140	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	300	mA
I <sub>CM</sub>	peak collector current			-	600	mA
I <sub>BM</sub>	peak base current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	200	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## 9. Thermal characteristics

### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

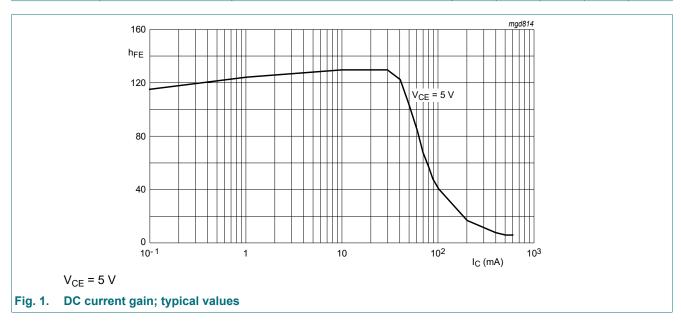
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

## **NPN** high-voltage transistor

# 10. Characteristics

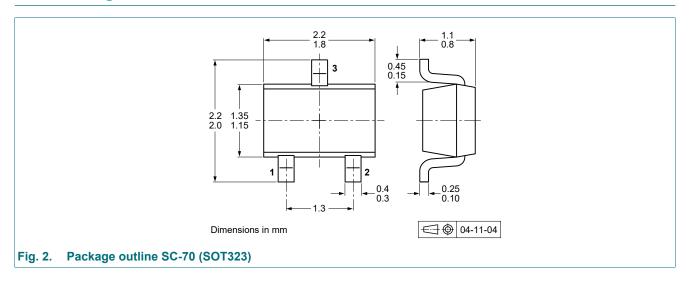
**Table 7. Characteristics** 

Symbol	Parameter	Conditions	Mir	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 100 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
	current	V <sub>CB</sub> = 100 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 100 °C	-	-	100	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 4 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C	60	-	-	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; T <sub>amb</sub> = 25 °C	60	-	250	
		$V_{CE}$ = 5 V; $I_{C}$ = 50 mA; pulsed; $t_{p}$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	20	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA; T <sub>amb</sub> = 25 °C	-	-	150	mV
		$I_C$ = 50 mA; $I_B$ = 5 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	250	V
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA; T <sub>amb</sub> = 25 °C	-	-	1	V
		$I_C$ = 50 mA; $I_B$ = 5 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	1.2	V
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25  ^{\circ}\text{C}$	-	-	6	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_{C} = 0 \text{ A}; i_{c} = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	-	-	30	pF
f <sub>T</sub>	transition frequency	$V_{CE} = 10 \text{ V}; I_{C} = 10 \text{ mA}; f = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	100	-	-	MHz
NF	noise figure	$V_{CE}$ = 5 V; $I_{C}$ = 200 μA; $R_{S}$ = 2 kΩ; f = 10 Hz to 15.7 kHz	-	-	8	dB

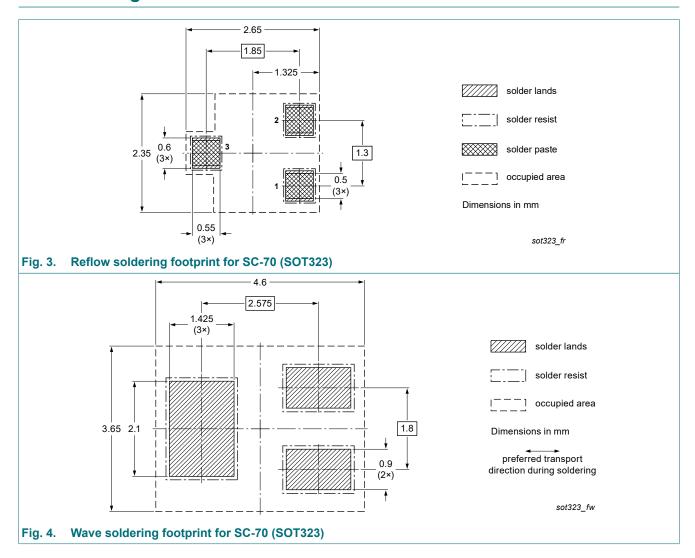


## NPN high-voltage transistor

# 11. Package outline



# 12. Soldering



## NPN high-voltage transistor

# 13. Revision history

### **Table 8. Revision history**

Table of Revision Instoly								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PMST5550 v.5	20241008	Product data sheet	-	PMST5550 v.4				
Modifications:		<ul> <li>Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).</li> </ul>						
PMST5550 v.4	20240119	Product data sheet	-	PMST5550 v.3				
PMST5550 v.3	20230726	Product data sheet	-	PMST5550_5551 v.2				
PMST5550_5551 v.2	19990429	Product data sheet	-	PMST5550_5551 v.1				
PMST5550_5551 v.1	19970520	Product data sheet	-	-				

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## 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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PMST5550

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