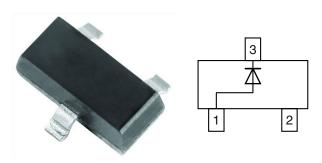


# Vishay Semiconductors

# **Small Signal Fast Switching Diode**



#### **LINKS TO ADDITIONAL RESOURCES**











#### **FEATURES**

- · Silicon epitaxial planar diode
- Ultra fast switching speed (≤ 4 ns)
- Surface mount package ideally suited for automatic insertion
- High conductance
- AEC-Q101 qualified available (part number on request)
- Moisture sensitivity level (MSL) 1
- Base P/N-G3-green, commercial grade
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE

**GREEN** (5-2008)

#### **MECHANICAL DATA**

Case: SOT-23

Weight: approx. 9.2 mg
Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE							
PART	ORDERING CODE	AEC-Q101 QUALIFIED	TYPE MARKING	CIRCUIT CONFIGURATION	TAPED UNITS PER REEL	MINIMUM ORDER QUANTITY	
BAS16-G	BAS16-G3-08	no	AK	Single	3 000 (8 mm tape on 7" reel)	15 000	
	BAS16-G3-18	no	AK		10 000 (8 mm tape on 13" reel)	10 000	

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Non repetitive peak reverse voltage		$V_{RM}$	100	V			
Repetitive peak reverse voltage = working peak reverse voltage = DC blocking voltage		$V_{RRM} = V_{RWM} = V_{R}$	75	V			
Peak forward surge current (1)	t <sub>p</sub> = 1 s	I <sub>FSM</sub>	1	Α			
Feak lot ward surge current (**)	t <sub>p</sub> = 1 μs	I <sub>FSM</sub>	2	Α			
Average forward current (1)	Half wave rectification with resistive load and f ≥ 50 Hz	I <sub>F(AV)</sub>	250	mA			
Forward current (1)		I <sub>F</sub>	350	mA			
Power dissipation	On FR-4 board with recommended soldering footprint	P <sub>tot</sub>	270	mW			
rowei dissipation	Infinite heatsink	⁻tot	390	mW			

#### Note

(1) Infinite heatsink

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air	according to JEDEC <sup>®</sup> 51-3 on FR-4 board with recommended soldering footprint	R <sub>thJA</sub>	460	K/W		
Thermal resistance junction to lead	Infinite heat sink	R <sub>thJL</sub>	320	K/W		
Junction temperature		Tj	125	°C		
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C		
Operating temperature range		T <sub>op</sub>	-55 to +150	°C		

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MAX.	UNIT		
	I <sub>F</sub> = 1 mA	V <sub>F</sub>	0.715	V		
Forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>	855	mV		
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>	1	V		
	I <sub>F</sub> = 150 mA	V <sub>F</sub>	0.715	V		
	V <sub>R</sub> = 75 V	I <sub>R</sub>	100	nA		
Reverse current	$V_R = 75 \text{ V}, T_j = 150 ^{\circ}\text{C}$	V <sub>F</sub> 0.715  V <sub>F</sub> 855  V <sub>F</sub> 1  V <sub>F</sub> 1.25  I <sub>R</sub> 100  I <sub>R</sub> 50  I <sub>R</sub> 30  C <sub>D</sub> 1.5	μΑ			
	V <sub>R</sub> = 25 V, T <sub>j</sub> = 150 °C	I <sub>R</sub>	0.715 855 1 1.25 100 50 30 1.5	μΑ		
Diode capacitance	V <sub>R</sub> = 0, f = 1 MHz	C <sub>D</sub>	1.5	pF		
Reverse recovery time	$I_F$ = 10 mA to $i_R$ = 1 mA, $V_R$ = 6 V, $R_L$ = 100 $\Omega$	t <sub>rr</sub>	6	ns		

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

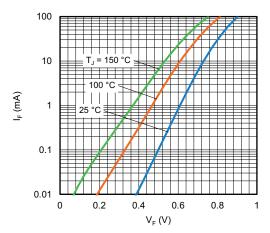


Fig. 1 - Typical Forward Current vs. Forward Voltage

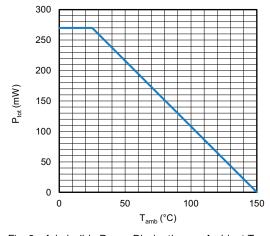


Fig. 2 - Admissible Power Dissipation vs. Ambient Temperature

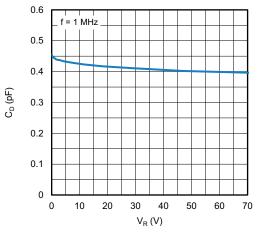


Fig. 3 - Typical Capacitance vs. Reverse Voltage

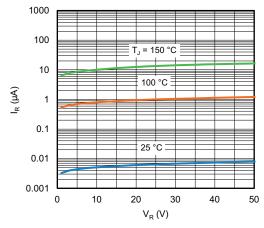
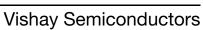
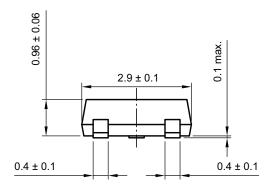


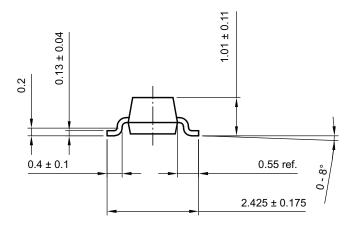
Fig. 4 - Typical Reverse Leakage Current vs. Reverse Voltage

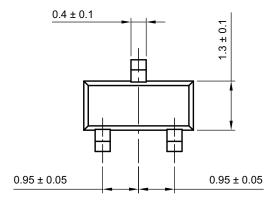




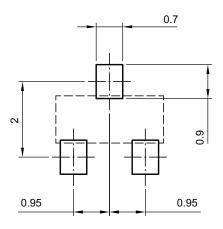
#### **PACKAGE DIMENSIONS** in millimeters: **SOT-23**







#### footprint recommendation:



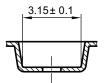
Created - Date: 18-Oct-2021 Rev. 01 - Date: 18-Jan-2022 S8-V-3929.01-009 (4)

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#### **CARRIER TAPE SOT-23**

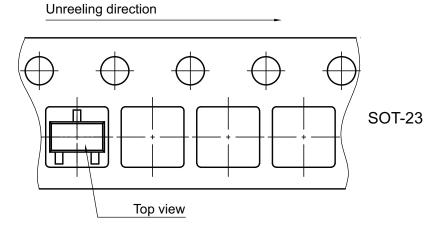
# A-A Section 0.229 ± 0.013 0.229 ± 0.013 0.229 ± 0.013 0.229 ± 0.013

**B-B Section** 



Created Date: 04-Feb-2010 Rev. Date: 07-Feb-2022 S8-V-3929.01-005 (4)

#### **ORIENTATION IN CARRIER TAPE SOT-23**



Created Date: 04-Feb-2010 Rev. Date: 07-Nov-2022 S8-V-3929.01-005 (4)



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