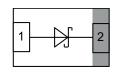


Small Signal Schottky Diode with T_J max. = 175 °C





LINKS TO ADDITIONAL RESOURCES







MECHANICAL DATA

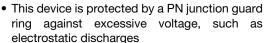
Case: DFN1006-2A Weight: 0.83 mg

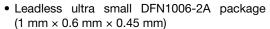
Molding compound flammability rating: UL 94 V-0 Terminals: high temperature soldering guaranteed:

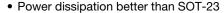
Peak temperature max. 260 °C Packaging codes/options: 08/10K per 7" reel (8 mm tape)

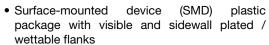
FEATURES

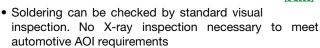












AEC-Q101 qualified available

· Material categorization: for definitions of compliance please see www.vishay.com/doc?99912









PARTS TABLE						
PART	ORDERING CODE	AEC-Q101 QUALIFIED	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
BAS40LTH	BAS40LTH-G3-08	no Single		GE	Tape and reel	
	BAS40LTH-HG3-08	yes	Sirigie	GL	rape and ree	

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage	erse voltage		40	V	
Forward current	current on FR-4 board with recommended soldering footprint		200	mA	
	$T_J = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$		500	mA	
Non-repetitive peak forward current	$T_{J} = 100 ^{\circ}\text{C}, t_{p} = 10 \text{ms}$	I _{FSM}	200		
	T _J = 125 °C, t _p = 20 μs		500		
Power dissipation	on FR-4 board with recommended soldering footprint	D	350	mW	
Power dissipation	$R_{thJL} = 100 \text{ K/W}$	P _{tot}	1500	mW	

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air	according to JEDEC® 51-3 on FR-4 board with recommended soldering footprint	R _{thJA} 420		K/W		
Thermal resistance junction to lead		R _{thJL}	100	K/W		
Maximum junction temperature		T _{j max.}	175	°C		
Storage temperature range		T _{stg}	-55 to +175	°C		
Operating temperature range		T _{op}	-55 to +175	°C		



ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$V_R = 40 \text{ V}, T_J = 25 ^{\circ}\text{C}$				10	μΑ
Leakage current	V _R = 30 V, T _J = 150 °C	I _R			200	μΑ
	V _R = 40 V, T _J = 150 °C				500	μΑ
	I _F = 1 mA	V _F			400	mV
Forward voltage	I _F = 10 mA				560	mV
	I _F = 40 mA				1000	mV
Diode capacitance	V _R = 0 V, f = 1 MHz	C _D		2.9		pF

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

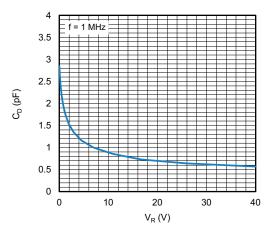


Fig. 1 - Typical Capacitance vs. Reverse Voltage

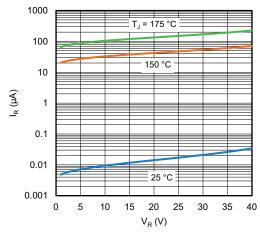


Fig. 3 - Typical Reverse Leakage Current vs. Reverse Voltage

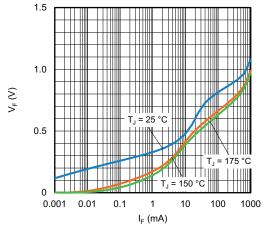
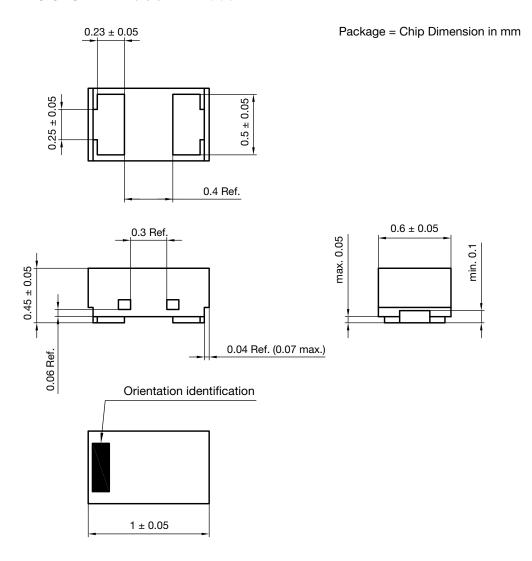
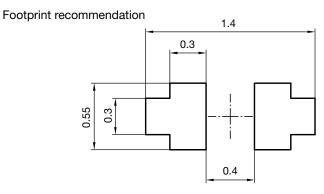


Fig. 2 - Typical Forward Voltage vs. Forward Current



PACKAGE DIMENSIONS in millimeters: DFN1006-2A

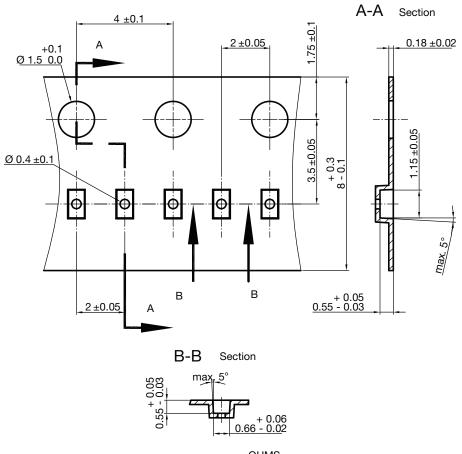




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CARRIER TAPE DFN1006-2A



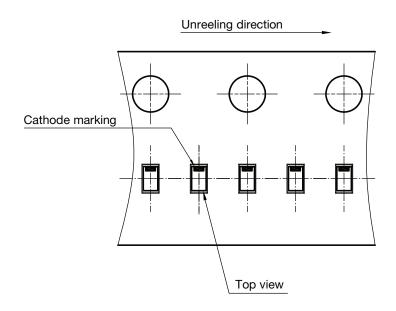
S8-V-3906.04-063 (4) created 28.10.2019

S8-V-3906.04-064 (4)

created 28.10.2019

surface resistance: 10^5 - $10^{11} \frac{OHMS}{SQ}$ Cummulative tolerances of 10 sprocket holes is ± 0.2 mm

ORIENTATION IN CARRIER TAPE DFN1006-2A





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