BAT54M3T5G

Schottky Barrier Diode

This Schottky barrier diode is designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand held and portable applications where space is limited.

The BAT54M3T5G device is a spin-off of our popular SOT-23 three-leaded device and is housed in the SOT-723 surface mount package. This device is ideal for low-power surface mount applications where board space is at a premium.

Features

- Extremely Fast Switching Speed
- Low Forward Voltage -0.35 Volts (Typ) @ $I_F = 10$ mAdc
- Reduces Board Space
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 125°C unless otherwise noted)

| Rating | Symbol | Value | Unit |
|--|------------------|-------------|-------------|
| Reverse Voltage | V _R | 30 | V |
| Forward Power Dissipation @ T _A = 25°C Derate above 25°C | P _F | 200 2.0 | mW mW/°C |
| Forward Current (DC) | IF | 200 Max | mA |
| Non–Repetitive Peak Forward Current t _p < 10 msec | I _{FSM} | 600 | mA |
| Repetitive Peak Forward Current Pulse Wave = 1 sec, Duty Cycle = 66% | I _{FRM} | 300 | mA |
| Junction Temperature | TJ | -55 to +125 | °C |
| Storage Temperature Range | T _{stg} | -55 to +150 | °C |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



ON Semiconductor®

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30 V SILICON HOT-CARRIER DETECTOR AND SWITCHING DIODE



MARKING DIAGRAM



SOT-723 CASE 631AA STYLE 2



AP = Specific Device Code M = Date Code

ORDERING INFORMATION

| Device | Package | Shipping [†] |
|---------------|----------------------|-----------------------|
| BAT54M3T5G | SOT-723 (Pb-Free) | 8000/Tape & Reel |
| NSVBAT54M3T5G | SOT-723 (Pb-Free) | 8000/Tape & Reel |

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

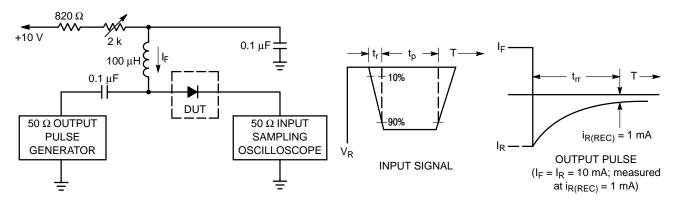
BAT54M3T5G

ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|---|--------------------|-----|------|------|------|
| Reverse Breakdown Voltage (I _R = 10 μA) | V _{(BR)R} | 30 | - | - | V |
| Total Capacitance (V _R = 1.0 V, f = 1.0 MHz) | C _T | - | 7.6 | 10 | pF |
| Reverse Leakage (V _R = 25 V) | I _R | - | 0.5 | 2.0 | μΑ |
| Forward Voltage $(I_F = 0.1 \text{ mA})$ | V _F | - | 0.22 | 0.24 | V |
| $(I_F = 1.0 \text{ mA})$ | | - | 0.29 | 0.32 | |
| $(I_F = 10 \text{ mA})$ | | - | 0.35 | 0.40 | |
| $(I_F = 30 \text{ mA})$ | | - | 0.41 | 0.5 | |
| $(I_F = 100 \text{ mA})$ | | - | 0.52 | 0.8 | |
| Reverse Recovery Time ($I_F = I_R = 10 \text{ mA}$, $I_{R(REC)} = 1.0 \text{ mA}$, Figure 1) | t _{rr} | - | - | 5.0 | ns |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

BAT54M3T5G



Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA.

- 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.
- 3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

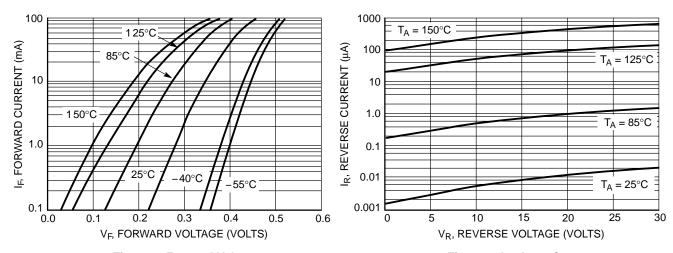


Figure 2. Forward Voltage

Figure 3. Leakage Current

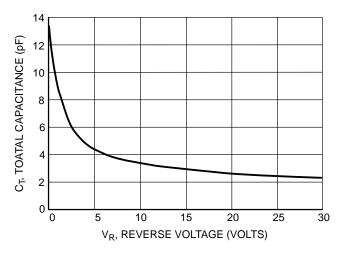


Figure 4. Total Capacitance





SOT-723 1.20x0.80x0.50, 0.40P CASE 631AA ISSUE E

DATE 24 JAN 2024

MAX.

0.55

0.27

0.37

0.17

1.25

0.85

1.25

0.25

MILLIMETERS

 $N\square M$.

0.50

0.21

0.31

0.12

1.20

0.80

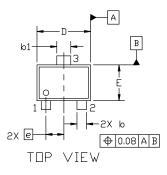
0.40 BSC

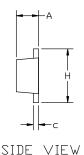
1.20

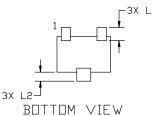
0.29 REF

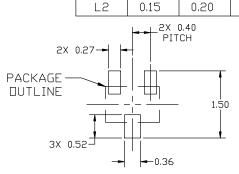
NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH, MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.









DIM

Α

b

b1

c D

Ε

e H

L

MIN.

0.45

0.15

0.25

0.07

1.15

0.75

1.15

RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

GENERIC MARKING DIAGRAM*



XX = Specific Device Code M = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

| STYLE 1: | STYLE 2: | STYLE 3: | STYLE 4: | STYLE 5: |
|-----------------------------|---------------------------|---------------------------|---------------------------|--------------------------|
| PIN 1. BASE | PIN 1. ANODE | PIN 1. ANODE | PIN 1. CATHODE | PIN 1. GATE |
| 2. EMITTER | 2. N/C | 2. ANODE | CATHODE | SOURCE |
| COLLECTOR | CATHODE | CATHODE | ANODE | DRAIN |

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|------------------|-------------------------------|---|-------------|--|
| DESCRIPTION: | SOT-723 1.20x0.80x0.50, 0.40P | | PAGE 1 OF 1 | |

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