

NPN General Purpose Amplifier BCP55

This device is designed for general purpose medium power amplifiers and switching circuits requiring collector currents to 1.0 A. Sourced from Process 38.

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C unless otherwise noted.)

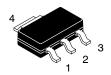
Symbol	Parameter	Ratings	Unit
V _{CEO}	Collector-Emitter Voltage	60	V
V _{CBO}	Collector-Base Voltage	60	V
V _{EBO}	Emitter-Base Voltage	5.0	V
I _C	Collector Current - Continuous	1.5	Α
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. These ratings are based on a maximum junction temperature of 150 °C.
- These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

THERMAL CHARACTERISTICS (T_A = 25 °C unless otherwise noted.)

Symbol	Characteristic	Max	Unit
P _D	Total Device Dissipation Derate Above 25°C	1.5 12	W mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	83.3	°C/W



- 1: Base
- 2, 4: Collector 3: Emitter
- SOT-223 CASE 318H

MARKING DIAGRAM



A = Assembly Location

Y = Year

W = Work Week

BCP55 = Specific Device Code • Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping
BCP55	SOT-223 (Pb-Free)	4,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_A = 25 °C unless otherwise noted.)

Symbol	Parameter	Conditions	Min	Max	Unit
OFF CHAR	ACTERISTICS	•			
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10 mA, I _B = 0	60	_	V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 100 μA, I _E = 0	60	_	V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 10 μA, I _C = 0	5.0	-	V
I _{CBO}	Collector-Cutoff Current	V _{CB} = 30 V, I _E = 0 V _{CB} = 30 V, I _E = 0, T _A = 125 °C	_ _	100 10	nA μA
I _{EBO}	Emitter-Cutoff Current	V _{EB} = 5.0 V, I _C = 0	-	10	μΑ
ON CHARA	CTERISTICS				
h⊨⊨	DC Current Gain	Ic = 5.0 mA. Vc= = 2.0 V	25	_	

h _{FE}	DC Current Gain	$I_C = 5.0 \text{ mA}, V_{CE} = 2.0 \text{ V}$	25	-	
		I _C = 150 mA, V _{CE} = 2.0 V	40	250	
		$I_C = 500 \text{ mA}, V_{CE} = 2.0 \text{ V}$	25	-	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 500 mA, I _B = 50 mA	-	0.5	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = 500 mA, V _{CE} = 2.0 V	-	1.0	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

BCP55

TYPICAL CHARACTERISTICS

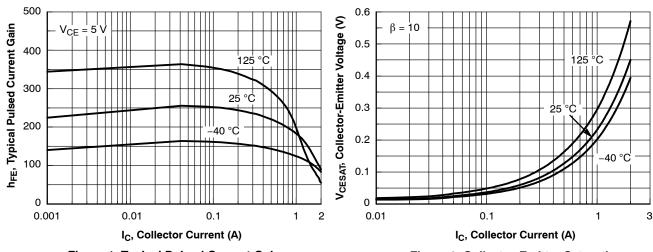


Figure 1. Typical Pulsed Current Gain vs Collector Current

Figure 2. Collector-Emitter Saturation Voltage vs Collect or Current

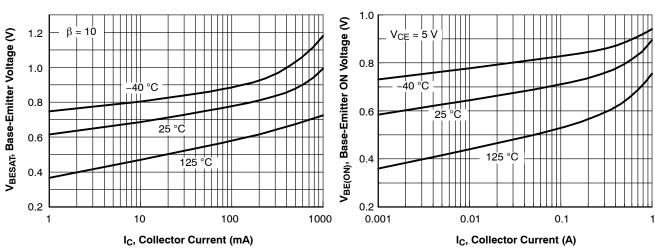


Figure 3. Base-Emitter Saturation Voltage vs Collector Current

Figure 4. Base-Emitter ON Voltage vs Collector
Current

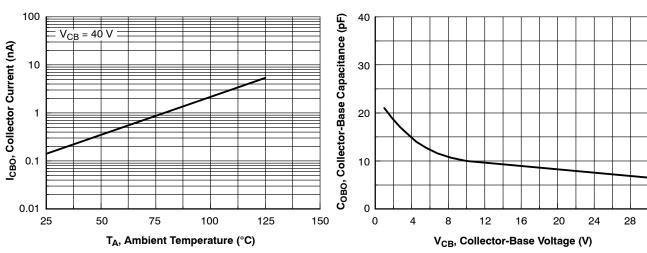


Figure 5. Collector-Cutoff Current vs Ambient Temperature

Figure 6. Collector-Base Capacitance vs Collector-Base Voltage

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TYPICAL CHARACTERISTICS (Continued)

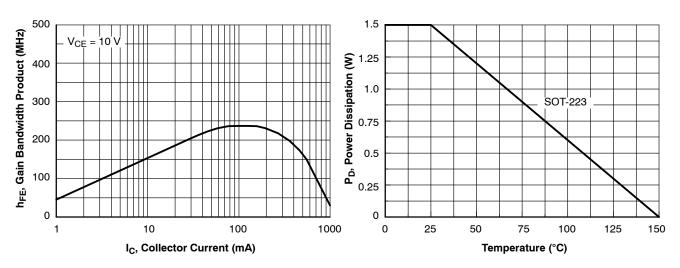
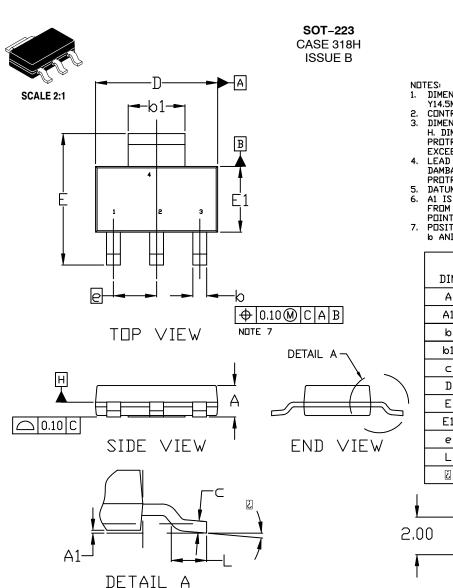


Figure 7. Gain Bandwidth Product vs Collector Current

Figure 8. Power Dissipation vs Ambient Temperature



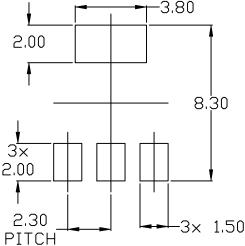


DATE 13 MAY 2020

- DIMENSIONING AND TOLERANCING PER ASME
- DIMENSIDNING AND TOLERANCING PER ASME Y14.5M, 2009.
 CONTROLLING DIMENSION: MILLIMETERS DIMENSIONS D & E1 ARE DETERMINED AT DATUM H. DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSIONS DR GATE BURRS. SHALL NOT EXCEED 0.23mm PER SIDE.
 LEAD DIMENSIONS & AND &1 DO NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBBAR PROTRUSION. ALLOWABLE DAMBBAR PROTRUSION IS 0.08mm PER SIDE.
 DATUMS A AND B ARE DETERMINED AT DATUM H. A1 IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT OF THE PACKAGE BODY.
 POSITIONAL TOLERANCE APPLIES TO DIMENSIONS & AND &1.

- b AND b1.

	MILLIMETERS			
DIM	MIN.	N□M.	MAX.	
Α			1.80	
A1	0.02	0.06	0.11	
b	0.60	0.74	0.88	
b1	2.90	3.00	3.10	
С	0.24		0.35	
D	6.30	6.50	6.70	
E	6.70	7.00	7.30	
E1	3,30	3.50	3.70	
е	2.30 BSC			
L	0.25			
į.	0*		10°	



GENERIC MARKING DIAGRAM*



= Assembly Location Υ

= Year

W = Work Week

XXXXX = Specific Device Code

= Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the IIN Semiconductor Soldering and Mounting Techniques Reference Manual, SILDERRM/D.

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