

SANYO Semiconductors DATA SHEET

2SA1729 — PNP Epitaxial Planar Silicon Transistor High-Speed Switching Applications

Features

- · Adoption of FBET, MBIT processes.
- Large current capacity.
- · Low collector-to-emitter saturation voltage.
- · High-speed switching.
- · Small-sized package.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		-50	V
Collector-to-Emitter Voltage	VCEO		-40	V
Emitter-to-Base Voltage	VEBO		-5	V
Collector Current	IC		-1.5	А
Collector Current (Pulse)	ICP		-3	Α
Collector Dissipation	PC	Mounted on a ceramic board (250mm ² ×0.8mm)	1.3	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
	Symbol	Conditions	min	typ	max	UIIIL
Collector Cutoff Current	ICBO	V _{CB} =-40V, I _E =0A			-1	μΑ
Emitter Cutoff Current	IEBO	VEB=-3V, IC=0A			-1	μΑ

Marking: AG Continued on next page.

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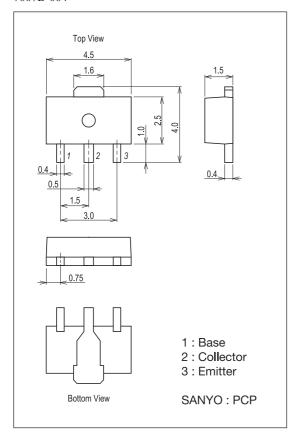
Parameter	Symbol	Odisi	Ratings			1.1-4
		Conditions	min	typ	max	Unit
DC Current Gain	hFE1	VCE=-2V, IC=-100mA	70*		280*	
	hFE2	V _{CE} =-2V, I _C =-1.5A	25			
Gain-Bandwidth Product	fT	V _{CE} =-2V, I _C =-100mA		300		MHz
Output Capacitance	Cob	V _{CB} =-10V, f=1MHz		18		pF
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =-800mA, I _B =-40mA		-0.3	-0.8	V
Base-to-Emitterr Saturation Voltage	V _{BE} (sat)	IC=-800mA, IB=-40mA		-0.9	-1.3	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =-10μA, I _E =0A	-50			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=-1mA, RBE=∞	-40			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =-10μA, I _C =0A	-5			V
Turn-ON Time	ton	See specified Test Circuit.		50	100	ns
Storage Time	t _{stg}	See specified Test Circuit.		120	220	ns
Turn-OFF Time	toff	See specified Test Circuit.		150	300	ns

^{*:} The 2SA1729 is classified by 100mA hFE as follows:

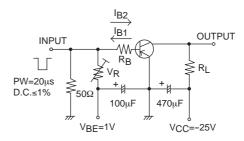
Rank	Q	R	S	
hFE	70 to 140	100 to 200	140 to 280	

Package Dimensions

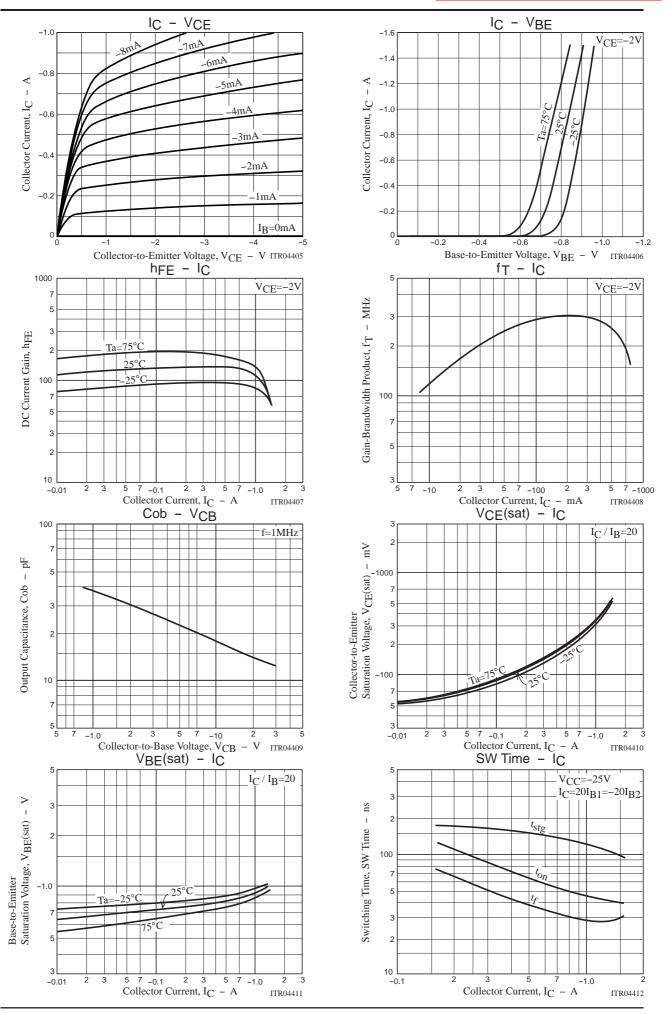
unit : mm (typ) 7007B-004

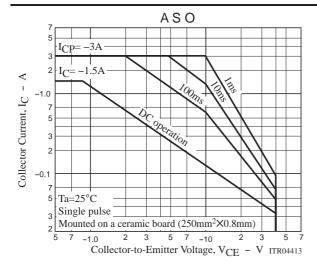


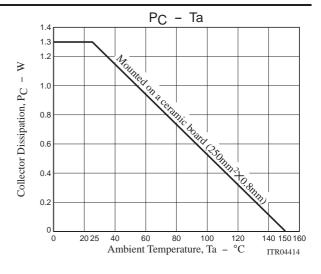
Switching Time Test Circuit



$$I_{C}=20I_{B1}=-20I_{B2}=-800mA$$







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