

Motion SPM[®] 45 Series

FNB43060T2

FNB43060T2 is an advanced Motion SPM 45 module providing a fully-featured, high-performance inverter output stage for AC Induction, BLDC, and PMSM motors. These modules integrate optimized gate drive of the built-in IGBTs to minimize EMI and losses, while also providing multiple on-module protection features including under-voltage lockouts, over-current shutdown, thermal monitoring of drive IC, and fault reporting. The built-in, high-speed HVIC requires only a single supply voltage and translates the incoming logic-level gate inputs to the high-voltage, high-current drive signals required to properly drive the module's internal IGBTs. Separate negative IGBT terminals are available for each phase to support the widest variety of control algorithms.

Features

- UL Certified No. E209204 (UL1557)
- 600 V – 30 A 3-Phase IGBT Inverter with Integral Gate Drivers and Protection
- Low Thermal Resistance Using Ceramic Substrate
- Low-Loss, Short-Circuit Rated IGBTs
- Built-In Bootstrap Diodes and Dedicated Vs Pins Simplify PCB Layout
- Built-In NTC Thermistor for Temperature Monitoring
- Separate Open-Emitter Pins from Low-Side IGBTs for Three-Phase Current Sensing
- Single-Grounded Power Supply
- Isolation Rating: 2000 V_{rms} / min.

Applications

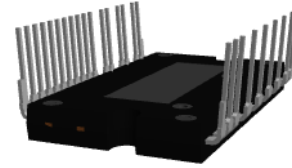
- Motion Control – Home Appliance / Industrial Motor

Integrated Power Functions

- 600 V – 30 A IGBT inverter for three-phase DC / AC power conversion (please refer to Figure 2)

Integrated Drive, Protection and System Control Functions

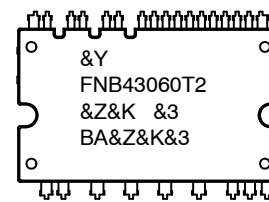
- For Inverter High-side IGBTs: gate drive circuit, high-voltage isolated high-speed level shifting control circuit Under-Voltage Lock-Out Protection (UVLO)(Note: Available bootstrap circuit example is given in Figure 14)
- For Inverter Low-side IGBTs: gate drive circuit, Short-Circuit Protection (SCP) control supply circuit Under-Voltage Lock-Out protection (UVLO)
- Fault Signaling: corresponding to UVLO (low-side supply) and SC faults
- Input Interface: High-active interface, works with 3.3 / 5 V logic, Schmitt trigger input



3D Package Drawing (Click to Activate 3D Content)

**SPMAA-C26 / 26LD, PDD STD CERAMIC TYPE,
LONG LEAD DUAL FORM TYPE
(SPMAB-C26)
CASE MODFC**

MARKING DIAGRAM



\$Y = onsemi Logo
 &Z = Assembly Plant Code
 &K = 2-Digits Lot Run Traceability Code
 &3 = 3-Digit Date Code
 BA = Specific Product Name
 FNB43060T2 = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 13 of this data sheet.

Related Resources

- [AN-9084 – Smart Power Module, Motion SPM[®] 45 H V3 Series User's Guide](#)
- [AN-9072 – Smart Power Module Motion SPM[®] in SPM45H Thermal Performance Information](#)
- [AN-9071 – Smart Power Module Motion SPM[®] in SPM45H Mounting Guidance](#)
- [AN-9760 – PCB Design Guidance for SPM[®]](#)

FNB43060T2

PIN CONFIGURATION

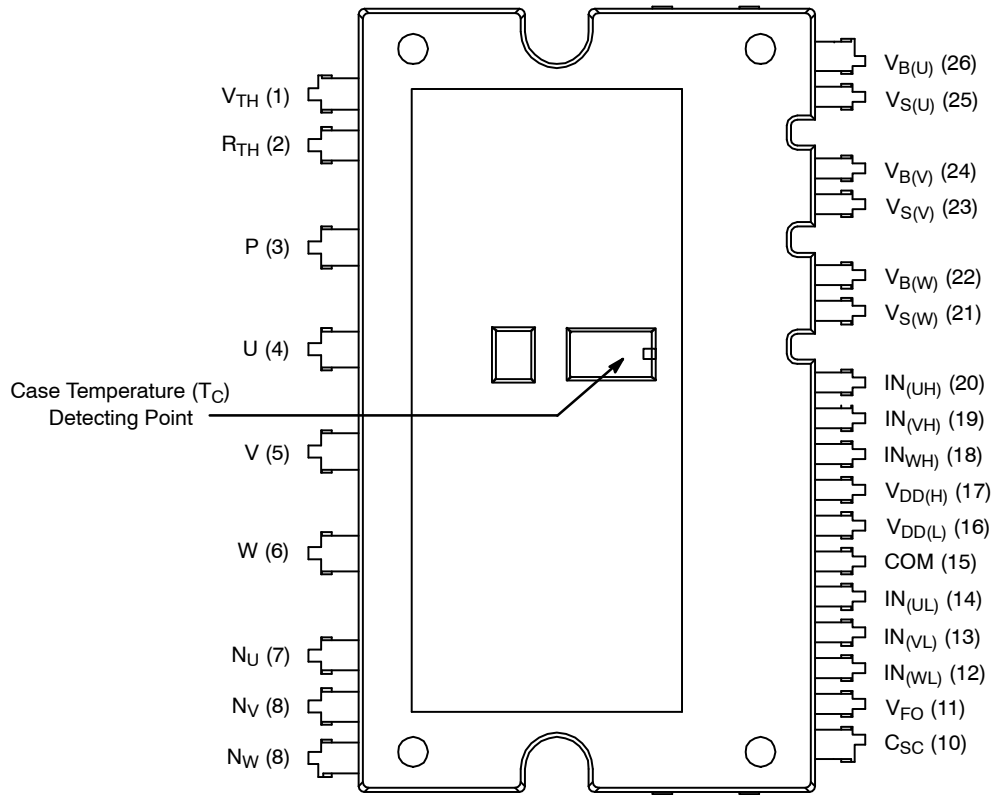


Figure 1. Pin Configuration – Top View

PIN DESCRIPTIONS

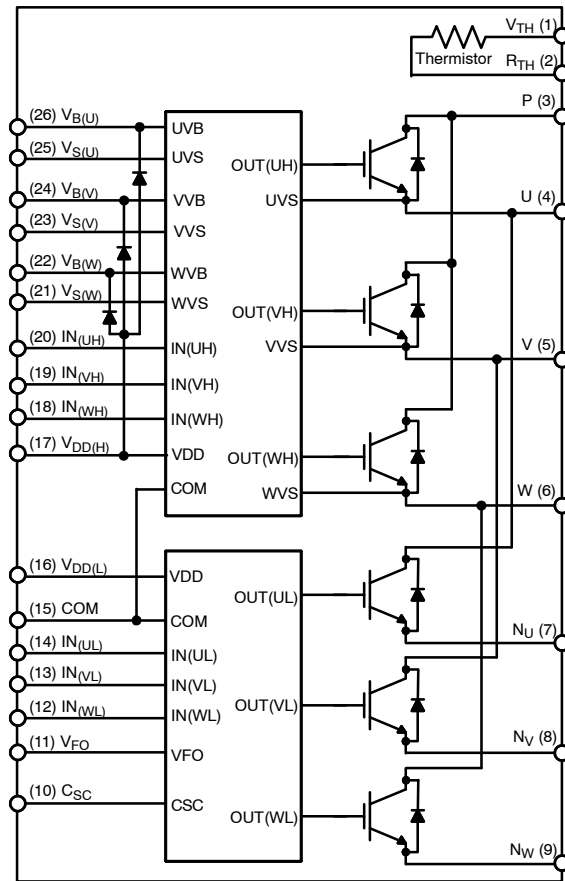
Pin Number	Pin Name	Pin Description
1	V _{TH}	Thermistor Bias Voltage
2	R _{TH}	Series Resistor for the Use of Thermistor (Temperature Detection)
3	P	Positive DC–Link Input
4	U	Output for U–Phase
5	V	Output for V–Phase
6	W	Output for W–Phase
7	N _U	Negative DC–Link Input for U–Phase
8	N _V	Negative DC–Link Input for V–Phase
9	N _W	Negative DC–Link Input for W–Phase
10	C _{SC}	Shut Down Input for Short–circuit Current Detection Input
11	V _{FO}	Fault Output
12	IN _(WL)	Signal Input for Low–Side W–Phase
13	IN _(VL)	Signal Input for Low–Side V–Phase
14	IN _(UL)	Signal Input for Low–Side U–Phase
15	COM	Common Supply Ground
16	V _{DD(L)}	Low–Side Common Bias Voltage for IC and IGBTs Driving
17	V _{DD(H)}	High–Side Common Bias Voltage for IC and IGBTs Driving

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PIN DESCRIPTIONS (continued)

Pin Number	Pin Name	Pin Description
18	IN _(WH)	Signal Input for High-Side W-Phase
19	IN _(VH)	Signal Input for High-Side V-Phase
20	IN _(UH)	Signal Input for High-Side U-Phase
21	V _{S(W)}	High-Side Bias Voltage Ground for W-Phase IGBT Driving
22	V _{B(W)}	High-Side Bias Voltage for W-Phase IGBT Driving
23	V _{S(V)}	High-Side Bias Voltage Ground for V-Phase IGBT Driving
24	V _{B(V)}	High-Side Bias Voltage for V-Phase IGBT Driving
25	V _{S(U)}	High-Side Bias Voltage Ground for U-Phase IGBT Driving
26	V _{B(U)}	High-Side Bias Voltage for U-Phase IGBT Driving

INTERNAL EQUIVALENT CIRCUIT AND INPUT/OUTPUT PINS



Notes:

1. Inverter high-side is composed of three IGBTs, freewheeling diodes, and one control IC for each IGBT.
2. Inverter low-side is composed of three IGBTs, freewheeling diodes, and one control IC for each IGBT. It has gate drive and protection functions.
3. Inverter power side is composed of four inverter DC-link input terminals and three inverter output terminals.

Figure 2. Internal Block Diagram

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ABSOLUTE MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Rating	Unit
INVERTER PART				
V_{PN}	Supply Voltage	Applied between P – N_U , N_V , N_W	450	V
$V_{PN(\text{Surge})}$	Supply Voltage (Surge)	Applied between P – N_U , N_V , N_W	500	V
V_{CES}	Collector – Emitter Voltage		600	V
$\pm I_C$	Each IGBT Collector Current	$T_C = 25^\circ\text{C}$, $T_J < 150^\circ\text{C}$	30	A
$\pm I_{CP}$	Each IGBT Collector Current (Peak)	$T_C = 25^\circ\text{C}$, $T_J < 150^\circ\text{C}$, Under 1 ms Pulse Width (Note 4)	60	A
P_C	Collector Dissipation	$T_C = 25^\circ\text{C}$ per One Chip (Note 4)	59	W
T_J	Operating Junction Temperature		-40 ~ 150	$^\circ\text{C}$

CONTROL PART

V_{DD}	Control Supply Voltage	Applied between $V_{DD(H)}$, $V_{DD(L)}$ – COM	20	V
V_{BS}	High-Side Control Bias Voltage	Applied between $V_{B(U)} - V_{S(U)}$, $V_{B(V)} - V_{S(V)}$, $V_{B(W)} - V_{S(W)}$	20	V
V_{IN}	Input Signal Voltage	Applied between $IN_{(UH)}$, $IN_{(VH)}$, $IN_{(WH)}$, $IN_{(UL)}$, $IN_{(VL)}$, $IN_{(WL)}$ – COM	-0.3 ~ $V_{DD} + 0.3$	V
V_{FO}	Fault Output Supply Voltage	Applied between V_{FO} – COM	-0.3 ~ $V_{DD} + 0.3$	V
I_{FO}	Fault Output Current	Sink Current at V_{FO} pin	1	mA
V_{SC}	Current-Sensing Input Voltage	Applied between C_{SC} – COM	-0.3 ~ $V_{DD} + 0.3$	V

BOOTSTRAP DIODE PART

V_{RRM}	Maximum Repetitive Reverse Voltage		600	V
I_F	Forward Current	$T_C = 25^\circ\text{C}$, $T_J < 150^\circ\text{C}$	0.5	A
I_{FP}	Forward Current (Peak)	$T_C = 25^\circ\text{C}$, $T_J < 150^\circ\text{C}$, Under 1 ms Pulse Width (Note 4)	2.0	A
T_J	Operating Junction Temperature		-40 ~ 150	$^\circ\text{C}$

TOTAL SYSTEM

$V_{PN(\text{PROT})}$	Self Protection Supply Voltage Limit (Short-Circuit Protection Capability)	$V_{DD} = V_{BS} = 13.5 \sim 16.5 \text{ V}$, $T_J = 150^\circ\text{C}$, Non-Repetitive, < 2 μs	400	V
T_C	Module Case Operation Temperature	See Figure 1	-40 ~ 125	$^\circ\text{C}$
T_{STG}	Storage Temperature		-40 ~ 125	$^\circ\text{C}$
V_{ISO}	Isolation Voltage	60 Hz, Sinusoidal, AC 1 Minute, Connect Pins to Heat Sink Plate	2000	V_{rms}

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

4. These values had been made an acquisition by the calculation considered to design factor.

THERMAL RESISTANCE

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-c)Q}$	Junction to Case Thermal Resistance (Note 5)	Inverter IGBT part, (Per 1 / 6 Module)	–	–	2.1	$^\circ\text{C/W}$
$R_{th(j-c)F}$		Inverter FWDi part, (Per 1 / 6 Module)	–	–	2.8	$^\circ\text{C/W}$

5. For the measurement point of case temperature (T_C), please refer to Figure 1.

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions		Min	Typ	Max	Unit	
INVERTER PART								
V _{CE(SAT)}		Collector – Emitter Saturation Voltage	V _{DD} = V _{BS} = 15 V, V _{IN} = 5 V	I _C = 30 A, T _J = 25°C	–	1.65	2.25	V
V _F		FWDi Forward Voltage	V _{IN} = 0 V	I _F = 30 A, T _J = 25°C	–	2.00	2.60	V
HS	t _{ON}	Switching Times	V _{PN} = 300 V, V _{DD} = V _{BS} = 15 V, I _C = 30 A, T _J = 25°C V _{IN} = 0 V ↔ 5 V, Inductive load (Note 6)	0.45	0.85	1.35	μs	
	t _{C(ON)}			–	0.20	0.50	μs	
	t _{OFF}			–	0.70	1.20	μs	
	t _{C(OFF)}			–	0.15	0.45	μs	
	t _{rr}			–	0.10	–	μs	
LS	t _{ON}	Switching Times	V _{PN} = 300 V, V _{DD} = V _{BS} = 15 V, I _C = 30 A, T _J = 25°C V _{IN} = 0 V ↔ 5 V, Inductive load (Note 6)	0.25	0.90	1.40	μs	
	t _{C(ON)}			–	0.30	0.60	μs	
	t _{OFF}			–	0.80	1.30	μs	
	t _{C(OFF)}			–	0.15	0.45	μs	
	t _{rr}			–	0.15	–	μs	
I _{CES}		Collector – Emitter Leakage Current	V _{CE} = V _{CES}		–	–	1.00	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

6. t_{ON} and t_{OFF} include the propagation delay of the internal drive IC. $t_{C(ON)}$ and $t_{C(OFF)}$ are the switching times of IGBT itself under the given gate driving condition internally. For the detailed information, please see Figure 3.

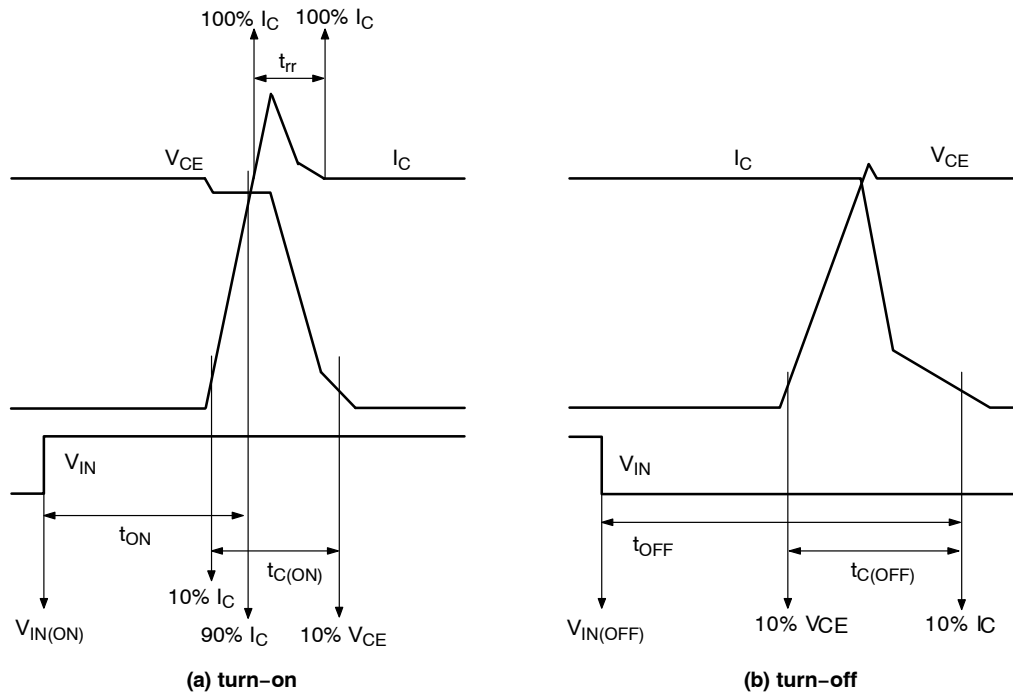


Figure 3. Switching Time Definition

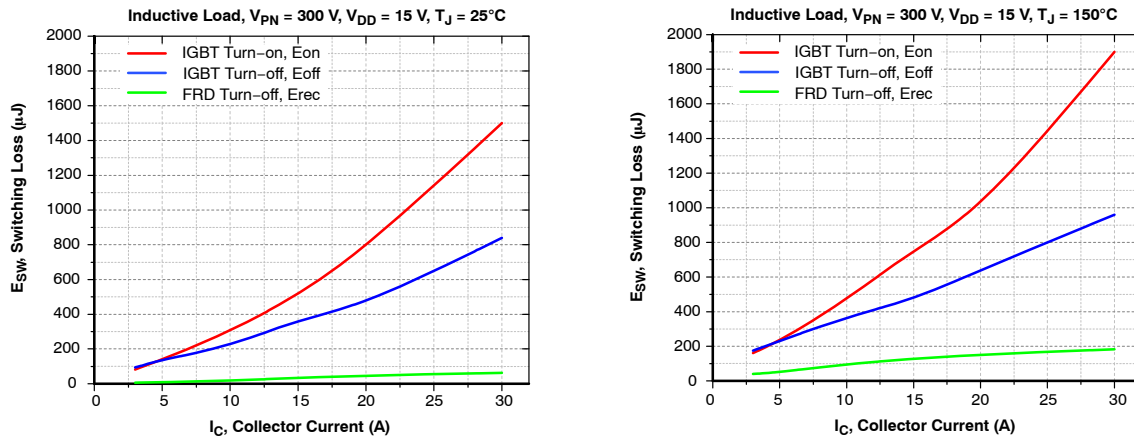


Figure 4. Switching Loss Characteristics (Typical)

Table 1. ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
CONTROL PART							
I _{QDDH}	Quiescent V _{DD} Supply Current	V _{DD(H)} = 15 V, I _{N(UH,VH,WH)} = 0 V	V _{DD(H)} – COM	–	–	0.10	mA
I _{QDDL}		V _{DD(L)} = 15 V, I _{N(UL,VL,WL)} = 0 V	V _{DD(L)} – COM	–	–	2.65	mA
I _{PDDH}	Operating V _{DD} Supply Current	V _{DD(H)} = 15 V, f _{PWM} = 20 kHz, duty = 50%, Applied to One PWM Signal Input for High–Side	V _{DD(H)} – COM	–	–	0.15	mA
I _{PDDL}		V _{DD(L)} = 15 V, f _{PWM} = 20 kHz, duty = 50%, Applied to One PWM Signal Input for Low–Side	V _{DD(L)} – COM	–	–	4.00	mA
I _{QBS}	Quiescent V _{BS} Supply Current	V _{BS} = 15 V, I _{N(UH, VH, WH)} = 0 V	V _{B(U)} – V _{S(U)} , V _{B(V)} – V _{S(V)} , V _{B(W)} – V _{S(W)}	–	–	0.30	mA
I _{PBS}	Quiescent V _{BS} Supply Current	V _{DD} = V _{BS} = 15 V, f _{PWM} = 20 kHz, Duty = 50%, Applied to One PWM Signal Input for High–Side	V _{B(U)} – V _{S(U)} , V _{B(V)} – V _{S(V)} , V _{B(W)} – V _{S(W)}	–	–	2.00	mA
V _{FOH}	Fault Output Voltage	V _{SC} = 0 V, V _{FO} Circuit: 4.7 kΩ to 5 V Pull–up		4.5	–	–	V
V _{FOL}		V _{SC} = 1 V, V _{FO} Circuit: 4.7 kΩ to 5 V Pull–up		–	–	0.5	V
V _{SC(ref)}	Short Circuit Trip Level	V _{DD} = 15 V (Note 7)	C _{SC} – COM	0.45	0.50	0.55	V
UV _{DDD}	Supply Circuit Under–Voltage Protection	Detection level		10.5	–	13.0	V
UV _{DDR}		Reset level		11.0	–	13.5	V
UV _{BSD}		Detection level		10.0	–	12.5	V
UV _{BSR}		Reset level		10.5	–	13.0	V
t _{FOD}	Fault–Out Pulse Width			30	–	–	μs
V _{IN(ON)}	ON Threshold Voltage	Applied between I _{N(UH, VH, WH)} – COM, I _{N(UL, VL, WL)} – COM		–	–	2.6	V
V _{IN(OFF)}	OFF Threshold Voltage			0.8	–	–	V
R _{TH}	Resistance of Thermistor	@T _{TH} = 25°C, (Note 8)		–	47	–	kΩ
		@T _{TH} = 100°C		–	2.9	–	kΩ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

7. Short-circuit current protection functioning only at the low-sides.

8. T_{TH} is the temperature of thermistor itself. To know case temperature (T_C), please make the experiment considering your application.

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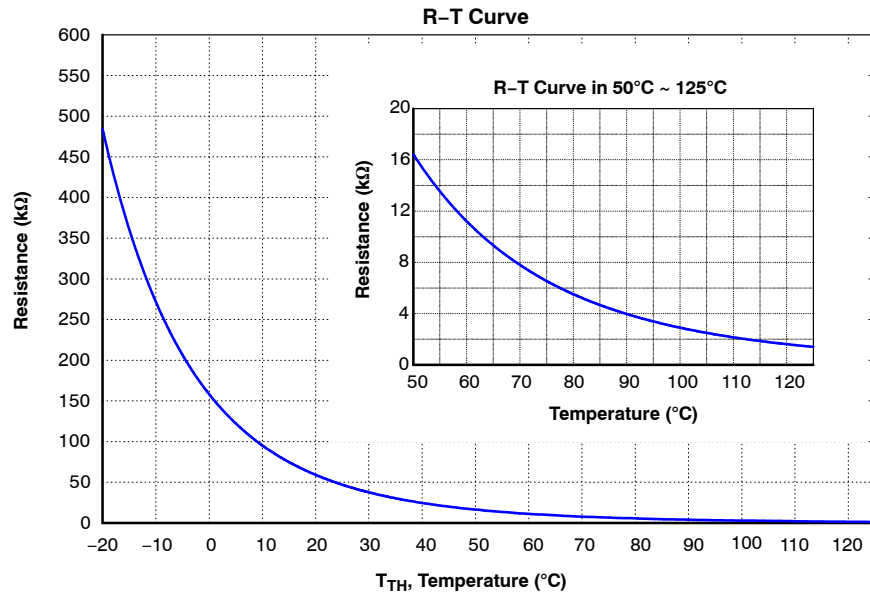


Figure 5. R-T Curve of The Built-In Thermistor

Table 2. ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
BOOTSTRAP DIODE PART						
V_F	Forward Voltage	$I_F = 0.1 \text{ A}$, $T_C = 25^\circ\text{C}$	–	2.5	–	V
t_{rr}	Reverse-Recovery Time	$I_F = 0.1 \text{ A}$, $dI_F / dt = 50 \text{ A}/\mu\text{s}$, $T_J = 25^\circ\text{C}$	–	80	–	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

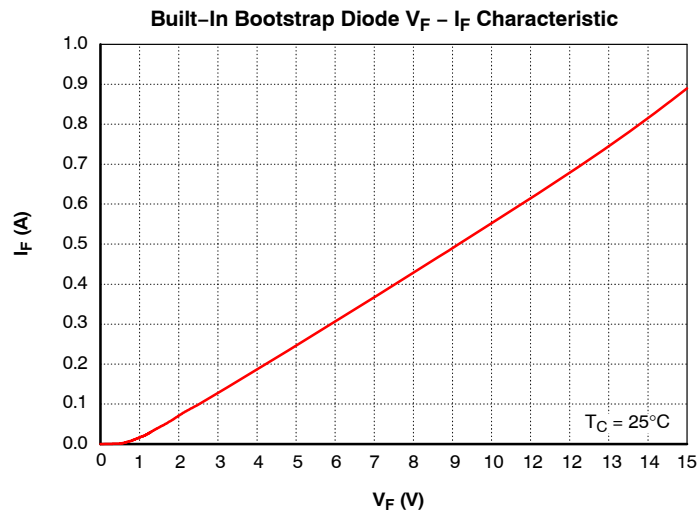


Figure 6. Built-In Bootstrap Diode Characteristic

NOTE:

9. Built-in bootstrap diode includes around 15 Ω resistance characteristic.

Table 3. RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{PN}	Supply Voltage	Applied between P – N _U , N _V , N _W	–	300	400	V
V_{DD}	Control Supply Voltage	Applied between V _{DD(H)} , V _{DD(L)} – COM	13.5	15.0	16.5	V
V_{BS}	High – Side Bias Voltage	Applied between V _{B(U)} – V _{S(U)} , V _{B(V)} – V _{S(V)} , V _{B(W)} – V _{S(W)}	13.0	15.0	18.5	V
dV_{DD}/dt , dV_{BS}/dt	Control Supply Variation		–1	–	1	V/ μ s
t_{dead}	Blanking Time for Preventing Arm – Short	For each input signal	1.0	–	–	μ s
f_{PWM}	PWM Input Signal	$-40^{\circ}\text{C} \leq T_C \leq 125^{\circ}\text{C}$, $-40^{\circ}\text{C} \leq T_J \leq 150^{\circ}\text{C}$	–	–	20	kHz
V_{SEN}	Voltage for Current Sensing	Applied between N _U , N _V , N _W – COM (Including surge voltage)	–4		4	V
$P_{WIN(ON)}$	Minimum Input Pulse Width	$V_{DD} = V_{BS} = 15\text{ V}$, $I_C \leq 60\text{ A}$, Wiring Inductance between N _U , V, W and DC Link N < 10 nH (Note 10)	1.2	–	–	μ s
$P_{WIN(OFF)}$			1.2	–	–	
T_J	Junction Temperature		–40	–	150	$^{\circ}\text{C}$

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

10. This product might not make response if input pulse width is less than the recommended value.

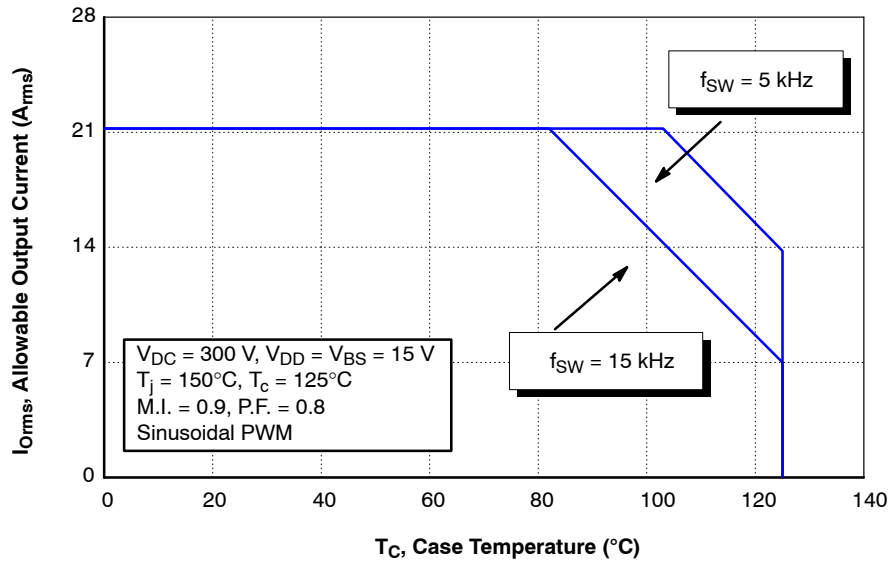


Figure 7. Allowable Maximum Output Current

NOTE:

11. This allowable output current value is the reference data for the safe operation of this product. This may be different from the actual application and operating condition.

Table 4. MECHANICAL CHARACTERISTICS AND RATINGS

Parameter	Conditions		Min	Typ	Max	Unit
Device Flatness	See Figure 8		0	–	+120	μm
Mounting Torque	Mounting Screw: M3 See Figure 9	Recommended 0.7 N • m	0.6	0.7	0.8	N • m
		Recommended 7.1 kg • cm	6.2	7.1	8.1	kg • cm
Weight			–	11.00	–	g

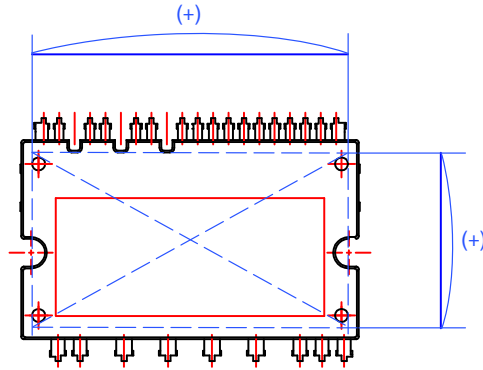


Figure 8. Flatness Measurement Position

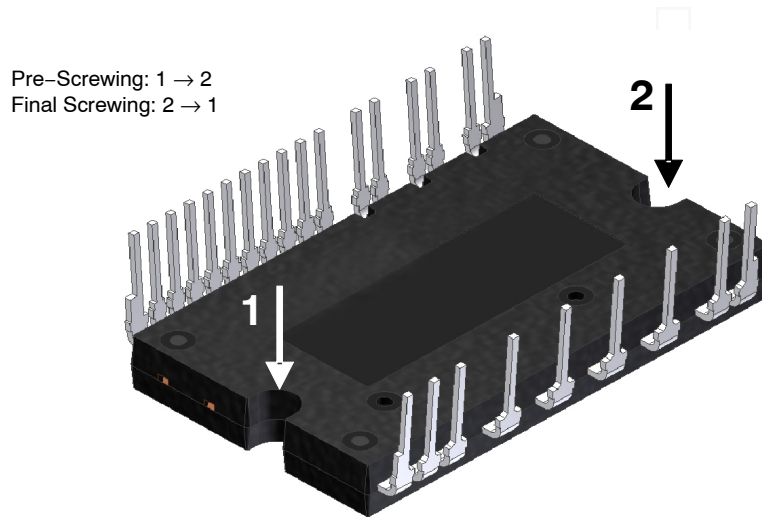
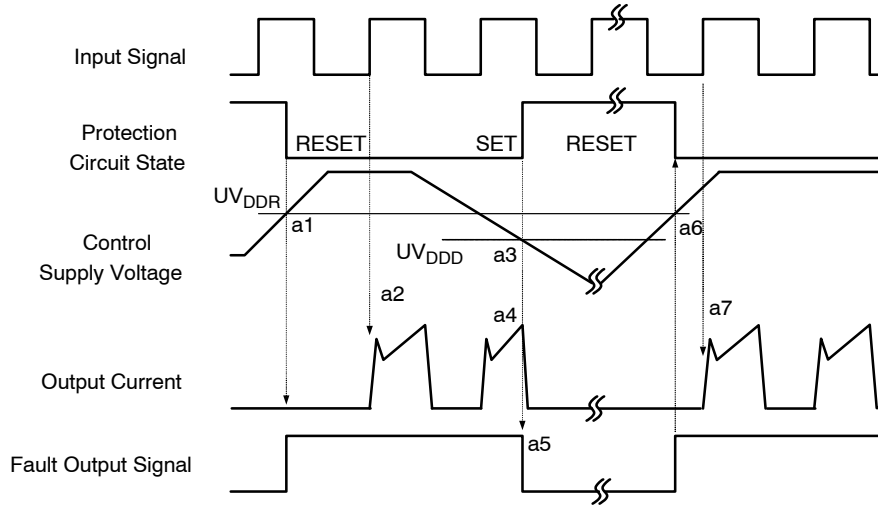


Figure 9. Mounting Screws Torque Order

NOTES:

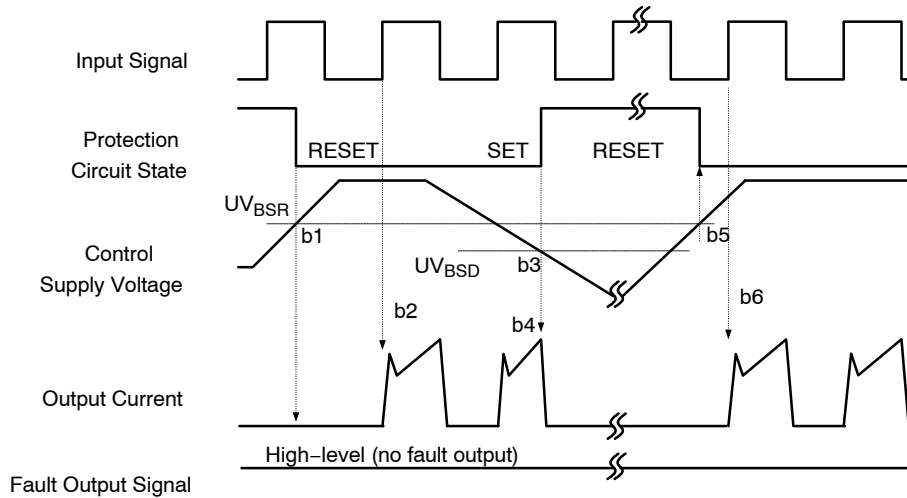
12. Do not make over torque when mounting screws. Much mounting torque may cause ceramic cracks, as well as bolts and Al heat-sink destruction.
13. Avoid one-sided tightening stress. Figure 9 shows the recommended torque order for mounting screws. Uneven mounting can cause the ceramic substrate of package to be damaged. The pre-screwing torque is set to 20 ~ 30% of maximum torque rating.

TIME CHARTS OF PROTECTIVE FUNCTION



- a1: Control supply voltage rises: after the voltage rises UV_{DDR} , the circuits start to operate when next input is applied.
a2: Normal operation: IGBT ON and carrying current.
a3: Under-voltage detection (UV_{DDD}).
a4: IGBT OFF in spite of control input condition.
a5: Fault output operation starts with a fixed pulse width.
a6: Under-voltage reset (UV_{DDR}).
a7: Normal operation: IGBT ON and carrying current by triggering next signal from LOW to HIGH.

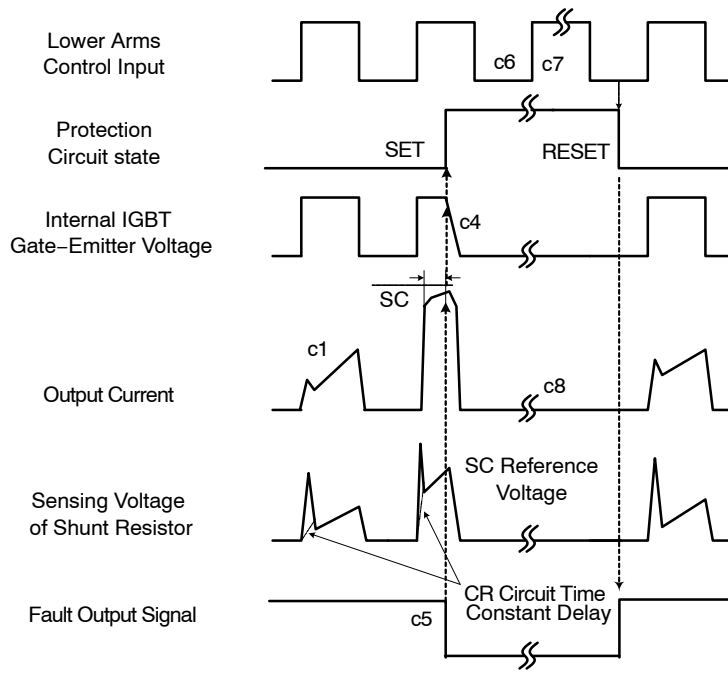
Figure 10. Under-Voltage Protection (Low-Side)



- b1: Control supply voltage rises: after the voltage reaches UV_{BSR} , the circuits start to operate when next input is applied.
b2: Normal operation: IGBT ON and carrying current.
b3: Under voltage detection (UV_{BSD}).
b4: IGBT OFF in spite of control input condition, but there is no fault output signal.
b5: Under-voltage reset (UV_{BSR}).
b6: Normal operation: IGBT ON and carrying current by triggering next signal from LOW to HIGH.

Figure 11. Under-Voltage Protection (High-Side)

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(with the external sense resistance and RC filter connection)

c1: Normal operation: IGBT ON and carrying current.

c2: Short circuit current detection (SC trigger).

c3: All low-side IGBT's gate are hard interrupted.

c4: All low-side IGBTs turn OFF.

c5: Fault output operation starts with a fixed pulse width.

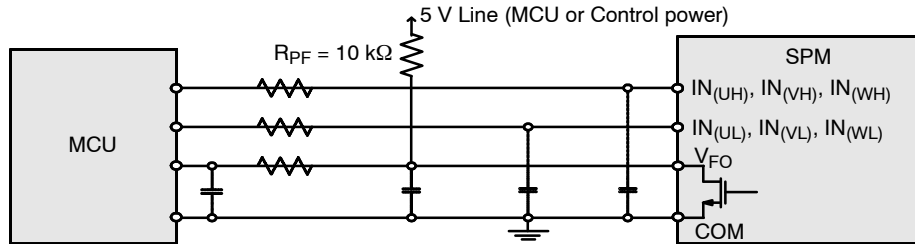
c6: Input HIGH: IGBT ON state, but during the active period of fault output the IGBT doesn't turn ON.

c7: Fault output operation finishes, but IGBT doesn't turn on until triggering next signal from LOW to HIGH.

c8: Normal operation: IGBT ON and carrying current.

Figure 12. Short-Circuit Protection (Low-Side Operation Only)

INPUT/OUTPUT INTERFACE CIRCUIT



NOTE:

14. RC coupling at each input might change depending on the PWM control scheme used in the application and the wiring impedance of the application's printed circuit board. The input signal section of the Motion SPM 45 product integrates 5 kΩ (typ.) pull-down resistor. Therefore, when using an external filtering resistor, please pay attention to the signal voltage drop at input terminal.

Figure 13. Recommended MCU I/O Interface Circuit

The diagram illustrates a three-phase motor drive system. A microcontroller (M-C-U) is connected to a motor (M) through a series of control and power stages. The control stage includes a Fault input and six gating signals (UH, VH, WH, UL, VL, WL). The power stage consists of two integrated circuits, HVIC and LVIC, which drive the motor phases. The HVIC (High Voltage IC) controls the U, V, and W phases, while the LVIC (Low Voltage IC) controls the N_U, N_V, and N_W phases. The motor is connected to the output of the HVIC and LVIC. The system also includes a temperature monitoring circuit (R_{TH}) and a short-circuit protection circuit (C_{SP15}, C_{SP05}, C_{SP05}). The input signals for short-circuit protection are U-Phase Current, V-Phase Current, and W-Phase Current.

NOTES:

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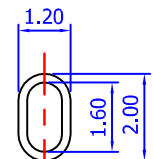
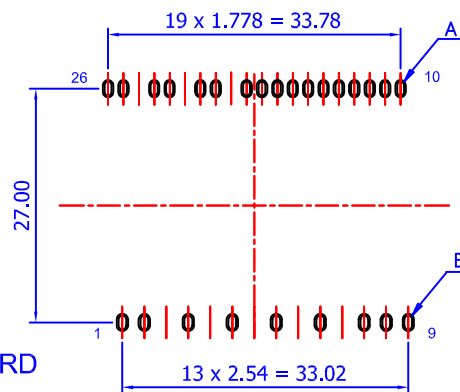
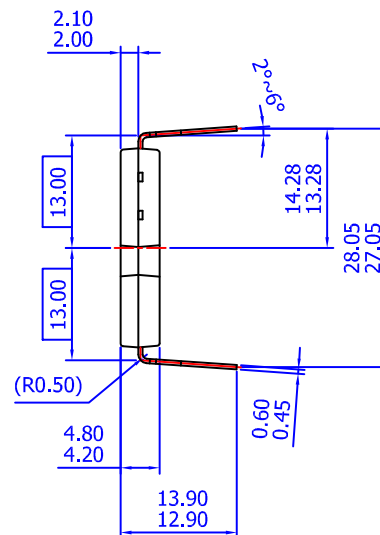
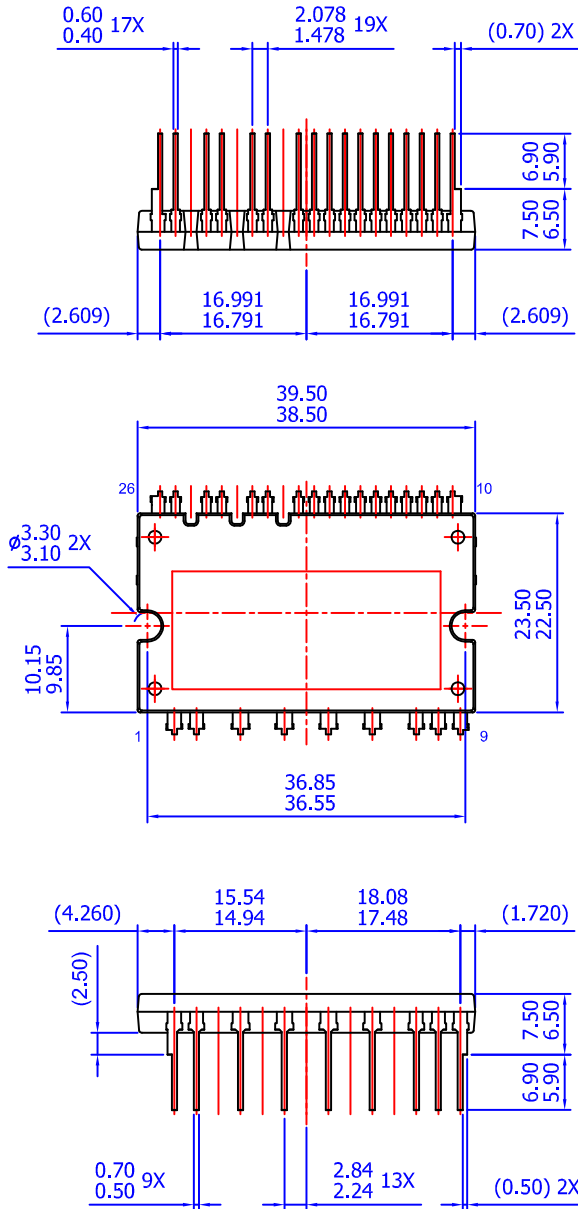
ORDERING INFORMATION

Device	Device Marking	Package	Shipping
FNB43060T2	FNB43060T2	SPMAA-C26 / 26LD, PDD STD CERAMIC TYPE, LONG LEAD DUAL FORM TYPE (Pb-Free)	72 Units / Tube

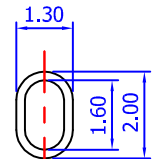
SPM is a registered trademark of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries.

SPMAA-C26 / 26LD, PDD STD CERAMIC TYPE, LONG LEAD DUAL FORM TYPE
CASE MODFC
ISSUE O

DATE 31 JAN 2017



DETAIL A
(SCALE N/A)



DETAIL B
(SCALE N/A)

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