

General Purpose Transistor

NPN Silicon

MMBT2222AWT1G, SMMBT2222AWT1G

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-323/SC-70 package which is designed for low power surface mount applications.

Features

- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V _{CEO}	40	Vdc
Collector - Base Voltage	V _{CBO}	75	Vdc
Emitter – Base Voltage	V _{EBO}	6.0	Vdc
Collector Current - Continuous	I _C	600	mAdc

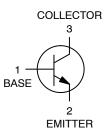
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board T _A = 25°C	P _D	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	280	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



SC-70 CASE 419 STYLE 3



MARKING DIAGRAM



P1 = Specific Device Code

M = Date Code

■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBT2222AWT1G	SC-70 (Pb-Free)	3,000 / Tape & Reel
SMMBT2222AWT1G	SC-70 (Pb-Free)	3,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic			Min	Max	Unit
OFF CHARACTERISTICS					•
Collector – Emitter Breakdown Voltage (Note $(I_C = 10 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	40	-	Vdc	
Collector – Base Breakdown Voltage (I _C = 10 μAdc, I _E = 0)		V _{(BR)CBO}	75	-	Vdc
Emitter – Base Breakdown Voltage (I _E = 10 µAdc, I _C = 0)		V _{(BR)EBO}	6.0	-	Vdc
Base Cutoff Current (V _{CE} = 60 Vdc, V _{EB} = 3.0 Vdc)		I _{BL}	-	20	nAdc
Collector Cutoff Current (V _{CE} = 60 Vdc, V _{EB} = 3.0 Vdc)		I _{CEX}	-	10	nAdc
ON CHARACTERISTICS (Note 1)					
DC Current Gain (Note 1) $ (I_{C} = 0.1 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}) $ $ (I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}) $ $ (I_{C} = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}) $ $ (I_{C} = 150 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}) $ $ (I_{C} = 500 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}) $ $ (I_{C} = 500 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}) $			35 50 75 100 40	- - 300 -	-
Collector – Emitter Saturation Voltage (Note 1) ($I_C = 150 \text{ mAdc}$, $I_B = 15 \text{ mAdc}$) ($I_C = 500 \text{ mAdc}$, $I_B = 50 \text{ mAdc}$)			- -	0.3 1.0	Vdc
Base – Emitter Saturation Voltage (Note 1) $(I_C = 150 \text{ mAdc}, I_B = 15 \text{ mAdc})$ $(I_C = 500 \text{ mAdc}, I_B = 50 \text{ mAdc})$			0.6	1.2 2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current – Gain – Bandwidth Product $(I_C = 20 \text{ mAdc}, V_{CE} = 20 \text{ Vdc}, f = 100 \text{ MHz})$		f _T	300	-	MHz
Output Capacitance ($V_{CB} = 10 \text{ Vdc}$, $I_E = 0$, $f = 1.0 \text{ MHz}$)		C _{obo}	-	8.0	pF
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)		C _{ibo}	-	30	pF
Input Impedance ($V_{CE} = 10 \text{ Vdc}$, $I_{C} = 10 \text{ mAdc}$, $f = 1.0 \text{ kHz}$)		h _{ie}	0.25	1.25	kΩ
Voltage Feedback Ratio (V _{CE} = 10 Vdc, I _C = 10 mAdc, f = 1.0 kHz)			-	4.0	X 10 ⁻⁴
Small – Signal Current Gain $(V_{CE} = 10 \text{ Vdc}, I_C = 10 \text{ mAdc}, f = 1.0 \text{ kHz})$			75	375	-
Output Admittance (V _{CE} = 10 Vdc, I _C = 10 mAdc, f = 1.0 kHz)			25	200	μmhos
Noise Figure (V _{CE} = 10 Vdc, I _C = 100 μ Adc, R _S = 1.0 k Ω , f = 1.0 kHz)			-	4.0	dB
SWITCHING CHARACTERISTICS					
Delay Time	(V _{CC} = 3.0 Vdc, V _{BE} = -0.5 Vdc,	t _d	_	10	Ī
Rise Time	$I_C = 150 \text{ mAdc}, I_{B1} = 15 \text{ mAdc})$	t _r	-	25	ns
Storage Time	(V _{CC} = 30 Vdc, I _C = 150 mAdc,	t _s	_	225	1
Fall Time $I_{B1} = I_{B2} = 15 \text{ mAdc},$			_	60	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 1. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

SWITCHING TIME EQUIVALENT TEST CIRCUITS

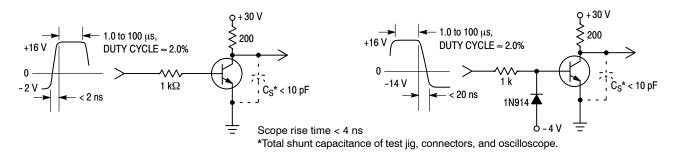


Figure 1. Turn-On Time

Figure 2. Turn-Off Time

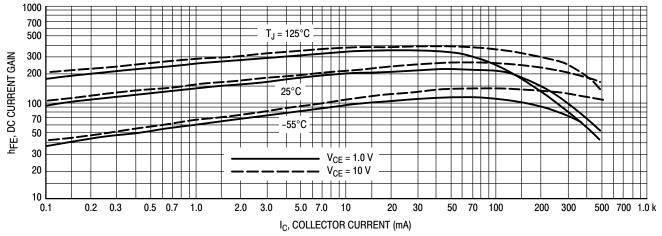


Figure 3. DC Current Gain

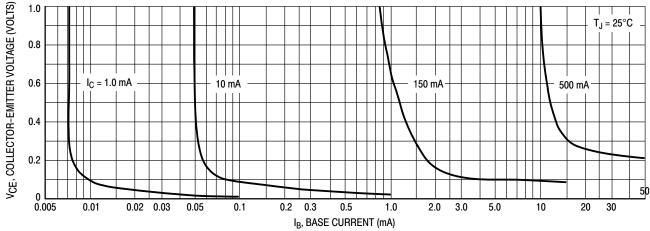


Figure 4. Collector Saturation Region

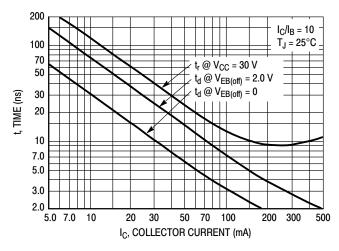


Figure 5. Turn - On Time

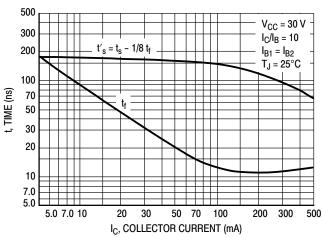


Figure 6. Turn - Off Time

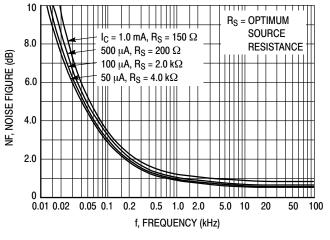


Figure 7. Frequency Effects

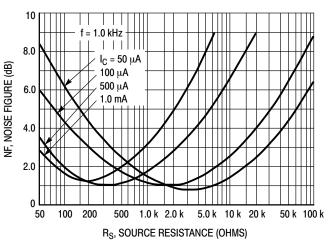


Figure 8. Source Resistance Effects

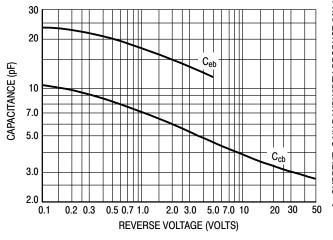


Figure 9. Capacitances

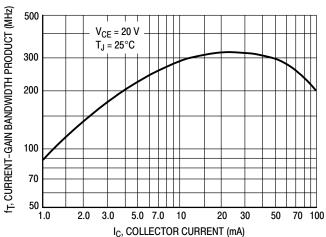


Figure 10. Current-Gain Bandwidth Product

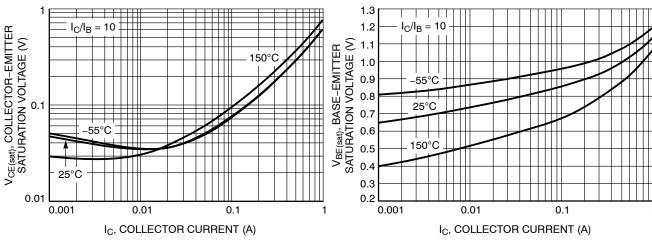


Figure 11. Collector Emitter Saturation Voltage vs. Collector Current

I_C, COLLECTOR CURRENT (A)

Figure 12. Base Emitter Saturation Voltage vs.

Collector Current

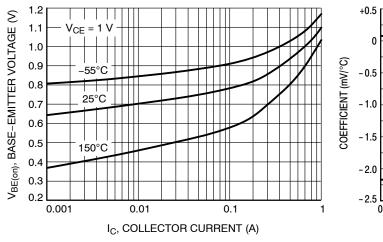


Figure 13. Base Emitter Voltage vs. Collector Current

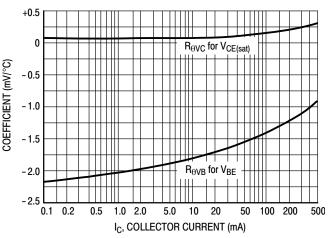


Figure 14. Temperature Coefficients

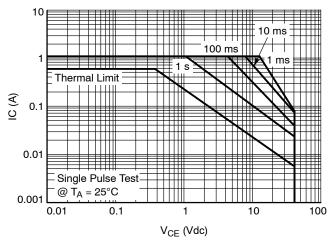


Figure 15. Safe Operating Area







SC-70 (SOT-323) CASE 419 ISSUE R

DATE 11 OCT 2022

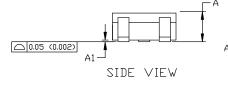
NOTES:

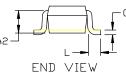
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH

	MILLIMETERS				INCHES		
DIM	MIN.	N□M.	MAX.	MIN.	N□M.	MAX.	
Α	0.80	0.90	1.00	0.032	0.035	0.040	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
A2		0.70 REF			0.028 BSC		
b	0.30	0.35	0.40	0.012	0.014	0.016	
С	0.10	0.18	0.25	0.004	0.007	0.010	
D	1.80	2.00	2.20	0.071	0.080	0.087	
E	1.15	1.24	1.35	0.045	0.049	0.053	
е	1.20	1.30	1.40	0.047	0.051	0.055	
e1	0.65 BSC				0.026 BS	C	
L	0.20	0.38	0.56	0.008	0.015	0.022	
HE	2.00	2.10	2.40	0.079	0.083	0.095	

SCALE 4:1

TOP VIEW





GENERIC MARKING DIAGRAM

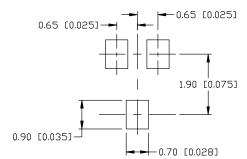


XX = Specific Device Code

M = Date Code

■ = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



For additional information on our Pb-Free strategy and soldering details, please download the ID Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

SOLDERING FOOTPRINT

STYLE 1: CANCELLED	STYLE 2: PIN 1. ANODE 2. N.C. 3. CATHODE	STYLE 3: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. CATHODE	
STYLE 6:	STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:	STYLE 11:
PIN 1. EMITTER	PIN 1. BASE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. CATHODE
2. BASE	2. EMITTER	2. SOURCE	2. CATHODE	2. ANODE	2. CATHODE
3. COLLECTOR	3. COLLECTOR	3. DRAIN	3. CATHODE-ANODE	3. ANODE-CATHODE	3. CATHODE

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