

# MSC3930-BT1

Preferred Device

## NPN RF Amplifier Transistor

- Pb-Free Package is Available



ON Semiconductor®

<http://onsemi.com>

### MAXIMUM RATINGS (T<sub>A</sub> = 25°C)

Rating	Symbol	Value	Unit
Collector-Base Voltage	V <sub>(BR)CBO</sub>	30	Vdc
Collector-Emitter Voltage	V <sub>(BR)CEO</sub>	20	Vdc
Emitter-Base Voltage	V <sub>(BR)EBO</sub>	5.0	Vdc
Collector Current — Continuous	I <sub>C</sub>	30	mAdc

### THERMAL CHARACTERISTICS

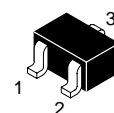
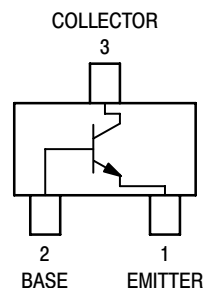
Characteristic	Symbol	Max	Unit
Power Dissipation	P <sub>D</sub>	200	mW
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 ~ +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)

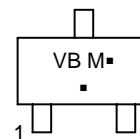
Characteristic	Symbol	Min	Max	Unit
Collector-Base Cutoff Current (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	—	0.1	μAdc
DC Current Gain <sup>(1)</sup> (V <sub>CB</sub> = 10 Vdc, I <sub>C</sub> = -1.0 mAdc)	h <sub>FE</sub>	70	140	—
Collector-Gain — Bandwidth Product (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = -1.0 mAdc)	f <sub>T</sub>	150	—	MHz
Reverse Transistor Capacitance (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 1.0 mAdc, f = 10.7 MHz)	C <sub>re</sub>	—	1.5	pF

1. Pulse Test: Pulse Width ≤ 300 μs, D.C. ≤ 2%.



SOT-323/SC-70  
CASE 419  
STYLE 3

### MARKING DIAGRAM



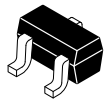
VB = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package  
(Note: Microdot may be in either location)

### ORDERING INFORMATION

Device	Package	Shipping†
MSC3930-BT1	SC-70	3000/Tape & Reel
MSC3930-BT1G	SC-70 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

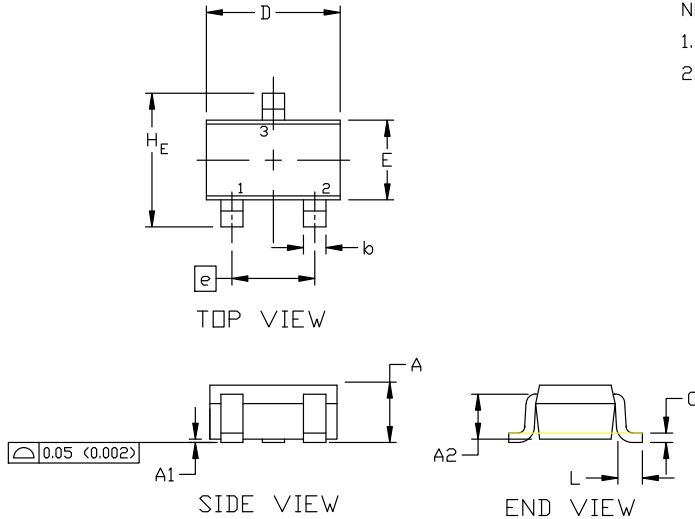
Preferred devices are recommended choices for future use and best overall value.



SCALE 4:1

SC-70 (SOT-323)  
CASE 419  
ISSUE R

DATE 11 OCT 2022

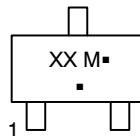


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH

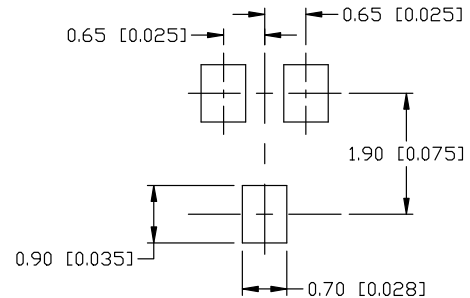
DIM	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.70 REF			0.028 BSC		
b	0.30	0.35	0.40	0.012	0.014	0.016
c	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.00	2.20	0.071	0.080	0.087
E	1.15	1.24	1.35	0.045	0.049	0.053
e	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC			0.026 BSC		
L	0.20	0.38	0.56	0.008	0.015	0.022
H <sub>E</sub>	2.00	2.10	2.40	0.079	0.083	0.095

GENERIC  
MARKING DIAGRAM



- XX = Specific Device Code
- M = Date Code
- = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.



\* For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

SOLDERING FOOTPRINT

- |   |   |   |  |   |   |
|---|---|---|--|---|---|
| STYLE 1:<br>CANCELLED                                 | STYLE 2:<br>PIN 1. ANODE<br>2. N.C.<br>3. CATHODE     | STYLE 3:<br>PIN 1. BASE<br>2. EMITTER<br>3. COLLECTOR | STYLE 4:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. ANODE       | STYLE 5:<br>PIN 1. ANODE<br>2. ANODE<br>3. CATHODE          |   |
| STYLE 6:<br>PIN 1. EMITTER<br>2. BASE<br>3. COLLECTOR | STYLE 7:<br>PIN 1. BASE<br>2. EMITTER<br>3. COLLECTOR | STYLE 8:<br>PIN 1. GATE<br>2. SOURCE<br>3. DRAIN      | STYLE 9:<br>PIN 1. ANODE<br>2. CATHODE<br>3. CATHODE-ANODE | STYLE 10:<br>PIN 1. CATHODE<br>2. ANODE<br>3. ANODE-CATHODE | STYLE 11:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. CATHODE |

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