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## Vishay Semiconductors

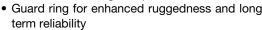
# High Performance Schottky Rectifier, 1.5 A

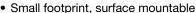


PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	1.5 A			
$V_{R}$	40 V			
V <sub>F</sub> at I <sub>F</sub>	0.34 V			
I <sub>RM</sub>	20 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
E <sub>AS</sub>	6.0 mJ			
Package	SMA (DO-214AC)			
Circuit configuration	Single			

#### **FEATURES**







• High frequency operation

 Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

Meets JESD 201 class 2 whisker test

AEC-Q101 qualified

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **APPLICATIONS**

- Switching power supplies
- Meter protection
- Reverse protection for power input to PC board circuits
- · Battery isolation and charging
- · Low threshold voltage diode
- · Freewheeling or by-pass diode
- Low voltage clamp

#### **DESCRIPTION**

The VS-15MQ040HM3 Schottky rectifier is designed to be used for low power applications where a reverse voltage of 40 V is encountered and surface mountable is required.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS VALUES UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	1.5	А	
V <sub>RRM</sub>		40	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	330	Α	
V <sub>F</sub>	2 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.43	V	
T <sub>J</sub>	Range	-40 to +150	°C	

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-15MQ040HM3	UNITS
Maximum DC reverse voltage	$V_R$	40	V
Maximum working peak reverse voltage	$V_{RWM}$	40	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDIT	TIONS	VALUES	UNITS
Maximum average forward current		50 % duty cycle at $T_L = 105$ °C, on PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area	· ·	2.1	۸
See fig. 4	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>L</sub> = 113 °C, on PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area	-	1.5	A
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	330	
non-repetitive surge current See fig. 6	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	140	Α
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25$ °C, $I_{AS} = 1$ A, $L = 12$ mH		6.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical  1.0		Α	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS		UNITS
		1.5 A	T <sub>.1</sub> = 25 °C	0.43	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	2 A	1j = 25 C	0.49	v
See fig. 1	VFM (*)	1.5 A	T <sub>.1</sub> = 125 °C	0.34	V
		2 A	1j = 125 C	0.43	
Maximum reverse leakage current	1	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.5	mA
See fig. 2	I <sub>RM</sub>	T <sub>J</sub> = 125 °C	VR = nated VR	20	IIIA
Threshold voltage	V <sub>F(TO)</sub>	$T_{J} = T_{J} \text{ maximum}$ 0.26 64.6		0.26	V
Forward slope resistance	r <sub>t</sub>			mΩ	
Typical junction capacitance	C <sub>T</sub>	$V_R = 10 V_{DC}$ , $T_J = 25 °C$ , test signal = 1 MHz		pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.0 nl		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µs		V/µs	

### Note

 $<sup>^{(1)}</sup>$  Pulse width = 300  $\mu$ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-40 to +150	°C
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	oz.
Marking device		Case style SMA (DO-214AC)	Х	F

### Note

(1) 
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink



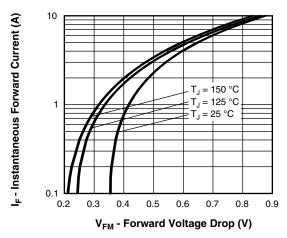


Fig. 1 - Maximum Forward Voltage Drop Characteristics

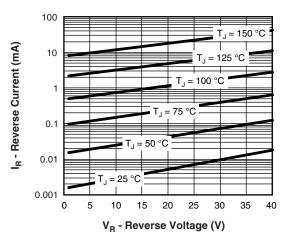


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

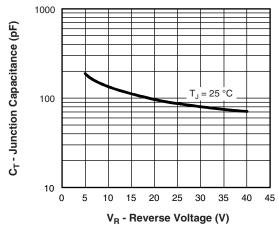
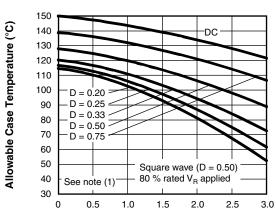


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



I<sub>F(AV)</sub> - Average Forward Current (A)

Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

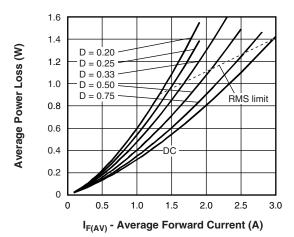


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

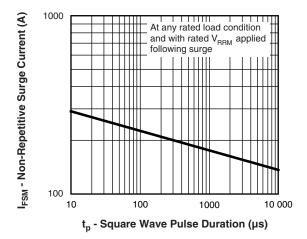


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

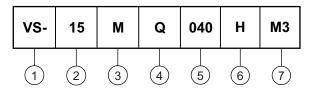
#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{th,JC}}; \\ \text{Pd} & = \text{Forward power loss} = \text{I}_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at } (\text{I}_{\text{F(AV)}}/\text{D}) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} & = \text{Inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D); I}_{\text{R}} \text{ at } \text{V}_{\text{R1}} = 80 \text{ \% rated V}_{\text{R}} \\ \end{array}$ 



### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating

- M = SMA

4 - Q = Schottky "Q" series

Voltage rating (040 = 40 V)

6 - H = AEC-Q101 qualified

7 - Environmental digit:

M3 = Halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION				
VS-15MQ040HM3/5AT	5AT	7500	13" diameter plastic tape and reel	

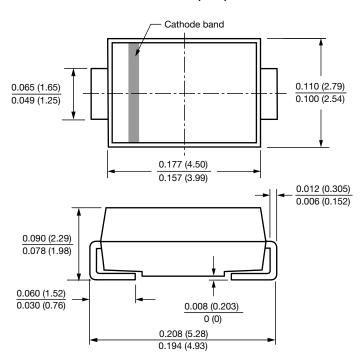
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95400		
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		



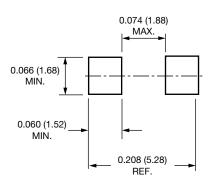
## **SMA**

### **DIMENSIONS** in inches (millimeters)

### **DO-214AC (SMA)**



### Mounting Pad Layout





## **Legal Disclaimer Notice**

Vishay

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