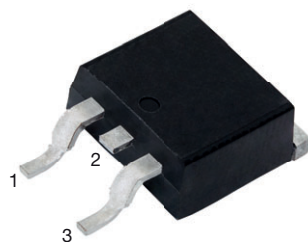
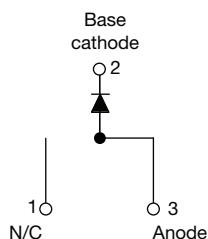


High Performance Schottky Rectifier, 20 A


D²PAK (TO-263AB)


FEATURES

- 125 °C T_J operation (V_R < 5 V)
- Single diode configuration
- Optimized for OR-ing applications
- Ultralow forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

DESCRIPTION

The Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

PRIMARY CHARACTERISTICS

I _{F(AV)}	20 A
V _R	15 V
V _F at I _F	0.33 V
I _{RM} max.	600 mA at 100 °C
T _J max.	125 °C
E _{AS}	10 mJ
Package	D ² PAK (TO-263AB)
Circuit configuration	Single

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
I _{F(AV)}	Rectangular waveform	20	A
V _{RRM}		15	V
I _{FSM}	t _p = 5 μs sine	700	A
V _F	19 A _{pk} , T _J = 125 °C (typical)	0.25	V
T _J	Range	-55 to +125	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VS-20L15TS-M3	UNITS
Maximum DC reverse voltage	V _R	T _J = 100 °C	15	V
Maximum working peak reverse voltage	V _{RWM}			

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 85 °C, rectangular waveform	20	A
Maximum peak one cycle non-repetitive surge current See fig. 7	I _{FSM}	5 μs sine or 3 μs rect. pulse	700	
		10 ms sine or 6 ms rect. pulse	330	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 6 mH	10	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T _J maximum V _A = 1.5 x V _R typical	2	A

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	TYP.	MAX.	UNITS
Forward voltage drop See fig. 1	$V_{FM}^{(1)}$	19 A	-	0.41	V
		40 A		0.52	
		19 A	0.25	0.33	
		40 A		0.50	
Reverse leakage current See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	10	mA
		$T_J = 100\text{ }^{\circ}\text{C}$	-	600	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$	0.182		V
Forward slope resistance	r_t		7.6		m Ω
Maximum junction capacitance	C_T	$V_R = 5\text{ V}_{DC}$, (test signal range 100 kHz to 1 MHz), $25\text{ }^{\circ}\text{C}$	-	2000	pF
Typical series inductance	L_S	Measured lead to lead 5 mm from package body	8	-	nH
Maximum voltage rate of change	dV/dt	Rated V_R	10 000		V/ μs

Note

(1) Pulse width < 300 μs , duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J		-55 to +125	°C
Maximum storage temperature range	T _{Stg}		-55 to +150	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	1.5	°C/W
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased (For TO-220)	0.50	
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	40	
Approximate weight			2	g
			0.07	oz.
Mounting torque	minimum	Non-lubricated threads	6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style D ² PAK (TO-263AB)	20L15TS	

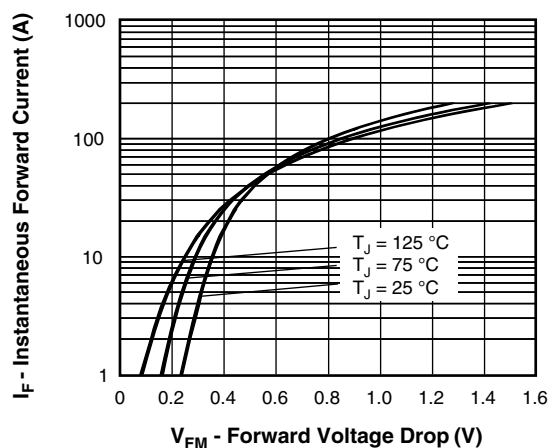


Fig. 1 - Maximum Forward Voltage Drop Characteristics

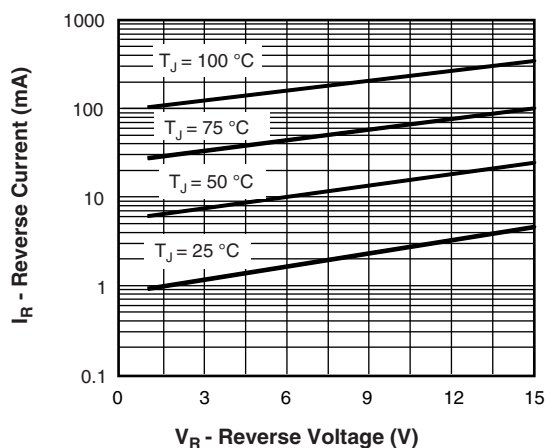


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

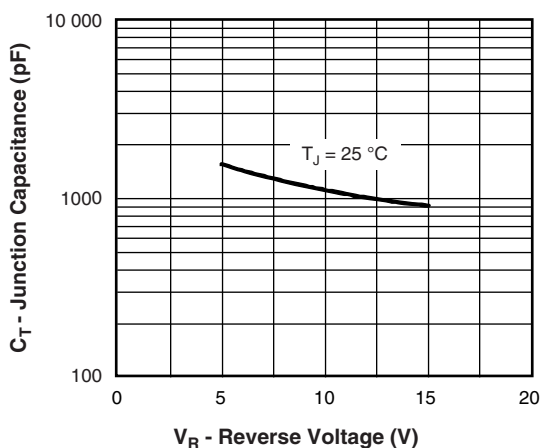


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

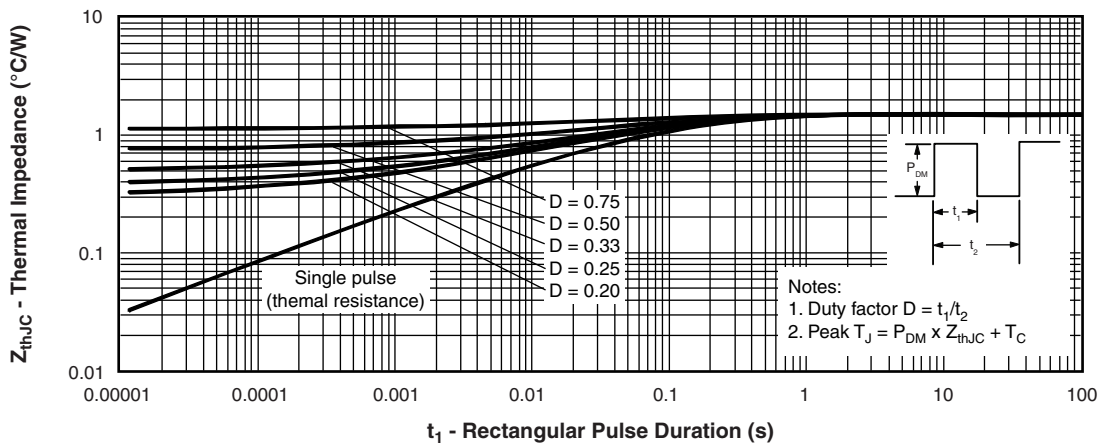
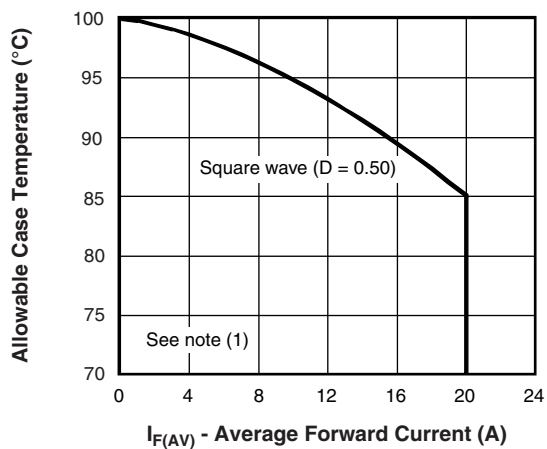

Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

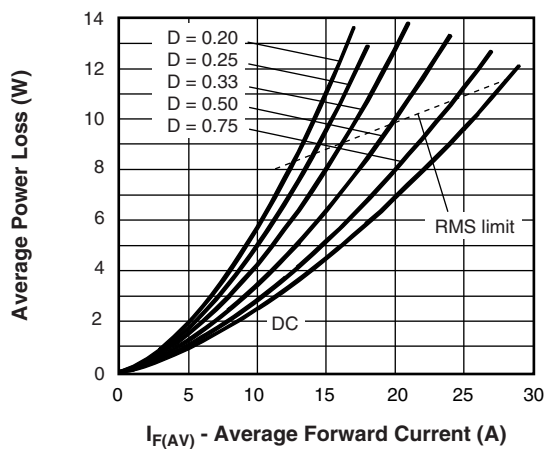


Fig. 6 - Forward Power Loss Characteristics

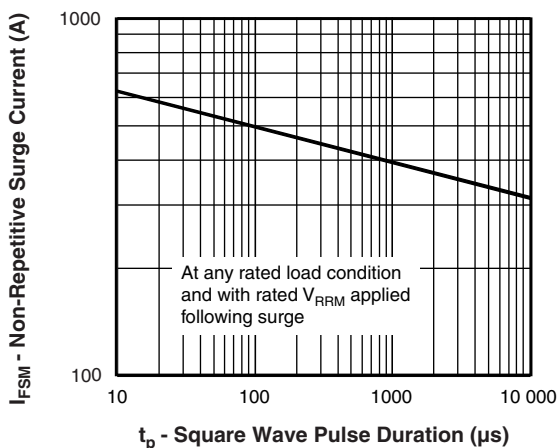


Fig. 7 - Maximum Non-Repetitive Surge Current

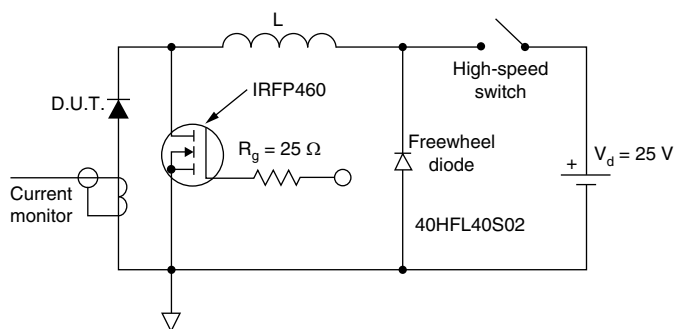


Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R

**ORDERING INFORMATION TABLE**

Device code	VS-	20	L	15	T	S	TRL	-M3
	1	2	3	4	5	6	7	8
1	- Vishay Semiconductors product							
2	- Current rating (20 A)							
3	- L = Low V_F							
4	- Voltage rating (15 = 15 V)							
5	- T = Schottky series							
6	- S = D ² PAK (TO-263AB)							
7	- • None = tube • TRL = tape and reel (left oriented) • TRR = tape and reel (right oriented)							
8	- -M3 = halogen-free, RoHS-compliant and termination lead (Pb)-free							

ORDERING INFORMATION (Example)

PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION
VS-20L15TS-M3	50	Antistatic plastic tubes
VS-20L15TSTRL-M3	800	13" diameter plastic tape and reel
VS-20L15TSTRR-M3	800	13" diameter plastic tape and reel

LINKS TO RELATED DOCUMENTS

Dimensions	www.vishay.com/doc?96164
Part marking information	www.vishay.com/doc?95444
Packaging information	www.vishay.com/doc?96424
SPICE model	www.vishay.com/doc?97117



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