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Vishay Semiconductors

FRED Pt® Gen 5 Ultrafast Rectifier Diode, 600 V, 150 A



SOT-227

PRIMARY CHARACTERISTICS						
V _R	600 V					
V _F (typical) at 75 A, per diode	1.34 V					
t _{rr} (typical) at 75 A, per diode	70 ns					
I _{F(DC)} per module at T _C = 117 °C	150 A					
Туре	Modules - diode, FRED Pt®					
Package	SOT-227					
Circuit configuration	Two separate diodes, parallel pin-out					

FEATURES

- Ultrafast and optimized Q_{rr}
- Best in class forward voltage drop and switching losses trade off



- Optimized for high speed operation
- 175 °C maximum operating junction temperature
- Electrically isolated base plate
- Large creepage distance between terminal
- Simplified mechanical designs, rapid assembly
- Designed and qualified for industrial level
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, the VS-U5FH150FA60 is the right choice for high frequency converters, both soft switched / resonant. The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters, and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V _R		600	V		
Continuous forward current per diode	I _F	T _C = 117 °C	75	Δ.		
Single pulse forward current per diode	I _{FSM}	T _J = 25 °C	575	А		
Maximum power dissipation per module	P _D	T _C = 117 °C	215	W		
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V		
Operating junction and storage temperature range	T _J , T _{Stg}		-55 to +175	°C		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 100 μA	600	-	-	
Forward voltage	V_{FM}	I _F = 75 A	-	1.34	1.7	V
		I _F = 75 A, T _J = 150 °C	-	1.12	-	
		V _R = 600 V	-	0.2	50	
Reverse leakage current	I _{RM}	T _J = 125 °C, V _R = 600 V	-	31	-	μA
		$T_J = 150 ^{\circ}\text{C}, V_R = 600 \text{V}$	-	139	-	



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Dovorgo roccyony timo		T _J = 25 °C	$I_F = 75 \text{ A},$ $di_F/dt = 1000 \text{ A/}\mu\text{s},$ $V_R = 400 \text{ V}$	-	70	-	- ns
Reverse recovery time	t _{rr}	T _J = 125 °C		-	99	-	
Dook recovery current	ı	T _J = 25 °C		-	19	-	A
Peak recovery current	I _{RRM}	T _J = 125 °C		-	40	-	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	0.6	-	
	Qrr	T _J = 125 °C		-	2.5	-	μC
Junction capacitance	C _T	V _R = 600 V, f = 1 MHz		-	64.4	-	pF

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance junction to case, per diode	В		-	-	0.54	
Thermal resistance junction to case, per module	R _{thJC}		-	-	0.27	°C/W
Thermal resistance case to heatsink, per module	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque per diode	-	-	1.1 (9.7)	Nm (lbf.in)
		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SOT	Γ-227	

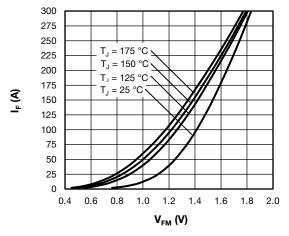


Fig. 1 - Typical Forward Voltage Drop Characteristics

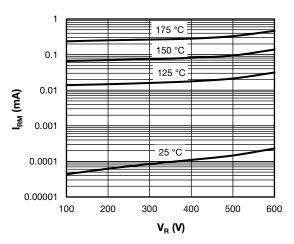


Fig. 2 - Typical Values of Reverse Current



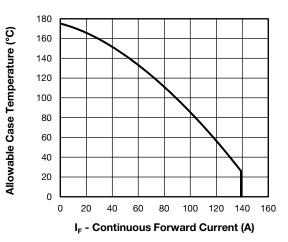


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Diode)

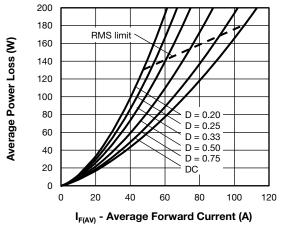


Fig. 4 - Average Power Loss vs. Average Forward Current

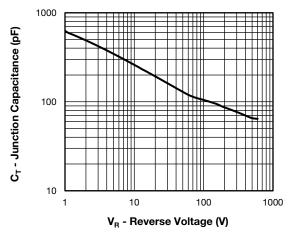


Fig. 5 - Typical Junction Capacitance vs. Reverse Voltage

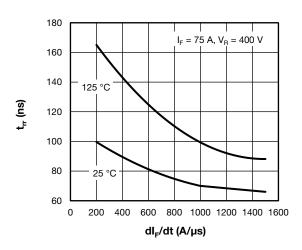


Fig. 6 - Diode Reverse Recovery Time vs. dI_Fdt

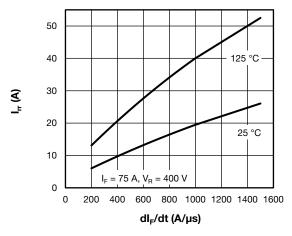


Fig. 7 - Diode Reverse Recovery Current vs. dl_Fdt

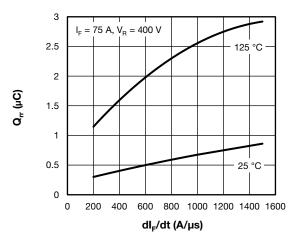


Fig. 8 - Diode Reverse Recovery Charge vs. dI_Fdt

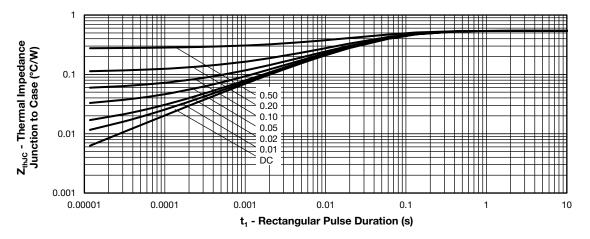
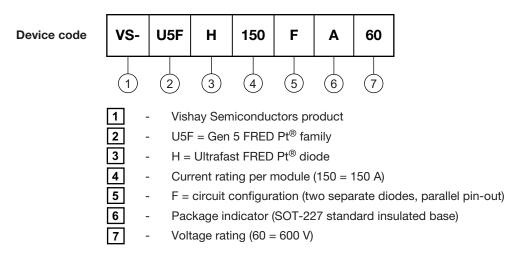


Fig. 9 - Maximum Thermal Impedance Junction to Case

ORDERING INFORMATION TABLE

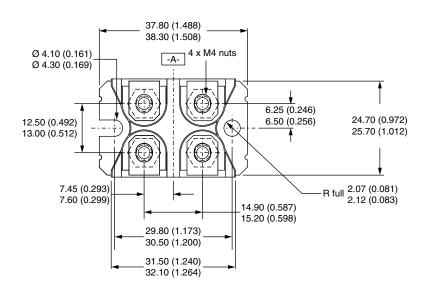


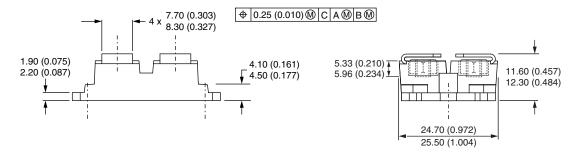
CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two separate diodes, parallel pin-out	F	Lead Assignment 4			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				

SOT-227 Generation 2

DIMENSIONS in millimeters (inches)





Note

· Controlling dimension: millimeter



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