nRF52832 Product Specification



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I. nRF52832 Product Specification

This Product Specification contains functional descriptions, register tables, and electrical specifications, and is organized into chapters based on the modules and peripherals that are available in this IC.

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Note: The HTML rendition of the Product Specification corresponds to the latest version only. All versions are available as PDF files.

Key features	Applications
2.4 GHz transceiver	Internet of Things (IoT)
-96 dBm sensitivity in Bluetooth® Low Energy mode	Home automation
Supported data rates: 1 Mbps, 2 Mbps Bluetooth Low Energy mode	Sensor networks
 -20 to +4 dBm TX power, configurable in 4 dB steps 	Building automation
On-chip balun (single-ended RF)	Industrial
• 5.3 mA peak current in TX (0 dBm)	• Retail
• 5.4 mA peak current in RX	Personal area networks
RSSI (1 dB resolution)	Health/fitness sensor and
Arm® Cortex®-M4 32-bit processor with FPU, 64 MHz	monitor devices
215 EEMBC CoreMark score running from flash memory	Medical devices
 58 μA/MHz running from flash memory 	Key fobs and wrist watches
 51.6 μA/MHz running from RAM 	Interactive entertainment devices
• Data watchpoint and trace (DWT), embedded trace macrocell (ETM), and	Remote controls
instrumentation trace macrocell (ITM)	Gaming controllers
Serial wire debug (SWD)	• Beacons
Trace port	A4WP wireless chargers and
Flexible power management	devices
 1.7 V–3.6 V supply voltage range 	Remote control toys
Fully automatic LDO and DC/DC regulator system	Computer peripherals and I/O
Fast wake-up using 64 MHz internal oscillator	devices
 0.3 μA at 3 V in System OFF mode 	• Mouse
 0.7 μA at 3 V in System OFF mode with full 64 kB RAM retention 	• Keyboard
- 1.9 μA at 3 V in System ON mode, no RAM retention, wake on RTC	Multi-touch trackpad



Key features	Applications
 Memory 512 kB flash/64 kB RAM 256 kB flash/32 kB RAM Nordic SoftDevice ready Support for concurrent multi-protocol Type 2 near field communication (NFC-A) tag with wakeup-on-field and touch-to-pair capabilities 12-bit, 200 ksps ADC - 8 configurable channels with programmable gain 64 level comparator 15 level low power comparator with wakeup from System OFF mode Temperature sensor 32 general purpose I/O pins 3x 4-channel pulse width modulator (PWM) unit with EasyDMA Digital microphone interface (PDM) 5x 32-bit timer with counter mode Up to 3x SPI master/slave with EasyDMA Up to 2x I2C compatible 2-wire master/slave I2S with EasyDMA UART (CTS/RTS) with EasyDMA Programmable peripheral interconnect (PPI) Quadrature decoder (QDEC) AES HW encryption with EasyDMA Autonomous peripheral operation without CPU intervention using PPI and EasyDMA 3x real-time counter (RTC) Single crystal operation Package variants QFN48 package, 6 × 6 mm WLCSP package, 3.0 × 3.2 mm 	• Gaming

