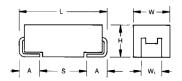
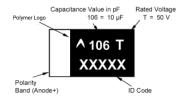
High Temperature Automotive Polymer Chip Capacitors







MARKING



FEATURES

- · Conductive Polymer Electrode
- Robust Design for Automotive Applications
- 100% surge current tested
- Meets Requirements of AEC-Q200
- -55 to +150°C Operation Temperature
- Comply with humidity 85°C/85%RH, Vr, 1000 Hours test
- 3x reflow cycles according to J-STD-020





APPLICATIONS

DC/DC converters, Telecommunication (coupling/decoupling), Industrial & special, Automotive (body electronics, cabin controls, infotainment, comfort, after market etc) Not recommended for use of conductive polymer parts in high power applications. For more information please see KYOCERA AVX automotive application guide at kyocera-avx.com, or contact manufacturer.

KYOCERA AVX's qualification of TCO capacitors meets requirements of AEC-Q200. TCO series is manufactured in an IATF 16949 certified facility.

CASE DIMENSIONS:

millimeters (inches)

Code	EIA Code	EIA Metric	L±0.20 (0.008)	W+0.20 (0.008) -0.10 (0.004)	H+0.20 (0.008) -0.10 (0.004)	W1±0.20 (0.008)	A+0.30 (0.012) -0.20 (0.008)	S Min.
В	1210	3528-21	3.50 (0.138)	2.80 (0.110)	1.90 (0.075)	2.20 (0.087)	0.80 (0.031)	1.40 (0.055)
D	2917	7343-31	7.30 (0.287)	4.30 (0.169)	2.90 (0.114)	2.40 (0.094)	1.30 (0.051)	4.40 (0.173)

W1 dimension applies to the termination width for A dimensional area only

050 = 50 Vdc

HOW TO ORDER





pF code: 1st two digits represent significant figures, 3rd digit represents multiplier (number of zeros to follow)

050 M Tolerance M = +20%

Rated DC Voltage 020 = 20Vdc R = Pure Tin 7" Reel 025 = 25 VdcS = Pure Tin 13" Reel

0150 **Packaging**

ESR in m0

Ε Additional Character E = Black resin

TECHNICAL SPECIFICATIONS

Technical Data:	All technical data relate to an ambient temperature of +25°C
Capacitance Range:	10 μF to 33μF
Capacitance Tolerance:	±20%
Leakage Current DCL:	0.1CV
Temperature Range:	-55°C to +150°C
	Meets requirements of AEC-Q200

NOTE: Conductive Polymer Capacitors are designed to operate within the limits of the environmental conditions specified for each series. If operated continuously at their maximum temperature and / or humidity limit, or beyond these limits, capacitors may exhibit a parametric shift in capacitance and increases in ESR. These changes may occur earlier if the specified environmental conditions are exceeded. Similarly, their normal operational time period will be significantly extended if their general duty cycle includes operation below maximum temperature within humidity controlled environments. Careful attention should be paid to maximum temperature with associated high humidity environments as well as voltage derating, ripple current and current surges. Please reference the KYOCERA AVX Conductive Polymer Capacitor Guidelines for more information or contact factory for application assistance.



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CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)

Capac	itance	Rated Voltage DC (V _R) @ 105°C							
μF	Code	20V (D)	25V (E)	35V (V)	50V (T)				
10	106				D(150)				
15	156								
22	226	B(150)							
33	336		D(100)						

Released ratings, (ESR ratings in mOhms in parentheses)

Note: Voltage ratings are minimum values. KYOCERA AVX reserves the right to supply higher volage ratings in the same case size, to the same reliability standards.

RATINGS & PART NUMBER REFERENCE

Part Number	Case Size	Capacitance (μF)	Rated Voltage (V)	Maximum Operating Temp.	DCL Max	DF Max	ESR Max @ 100kHz (mΩ)	100kHz RMS Current (mA)				Humidity 85°C/85% RH,	MSL	
Part Number				(°C)	(µA)	(%)		45°C	85°C	105°C	125°C	150°C		IVISL
	20 Volt													
TCOB226M020#0150E	В	22	20	150	44	8	150	913	639	411	228	137	1000	3
	25 Volt													
TCOD336M025#0100E	D	33	25	150	82.5	10	100	1500	1050	675	375	225	1000	3
	50 Volt													
TCOD106M050#0150E	D	10	50	150	50	10	150	1225	857	551	306	184	1000	3

Moisture Sensitivity Level (MSL) is defined according to J-STD-020.

All technical data relates to an ambient temperature of +25C.

Capacitance and DF are measured at 120Hz, 0.5RMS with DC bias of 2.2 volts.

DCL is measured at rated voltage after 5 minutes.

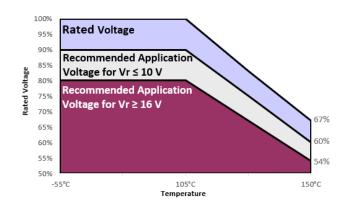
ESR allowed to move up to 1.25 times catalog limit post mounting.

For typical weight and composition see page 253.

RECOMMENDED DERATING FACTOR

Voltage and temperature derating as percentage of Vr

Rated	Operating Temperature						
voltage	≤85°C	105°C	150°C				
≤10V	90%	90%	60%				
≥16V	80%	80%	54%				





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QUALIFICATION TABLE

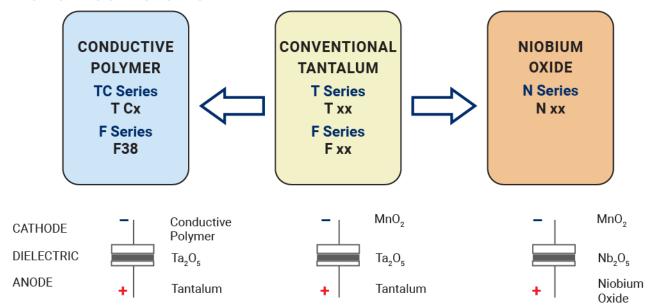
TEOT	TCO series (Temperature range -55°C to 150°C)											
TEST		Condition		Characteristics								
				Visual examination no visible damage								
		age (Ur) at 105°C fo		DCL	2 x initial	limit						
Endurance		e (Ur) at 150°C for 1 impedance of ≤0.1		ΔC/C	within +1	within +10/-20% of initial value						
		re for 1-2 hours bef		DF	2 x initial	2 x initial limit						
	l com temperata		oro modeaung.	ESR	2 x initial	2 x initial limit						
				Visual examination	no visible	no visible damage						
	Store at 150°C. r	no voltage applied, f	or 1000 hours.	DCL	2x initial	2x initial limit						
Storage Life		n temperature for 1-		ΔC/C	within +1	within +10/-20% of initial value						
•	measuring.			DF	2 x initial	2 x initial limit						
				ESR	2 x initial	limit						
				Visual examination	no visibl	e damage						
		age (Ur) at 85°C, 85°		DCL	2 x initia	2 x initial limit						
Biased Humidity		0 hours. Stabilize a		ΔC/C	within +3	within +35/-5% of initial value						
•	measuring.	I humidity for 1-2 ho	ours before	DF	1.5 x init	1.5 x initial limit						
	measuring.			ESR	2 x initia	l limit						
	Step	Temperature°C	Duration(min)		+20°C	-55°C	+20°C	+105°C	+150°C	+20°C		
	1	+20	15			33 0		1103 0				
Temperature	3	-55 +20	15 15	DCL	IL*	n/a	IL*	10 x IL*	12.5 x IL*	IL*		
Stability	4	+105	15	ΔC/C	n/a	±20%	±5%	±20%	±30%	±5%		
	5	+150	15	· ·				-				
	6	+20	15	DF	IL*	IL*	IL*	1.5 x IL*	1.5 x IL*	IL*		
				Visual examination	no visible	no visible damage						
				DCL	initial lim	initial limit						
Surge Voltage		rated voltage (Ur) a arge / discharge res		ΔC/C		within +10/-20% of initial value for Vr ≤ 10V within +20/-30% of initial value for Vr ≥ 16V						
	1000 Cycles, cire	irge / discriarge res	istance 3312.	DF		initial limit for Vr ≤ 10V 1.25x initial limit for Vr ≥ 16V						
				ESR	1.25 x ini	1.25 x initial limit						
				Visual examination	no visibl	no visible damage						
				DCL	initial lim	initial limit						
Mechanical	MIL-STD-202, M	ethod 213, Conditio	n F	ΔC/C	within ±	within ±10% of initial value						
Shock				DF	initial lim	initial limit						
				ESR	1.25 x in	1.25 x initial limit						
				Visual examination	no visibl	no visible damage						
				DCL	initial lim	initial limit						
Vibration	MIL-STD-202, M	ethod 204, Conditio	n D	ΔC/C	within ±	within ±10% of initial value						
				DF	initial lim	initial limit						
				ESR	1.25 x in	1.25 x initial limit						

For use outside of recommended conditions and special request, please contact KYOCERA AVX. Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.

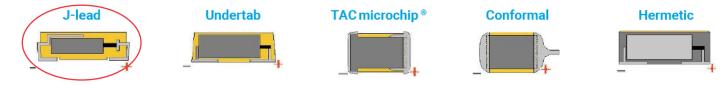




SOLID ELECTROLYTIC CAPACITOR ROADMAP



FIVE CAPACITOR CONSTRUCTION STYLES



SERIES LINE UP: Conductive Polymer

