

SWLNA for 5GHz Band Applications

KA29222K Datasheet

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Support for industry standards and quality standards

Functional safety standards for automobiles ISO26262	No
AECQ-100	No
Market failure rate	50Fit

Disclaimer

1. When the application system is designed using this IC, please design the system at your own risk. Please read, consider, and apply appropriate usage notes and description in this standard.
2. When designing your application system, please take into the consideration of break down and failure mode occurrence and possibility in semiconductor products. Measures on the systems such as, but not limited to, redundant design, mitigating the spread of fire, or preventing glitch, are recommended in order to prevent physical injury, fire, social damages, etc. in using the Nuvoton Technology Japan Corporation (hereinafter referred to as NTCJ) products.
3. When using this IC, for each actual application systems, verify the systems and the all functionality of this IC as intended in application systems and the safety including the long-term reliability at your own risk
4. Please use this IC in compliance with all applicable laws, regulations and safety-related requirements that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. NTCJ shall not be held responsible for any damage incurred as a result of this IC being used not in compliance with the applicable laws, regulations and safety-related requirements.
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KA29222K

SWLNA for 5 GHz Band Applications

■ Overview

- KA29222K is a LNA (Low Noise Amplifier)-IC integrated SW(SPDT) for 5GHz Band applications .
- Realizing high performance by using 0.3 μm CMOS process.
- TX mode , RX mode / High Gain , RX mode / Low Gain are controlled by integrated CMOS logic circuit .
- Achieving miniaturization by using small size Chip Size Package with solder bump .

■ Features

- | | | |
|--------------------------|-------------|---------------------|
| —Low voltage operation | +3.3 V typ. | |
| —Low current consumption | 12 mA typ. | (RX mode/High Gain) |
| | 90 μA typ. | (RX mode/Low Gain) |
| | 75 μA typ. | (TX mode) |
| | 25 μA typ. | (ALL Off mode) |
| —High gain | 11 dB typ. | (RX mode/High Gain) |
| —Low noise figure | 2.5 dB typ. | (RX mode/High Gain) |
| —Low Insertion Loss | 0.7dB typ. | (TX mode) |

■ Applications

- Wireless LAN

■ Package

- 11Pin Chip Size Package with solder bump
- Size : 0.711 mm × 0.923 mm × 0.3 mm

■ Type

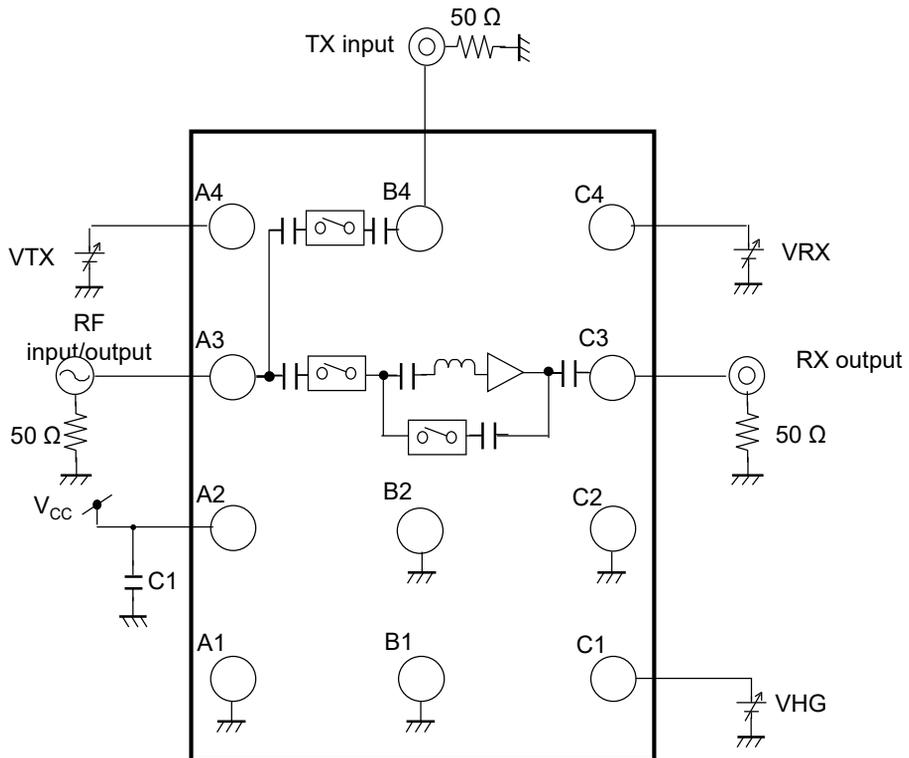
- CMOS IC

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■ Application Circuit Example (Block Diagram)

(Top View)



• External Components

Components	Size	Value	Part Number	Vendor
C1	0603	0.1 uF	GRM033B30J104KE18	Murata

Notes)

—This application circuit is an example. The operation of mass production set is not guaranteed. You should perform enough evaluation and verification on the design of mass production set. You are fully responsible for the incorporation of the above application circuit and information in the design of your equipment.

—This block diagram is for explaining functions. The part of the block diagram may be omitted, or it may be simplified.

■ Pin Descriptions

Pin No.	Pin name	Type	Description
A1	GND	Ground	GND
A2	VCC	Power Supply	V _{CC}
A3	RFC	Input/Output	RF input/output
A4	VTX	Input	TX mode SW
B1	GND	Ground	GND
B2	GND	Ground	GND
B4	TX	Input	TX input
C1	VHG	Input	High Gain/Low Gain SW
C2	GND	Ground	GND
C3	RX	Output	RX output
C4	VRX	Input	RX mode SW

■ Absolute Maximum Ratings

Note) Absolute maximum ratings are limit values which do not result in damages to this IC, and IC operation is not guaranteed at these limit values.

A No.	Parameter	Symbol	Rating	Unit	Notes
1	Supply voltage	V_{CC}	3.7	V	*1
2	Supply current	I_{CC}	20	mA	—
3	Operating ambient temperature	T_{opr}	-40 to +85	°C	*2
4	Storage temperature	T_{stg}	-55 to +150	°C	*2

Notes) *1 : The values under the condition not exceeding the above absolute maximum ratings.

*2 : Except for the operating ambient temperature and storage temperature, all ratings are for $T_a = 25^{\circ}\text{C}$.

■ Operating supply voltage range

Parameter	Symbol	Range	Unit	Notes
Supply voltage range	V_{CC}	3.0 to 3.6	V	*

Note) * : The values under the condition not exceeding the above absolute maximum ratings.

■ Allowable Current and Voltage Range

- Notes) — Allowable current and voltage ranges are limit ranges which do not result in damages to this IC, and IC operation is not guaranteed within these limit ranges.
- Voltage values, unless otherwise specified, are with respect to GND.
 - Do not apply external currents or voltages to any pin not specifically mentioned.

Pin No.	Pin name	Range	Unit	Notes
A3	RFC	—	V	*1
A4	VTX	-0.3 to (Vcc + 0.3)	V	*4
B4	TX	—	V	*2
C1	VHG	-0.3 to (Vcc + 0.3)	V	*4
C3	RX	—	V	*3
C4	VRX	-0.3 to (Vcc + 0.3)	V	*4

- Note) *1 : RF signal input / output pin.
 Maximum input / output power at TX mode is 27 dBm.
 Maximum input power at RX mode / High Gain is 0 dBm.
 Maximum input power at RX mode / Low Gain is 10 dBm.
- *2 :RF signal input pin.
 Maximum input power at TX mode is 27 dBm.
- *3 : RF signal output pin.
- *4 : (VCC + 0.3) V must not be exceeded 3.7 V

■ Electrical Characteristics at $V_{CC} = 3.3\text{ V}$

Note) $T_a = 25^{\circ}\text{C} \pm 2^{\circ}\text{C}$ unless otherwise specified.

B No.	Parameter	Symbol	Test Circuit	Conditions	Limits			Unit	Note
					Min	Typ	Max		
DC electrical characteristics									
DC-1	Supply current RX mode / High Gain	I_{CCRXHS}	1	V_{CC} current at RX mode / High Gain No input signal	—	12	15.5	mA	—
DC-2	Supply current RX mode / Low Gain	I_{CCRXLS}	1	V_{CC} current at RX mode / Low Gain No input signal	—	90	130	uA	—
DC-3	Supply current TX mode	I_{CCTXS}	1	V_{CC} current at TX mode No input signal	—	75	115	uA	—
DC-4	SW current (High Voltage)	IIHS	1	Current at VRX, VTX, VHG pin $V_{IH} = V_{CC}$	—	18	36	uA	—
DC-5	SW Input Voltage (High Level)	VIHS	1	—	1.6	3.3	—	V	—
DC-6	SW Input Voltage (Low Level)	VILS	1	—	—	0	0.3	V	—
DC-7	Supply current ALL Off mode	I_{CCOFFS}	1	V_{CC} current at ALL Off mode No input signal	—	25	60	uA	—

■ Electrical Characteristics (continued) at $V_{CC} = 3.3\text{ V}$

Note) $T_a = 25^{\circ}\text{C} \pm 2^{\circ}\text{C}$, $f_{RX} = 5.85\text{ GHz}$, $PRX = -10\text{ dBm}$, CW unless otherwise specified.

B No.	Parameter	Symbol	Test Circuit	Conditions	Limits			Unit	Note
					Min	Typ	Max		
AC electrical characteristics RX Mode / High Gain ($V_{RX}=3.3\text{V}$, $V_{HG}=3.3\text{V}$, $V_{TX}=0\text{V}$)									
A-1	Power Gain	GRXHS	1	RFC to RX	8.5	11	13.5	dB	—
AC electrical characteristics RX Mode / Low Gain ($V_{RX}=3.3\text{V}$, $V_{HG}=0\text{V}$, $V_{TX}=0\text{V}$)									
B-1	Power Gain	GRXLS	1	RFC to RX $f_{RX} = 5.15\text{ GHz}$	-9.5	-8.5	-7.5	dB	—
AC electrical characteristics TX Mode ($V_{RX}=0\text{V}$, $V_{HG}=0\text{V}$, $V_{TX}=3.3\text{V}$)									
C-1	Insertion Loss	ILTXS	1	RFC to TX	—	0.7	1.1	dB	—

■ Electrical Characteristics (Reference values for design) at $V_{CC} = 3.3\text{ V}$

Note) $T_a = 25^\circ\text{C} \pm 2^\circ\text{C}$, $f_{RX} = 5.5\text{GHz}$, $PRX = -10\text{ dBm}$, CW unless otherwise specified.

The characteristics listed below are reference values derived from the design of the IC and are not guaranteed by inspection.

If a problem does occur related to these characteristics, we will respond in good faith to user concerns.

B No.	Parameter	Symbol	Test Circuit	Conditions	Reference values			Unit	Note
					Min	Typ	Max		
AC electrical characteristics RX Mode / High Gain ($VRX=3.3\text{V}$, $VHG=3.3\text{V}$, $VTX=0\text{V}$)									
D-1	Noise Figure	NFRXH	1	RFC to RX	—	2.5	3.0	dB	—
D-2	Input Power 1dB Compression	IP1dBRXH	1	RFC to RX	-6.0	-1.0	—	dBm	—
D-3	IIP3 +10 MHz offset	IIP3RXH1	1	$f1 = f_{RX} + 10\text{ MHz}$ $f2 = f_{RX} + 20\text{ MHz}$ Input 2 signals ($f1, f2$)	4	8	—	dBm	—
D-4	Reverse Isolation	ISORXH	1	RX to RFC	21	26	—	dB	—
D-5	Isolation RFC_TX	ISORXH ₁	1	RFC to TX	15	21	—	dB	—
D-6	Isolation RX_TX	ISORXH ₂	1	RX to TX	21	25	—	dB	—
D-7	Isolation TX_RFC	ISORXH ₃	1	TX to RFC	15	18	—	dB	—
D-8	Isolation TX_RX	ISORXH ₄	1	TX to RX	8	11	—	dB	—

■ Electrical Characteristics (Reference values for design, continued) at $V_{CC} = 3.3\text{ V}$

Note) $T_a = 25^{\circ}\text{C} \pm 2^{\circ}\text{C}$, $f_{RX} = 5.5\text{GHz}$, $PRX = -10\text{ dBm}$, CW unless otherwise specified.

The characteristics listed below are reference values derived from the design of the IC and are not guaranteed by inspection.

If a problem does occur related to these characteristics, we will respond in good faith to user concerns. A

B No.	Parameter	Symbol	Test Circuit	Conditions	Reference values			Unit	Note
					Min	Typ	Max		
AC electrical characteristics RX Mode / Low Gain ($VRX=3.3\text{V}$, $VHG=0\text{V}$, $VTX=0\text{V}$)									
E-1	Isolation RFC_TX	ISORXL_1	1	RFC to TX	13	15	—	dB	—
E-2	Isolation RX-TX	ISORXL_2	1	RX to TX	19	21	—	dB	—
AC electrical characteristics TX Mode ($VRX=0\text{V}$, $VHG=0\text{V}$, $VTX=3.3\text{V}$)									
F-1	Input Power 0.1dB Compression	IP01dBTX	1	RFC to TX	25	28	—	dBm	—
F-2	Isolation RFC_RX	ISOTX_1	1	RFC to RX	35	40	—	dB	—
F-3	Isolation RX_TX	ISOTX_2	1	RX to TX	27	30	35	dB	—

■ Control Pin Mode Table

Note)See parameters B No. DC-5,6 in the Electrical Characteristics for control voltage retention ranges.

Pin No.	Description	Pin voltage		Remarks
		Low	High	
A4	TX mode SW	—	TX Mode	—
C1	High Gain/Low Gain SW	Low Gain	High Gain	—
C4	RX mode SW	—	RX Mode	—

■ Truth Table

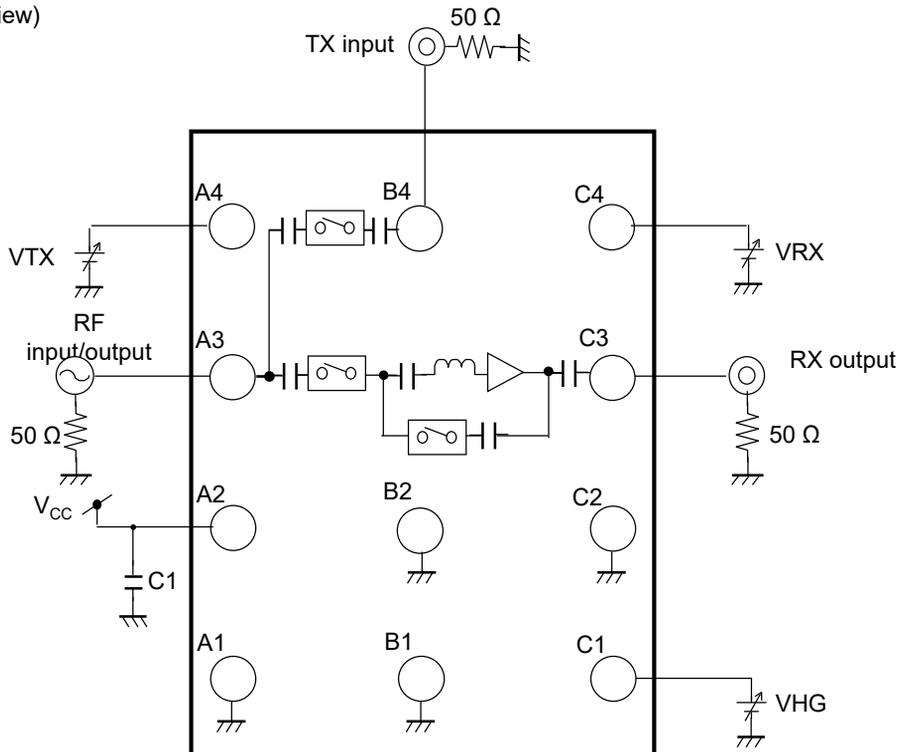
Note)See parameters B No. DC-5,6 in the Electrical Characteristics for control voltage retention ranges.

VTX	VHG	VRX	Mode
High	Low	Low	TX mode
Low	High	High	RX mode / High Gain
Low	Low	High	RX mode / Low Gain
Low	Low	Low	ALL Off mode

■ Test Circuit Diagram

- Test Circuit 1

(Top View)



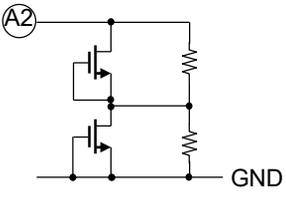
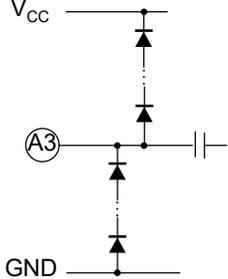
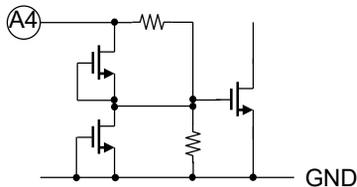
- External Components

Components	Size	Value	Part Number	Vendor
C1	0603	0.1 uF	GRM033B30J104KE18	Murata

■ Technical Data

— I/O block circuit diagram and pin function descriptions

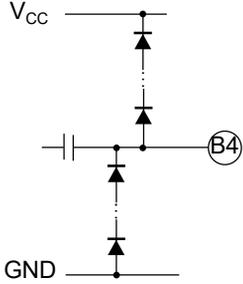
Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

Pin No.	Waveform and voltage	Internal circuit	Impedance	Description
A1	—	—	—	GND
A2	3.3 V		3.1MΩ	Vcc
A3	—		—	RF input/output
A4	—		280 kΩ	TX mode SW

■ Technical Data(continued.)

— I/O block circuit diagram and pin function descriptions

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

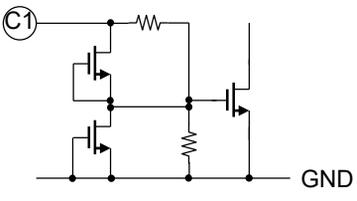
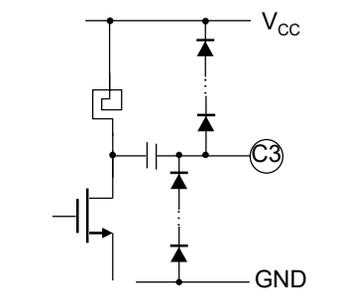
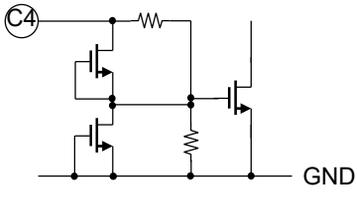
Pin No.	Waveform and voltage	Internal circuit	Impedance	Description
B1	—	—	—	GND
B2	—	—	—	GND
B4	—	 <p>The diagram shows the internal circuit for pin B4. It features a pull-up resistor connected to the V_{CC} supply. A diode is connected between the pin and GND, with the anode at the pin and the cathode at GND. The pin is labeled B4 in a circle.</p>	—	TX input

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■ Technical Data(continued.)

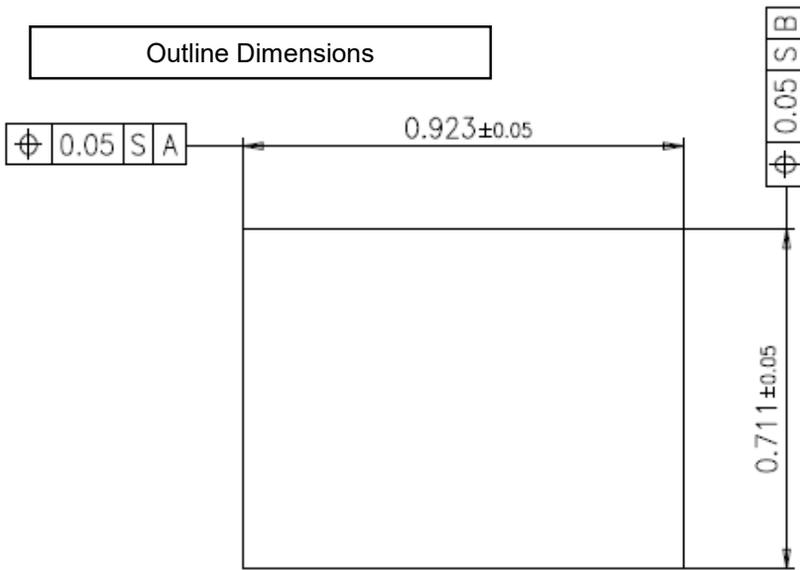
— I/O block circuit diagram and pin function descriptions

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

Pin No.	Waveform and voltage	Internal circuit	Impedance	Description
C1	—		430kΩ	High Gain mode / Low Gain mode SW
C2	—	—	—	GND
C3	—		—	RX output
C4	—		190kΩ	RX mode SW

■ PACKAGE INFORMATION (Reference Data)

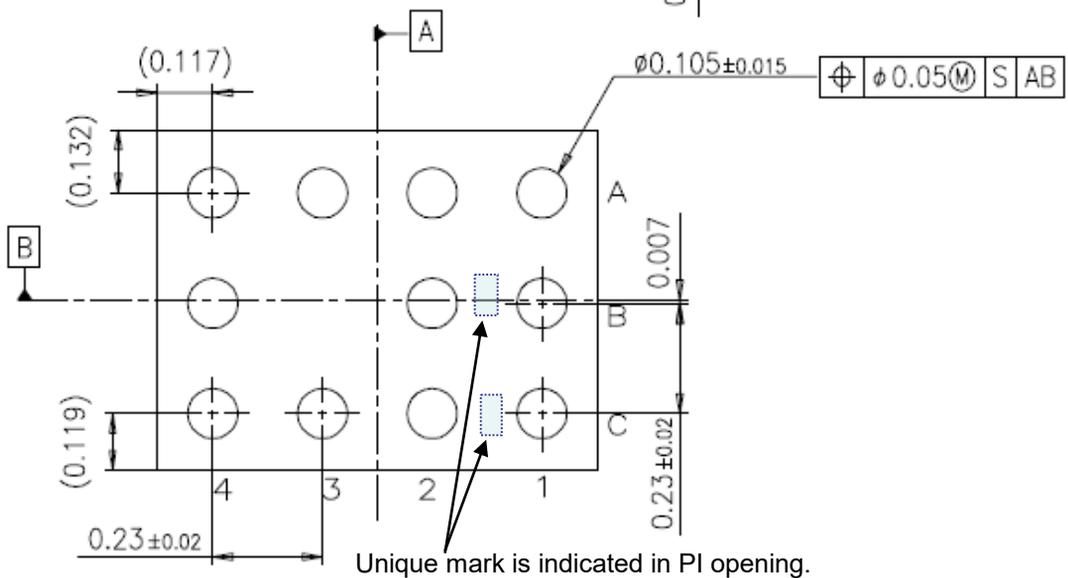
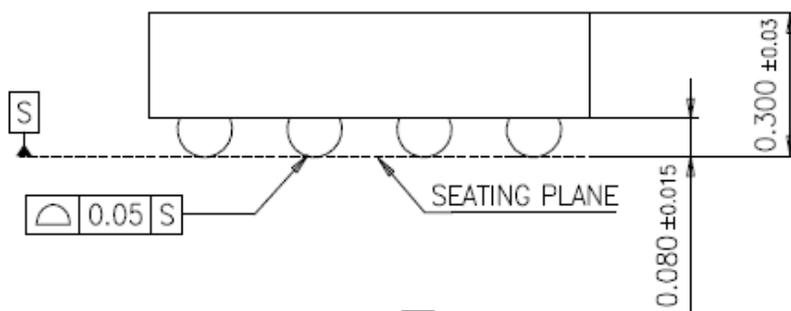
Unit:mm



PIN Location on Bottom View

※ Coordinate origin is defined as 0,0

Pin	X	Y
A1	0.345	0.223
A2	0.115	0.223
A3	-0.115	0.223
A4	-0.345	0.223
B1	0.345	-0.007
B2	0.115	-0.007
B4	-0.345	-0.007
C1	0.345	-0.237
C2	0.115	-0.237
C3	-0.115	-0.237
C4	-0.345	-0.237



Unique mark is indicated in PI opening.

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■ Usage Notes

1. Pay attention to the direction of the IC. When mounting it in the wrong direction onto the PCB (printed-circuit-board), it might be damaged.
2. Pay attention in the PCB (printed-circuit-board) pattern layout in order to prevent damage due to short circuit between pins.
In addition, refer to the Pin Description for the pin configuration.
3. Perform visual inspection on the PCB before applying power, otherwise damage might happen due to problems such as solder-bridge between the pins of the IC. Also, perform full technical verification on the assembly quality, because the same damage possibly can happen due to conductive substances, such as solder ball, that adhere to the IC during transportation.
4. Take notice in the use of this IC that it might be damaged when an abnormal state occurs such as output pin-VCC short (Power supply fault), output pin-GND short (Ground fault), or output-to-output-pin short (load short). Safety measures such as installation of fuses are recommended because the extent of the above-mentioned damage will depend on the current capability of the power supply.
5. Due to the unshielded structure of this IC, functions and characteristics of the IC cannot be guaranteed under the exposure of light. During normal operation or even under testing condition, please ensure that the IC is not exposed to light.
6. Please ensure that your design does not have metal shield parts touching the chip surface as the surface potential is GND voltage.
7. Pay attention to the breakdown voltage of this IC when using.

Pin No.	Pin Name	ESD Model	Standard	ESD Voltage (*1)
A2	VCC	HBM	GND	-900 V
A4	VTX	HBM	VCC	+900 V
A1	GND	HBM	VCC	+900 V
B1	GND	HBM	VCC	+900 V
B2	GND	HBM	VCC	+900 V
C2	GND	HBM	VCC	+900 V
C1	VHG	HBM	VCC	+900 V
C4	VRX	HBM	VCC	+900 V

The other pins are more than +/- 1000V.

(*1) This is the max value which is not broken.

Revision History

Date	Revision	Description
2021.2.2	1.00	1. Initially issued.
2021.9.21	1.01	1. Changed document name from Product Standards to Datasheet. 1. Changed important notice on page2
2022.2.1	1.02	2. Remove important notice page from previous version page 18 3. Added usage notes on page18,19

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