

# MOSFET – Power, Single N-Channel

## 40 V, 1.4 mΩ, 200 A

### NVMFS5C430NL

#### Features

- Small Footprint (5x6 mm) for Compact Design
- Low  $R_{DS(on)}$  to Minimize Conduction Losses
- Low  $Q_G$  and Capacitance to Minimize Driver Losses
- NVMFS5C430NLWF – Wettable Flank Option for Enhanced Optical Inspection
- AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

#### MAXIMUM RATINGS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

| Parameter                                                                 |                                               |                        | Symbol                            | Value        | Unit |
|---------------------------------------------------------------------------|-----------------------------------------------|------------------------|-----------------------------------|--------------|------|
| Drain-to-Source Voltage                                                   |                                               |                        | V <sub>DSS</sub>                  | 40           | V    |
| Gate-to-Source Voltage                                                    |                                               |                        | V <sub>GS</sub>                   | ±20          | V    |
| Continuous Drain Current R <sub>θJC</sub> (Notes 1, 3)                    | Steady State                                  | T <sub>C</sub> = 25°C  | I <sub>D</sub>                    | 200          | A    |
|                                                                           |                                               | T <sub>C</sub> = 100°C |                                   | 140          |      |
| Power Dissipation R <sub>θJC</sub> (Note 1)                               | Steady State                                  | T <sub>C</sub> = 25°C  | P <sub>D</sub>                    | 110          | W    |
|                                                                           |                                               | T <sub>C</sub> = 100°C |                                   | 53           |      |
| Continuous Drain Current R <sub>θJA</sub> (Notes 1, 2, 3)                 | Steady State                                  | T <sub>A</sub> = 25°C  | I <sub>D</sub>                    | 38           | A    |
|                                                                           |                                               | T <sub>A</sub> = 100°C |                                   | 27           |      |
| Power Dissipation R <sub>θJA</sub> (Notes 1 & 2)                          | Steady State                                  | T <sub>A</sub> = 25°C  | P <sub>D</sub>                    | 3.8          | W    |
|                                                                           |                                               | T <sub>A</sub> = 100°C |                                   | 1.9          |      |
| Pulsed Drain Current                                                      | T <sub>A</sub> = 25°C, t <sub>p</sub> = 10 μs |                        | I <sub>DM</sub>                   | 900          | A    |
| Operating Junction and Storage Temperature                                |                                               |                        | T <sub>J</sub> , T <sub>stg</sub> | -55 to + 175 | °C   |
| Source Current (Body Diode)                                               |                                               |                        | I <sub>S</sub>                    | 120          | A    |
| Single Pulse Drain-to-Source Avalanche Energy (I <sub>L(pk)</sub> = 15 A) |                                               |                        | E <sub>AS</sub>                   | 493          | mJ   |
| Single Pulse Drain-to-Source Voltage (t <sub>p</sub> = 10 μs)             |                                               |                        | V <sub>DSM</sub>                  | 48           | V    |
| Lead Temperature for Soldering Purposes (1/8" from case for 10 s)         |                                               |                        | T <sub>L</sub>                    | 260          | °C   |

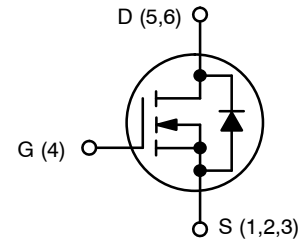
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL RESISTANCE MAXIMUM RATINGS

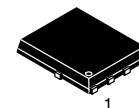
| Parameter                                   | Symbol          | Value | Unit                      |
|---------------------------------------------|-----------------|-------|---------------------------|
| Junction-to-Case – Steady State             | $R_{\theta JC}$ | 1.4   | $^\circ\text{C}/\text{W}$ |
| Junction-to-Ambient – Steady State (Note 2) | $R_{\theta JA}$ | 40    |                           |

1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
2. Surface-mounted on FR4 board using a 650 mm<sup>2</sup>, 2 oz. Cu pad.
3. Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.

| $V_{(BR)DS}$ | $R_{DS(ON)}$ MAX | $I_D$ MAX |
|--------------|------------------|-----------|
| 40 V         | 1.4 mΩ @ 10 V    | 200 A     |
|              | 2.2 mΩ @ 4.5 V   |           |

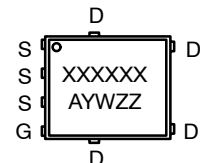


N-CHANNEL MOSFET



DFN5  
(SO-8FL)  
CASE 488AA  
STYLE 1

#### MARKING DIAGRAM



XXXXXX = 5C430L  
(NVMFS5C430NL) or  
430LWF  
(NVMFS5C430NLWF)

A = Assembly Location  
Y = Year  
W = Work Week  
ZZ = Lot Traceability

#### ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 5 of this data sheet.

NOTE: Some of the device on this data sheet have been **DISCONTINUED**. Please refer to the table on page 5

# NVMFS5C430NL

## ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Test Condition | Min | Typ | Max | Unit |
|-----------|--------|----------------|-----|-----|-----|------|
|-----------|--------|----------------|-----|-----|-----|------|

### OFF CHARACTERISTICS

|                                                           |                   |                                                     |                                  |     |     |                      |
|-----------------------------------------------------------|-------------------|-----------------------------------------------------|----------------------------------|-----|-----|----------------------|
| Drain-to-Source Breakdown Voltage                         | $V_{(BR)DSS}$     | $V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$ | 40                               |     |     | V                    |
| Drain-to-Source Breakdown Voltage Temperature Coefficient | $V_{(BR)DSS}/T_J$ |                                                     |                                  | 1.3 |     | mV/ $^\circ\text{C}$ |
| Zero Gate Voltage Drain Current                           | $I_{DSS}$         | $V_{GS} = 0\text{ V}, V_{DS} = 40\text{ V}$         | $T_J = 25\text{ }^\circ\text{C}$ |     | 10  | $\mu\text{A}$        |
|                                                           |                   |                                                     | $T_J = 125^\circ\text{C}$        |     | 250 |                      |
| Gate-to-Source Leakage Current                            | $I_{GSS}$         | $V_{DS} = 0\text{ V}, V_{GS} = 20\text{ V}$         |                                  |     | 100 | nA                   |

### ON CHARACTERISTICS (Note 4)

|                                   |                  |                                                 |     |      |     |                      |
|-----------------------------------|------------------|-------------------------------------------------|-----|------|-----|----------------------|
| Gate Threshold Voltage            | $V_{GS(TH)}$     | $V_{GS} = V_{DS}, I_D = 250\text{ }\mu\text{A}$ | 1.2 |      | 2.0 | V                    |
| Threshold Temperature Coefficient | $V_{GS(TH)}/T_J$ |                                                 |     | -5.6 |     | mV/ $^\circ\text{C}$ |
| Drain-to-Source On Resistance     | $R_{DS(on)}$     | $V_{GS} = 4.5\text{ V}, I_D = 50\text{ A}$      |     | 1.7  | 2.2 | $\text{m}\Omega$     |
|                                   |                  | $V_{GS} = 10\text{ V}, I_D = 50\text{ A}$       |     | 1.2  | 1.4 |                      |
| Forward Transconductance          | $g_{FS}$         | $V_{DS} = 15\text{ V}, I_D = 50\text{ A}$       |     | 180  |     | S                    |

### CHARGES, CAPACITANCES & GATE RESISTANCE

|                              |              |                                                                  |  |      |  |             |
|------------------------------|--------------|------------------------------------------------------------------|--|------|--|-------------|
| Input Capacitance            | $C_{ISS}$    | $V_{GS} = 0\text{ V}, f = 1\text{ MHz}, V_{DS} = 20\text{ V}$    |  | 4300 |  | $\text{pF}$ |
| Output Capacitance           | $C_{OSS}$    |                                                                  |  | 1900 |  |             |
| Reverse Transfer Capacitance | $C_{RSS}$    |                                                                  |  | 72   |  |             |
| Total Gate Charge            | $Q_{G(TOT)}$ | $V_{GS} = 4.5\text{ V}, V_{DS} = 20\text{ V}; I_D = 50\text{ A}$ |  | 32   |  | $\text{nC}$ |
| Total Gate Charge            | $Q_{G(TOT)}$ | $V_{GS} = 10\text{ V}, V_{DS} = 20\text{ V}; I_D = 50\text{ A}$  |  | 70   |  |             |
| Threshold Gate Charge        | $Q_{G(TH)}$  | $V_{GS} = 4.5\text{ V}, V_{DS} = 20\text{ V}; I_D = 50\text{ A}$ |  | 7.0  |  |             |
| Gate-to-Source Charge        | $Q_{GS}$     |                                                                  |  | 12   |  |             |
| Gate-to-Drain Charge         | $Q_{GD}$     |                                                                  |  | 9.0  |  |             |
| Plateau Voltage              | $V_{GP}$     |                                                                  |  | 2.9  |  | V           |

### SWITCHING CHARACTERISTICS (Note 5)

|                     |              |                                                                                         |  |     |  |    |
|---------------------|--------------|-----------------------------------------------------------------------------------------|--|-----|--|----|
| Turn-On Delay Time  | $t_{d(ON)}$  | $V_{GS} = 4.5\text{ V}, V_{DS} = 20\text{ V}, I_D = 50\text{ A}, R_G = 1\text{ }\Omega$ |  | 15  |  | ns |
| Rise Time           | $t_r$        |                                                                                         |  | 140 |  |    |
| Turn-Off Delay Time | $t_{d(OFF)}$ |                                                                                         |  | 31  |  |    |
| Fall Time           | $t_f$        |                                                                                         |  | 9   |  |    |

### DRAIN-SOURCE DIODE CHARACTERISTICS

|                         |          |                                                                              |                           |  |      |     |    |
|-------------------------|----------|------------------------------------------------------------------------------|---------------------------|--|------|-----|----|
| Forward Diode Voltage   | $V_{SD}$ | $V_{GS} = 0\text{ V}, I_S = 50\text{ A}$                                     | $T_J = 25^\circ\text{C}$  |  | 0.81 | 1.2 | V  |
|                         |          |                                                                              | $T_J = 125^\circ\text{C}$ |  | 0.68 |     |    |
| Reverse Recovery Time   | $t_{RR}$ | $V_{GS} = 0\text{ V}, di_S/dt = 100\text{ A}/\mu\text{s}, I_S = 50\text{ A}$ |                           |  | 61   |     | ns |
| Charge Time             | $t_a$    |                                                                              |                           |  | 29   |     |    |
| Discharge Time          | $t_b$    |                                                                              |                           |  | 32   |     |    |
| Reverse Recovery Charge | $Q_{RR}$ |                                                                              |                           |  | 80   |     | nC |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Pulse Test: pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

5. Switching characteristics are independent of operating junction temperatures.

TYPICAL CHARACTERISTICS

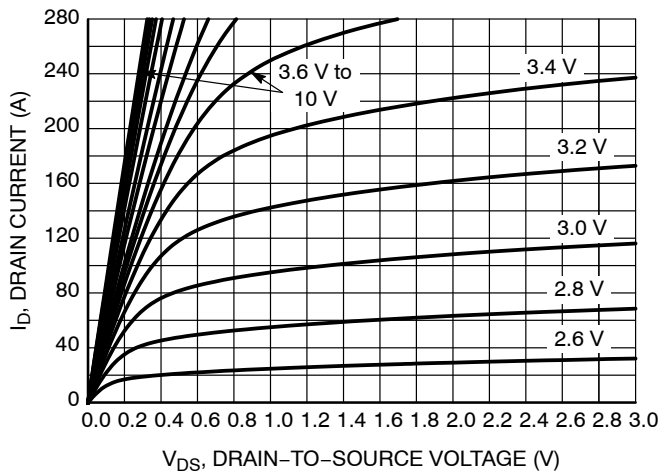


Figure 1. On-Region Characteristics

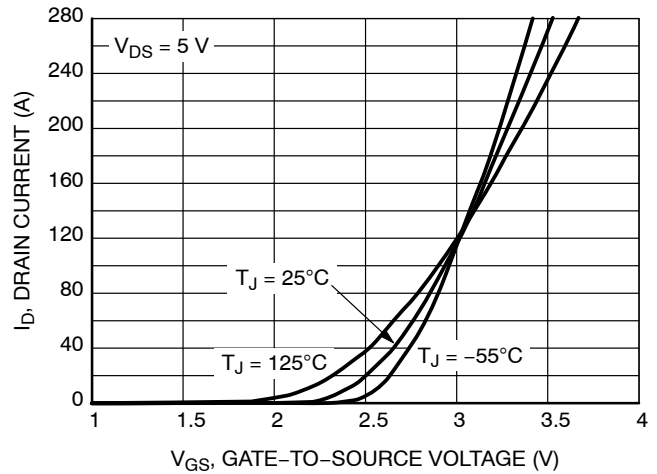


Figure 2. Transfer Characteristics

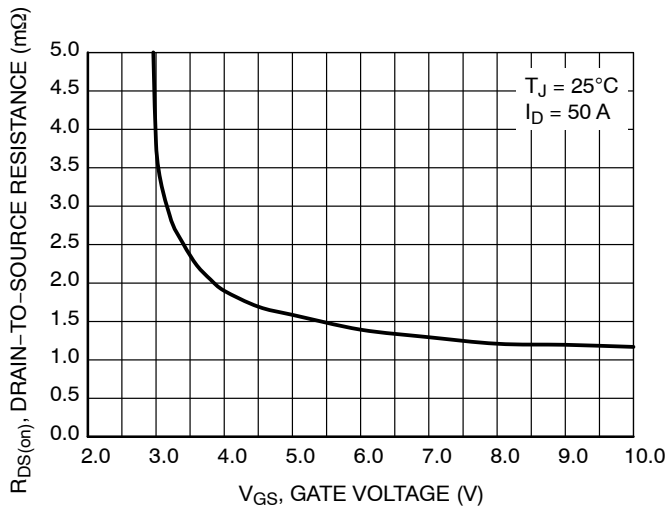


Figure 3. On-Resistance vs. Gate-to-Source Voltage

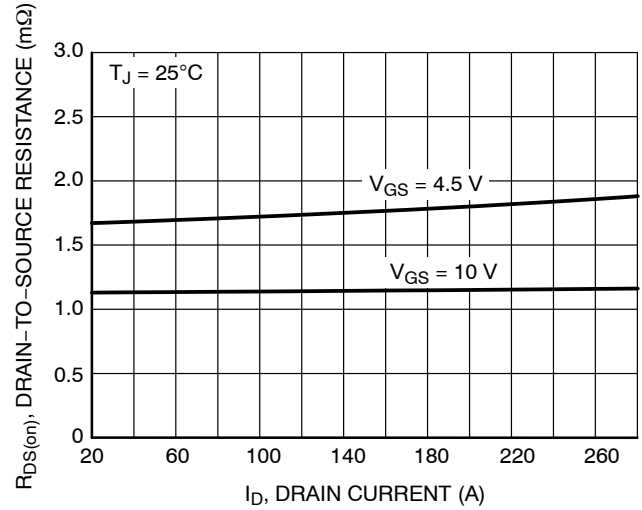


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

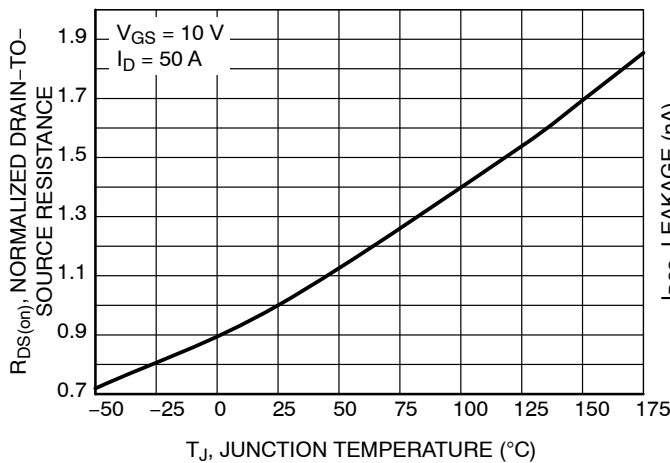


Figure 5. On-Resistance Variation with Temperature

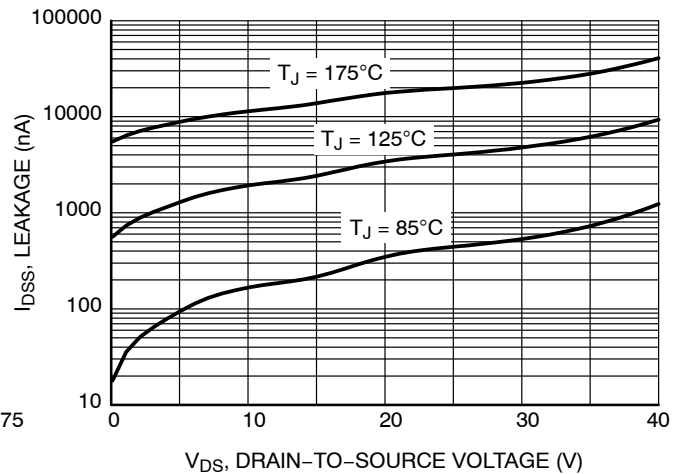


Figure 6. Drain-to-Source Leakage Current vs. Voltage

# NVMFS5C430NL

## TYPICAL CHARACTERISTICS

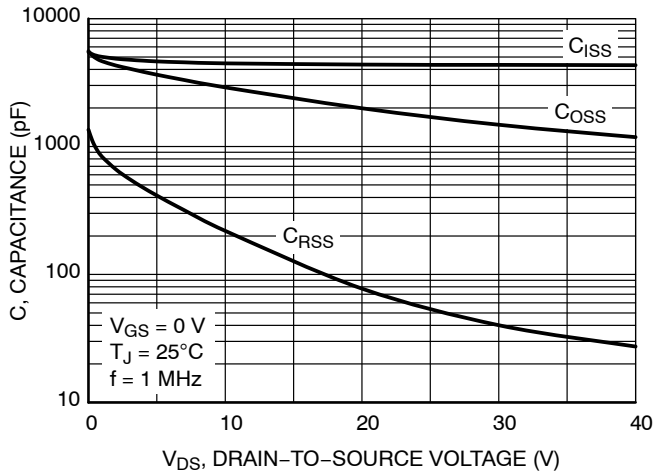


Figure 7. Capacitance Variation

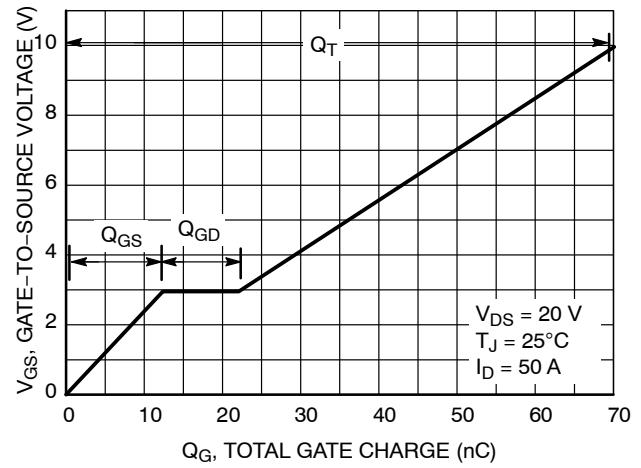


Figure 8. Gate-to-Source Voltage vs. Total Charge

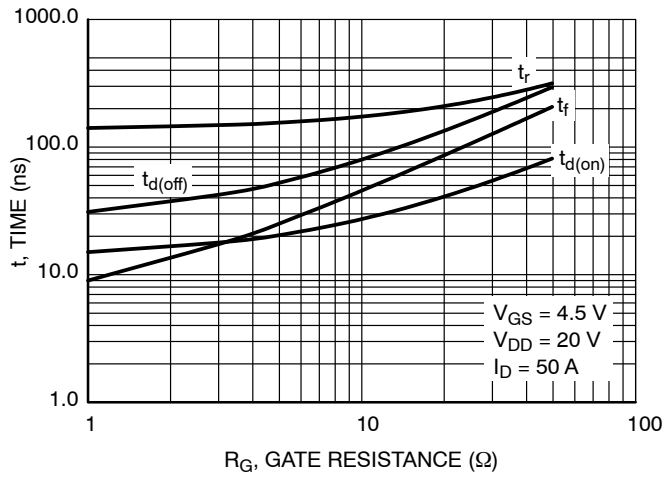


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

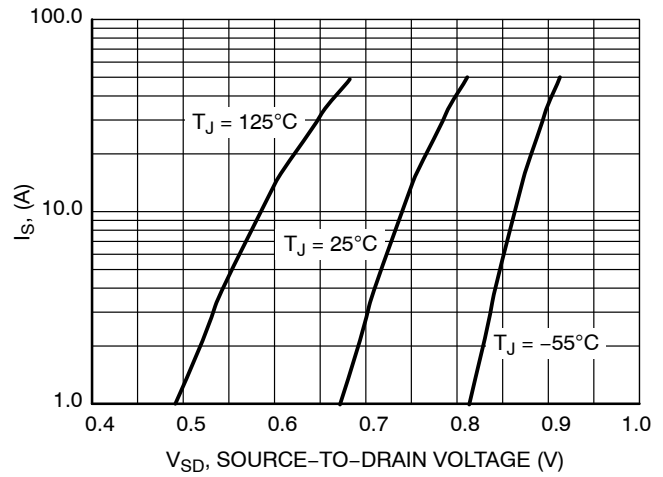


Figure 10. Diode Forward Voltage vs. Current

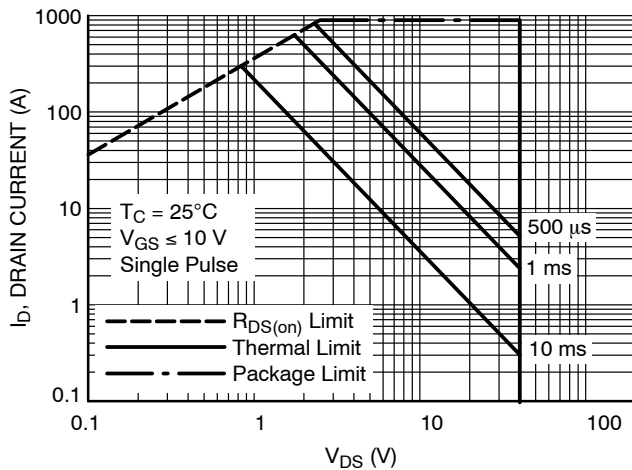


Figure 11. Safe Operating Area

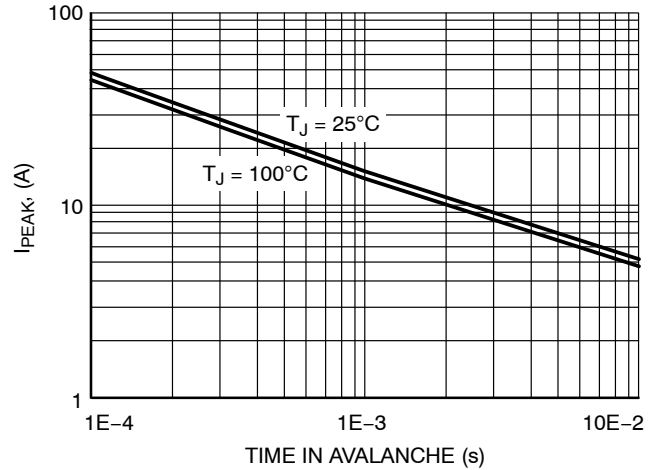


Figure 12.  $I_{PEAK}$  vs. Time in Avalanche

# NVMFS5C430NL

## TYPICAL CHARACTERISTICS

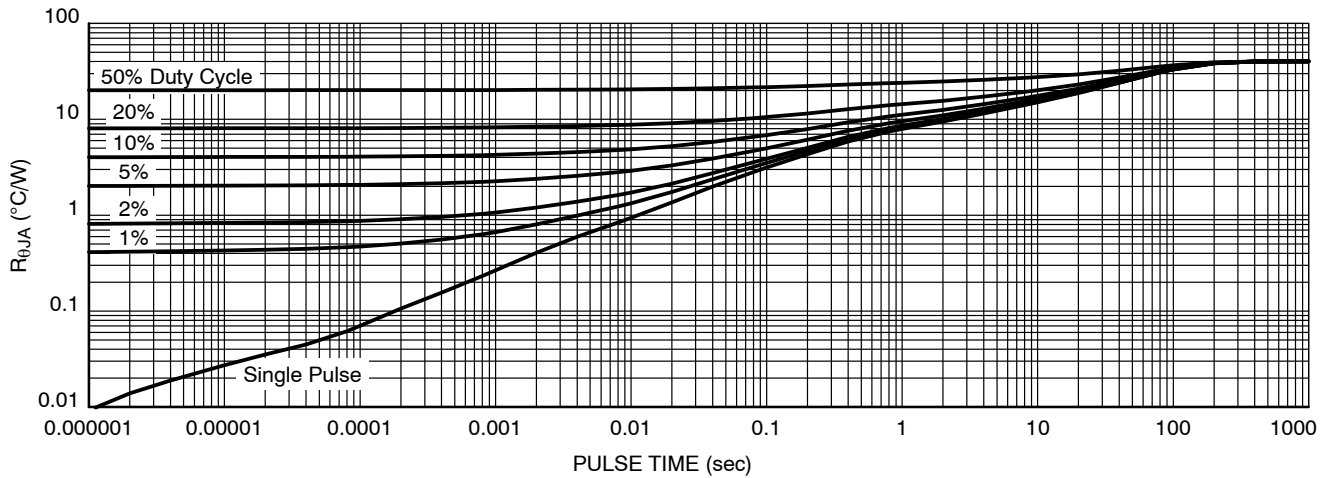


Figure 13. Thermal Characteristics

### DEVICE ORDERING INFORMATION

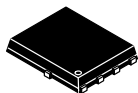
| Device              | Marking | Package                            | Shipping <sup>†</sup> |
|---------------------|---------|------------------------------------|-----------------------|
| NVMFS5C430NLWFT1G   | 430LWF  | DFN5<br>(Pb-Free, Wettable Flanks) | 1500 / Tape & Reel    |
| NVMFS5C430NLAFT1G   | 5C430L  | DFN5<br>(Pb-Free)                  | 1500 / Tape & Reel    |
| NVMFS5C430NLWFAFT1G | 430LWF  | DFN5<br>(Pb-Free, Wettable Flanks) | 1500 / Tape & Reel    |
| NVMFS5C430NLWFET1G  | 430LWF  | DFN5<br>(Pb-Free, Wettable Flanks) | 1500 / Tape & Reel    |

### DISCONTINUED (Note 5)

| Device            | Package Type | Package                            | Shipping <sup>†</sup> |
|-------------------|--------------|------------------------------------|-----------------------|
| NVMFS5C430NLT1G   | 5C430L       | DFN5<br>(Pb-Free)                  | 1500 / Tape & Reel    |
| NVMFS5C430NLT3G   | 5C430L       | DFN5<br>(Pb-Free)                  | 5000 / Tape & Reel    |
| NVMFS5C430NLWFT3G | 430LWF       | DFN5<br>(Pb-Free, Wettable Flanks) | 5000 / Tape & Reel    |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](#).

6. **DISCONTINUED:** This device is not recommended for new design. Please contact your **onsemi** representative for information. The most current information on this device may be available on [www.onsemi.com](http://www.onsemi.com).



SCALE 2:1

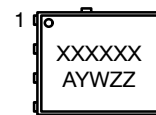
**DFN5 5x6, 1.27P**  
**(SO-8FL)**  
**CASE 488AA**  
**ISSUE N**

DATE 25 JUN 2018

## NOTES:

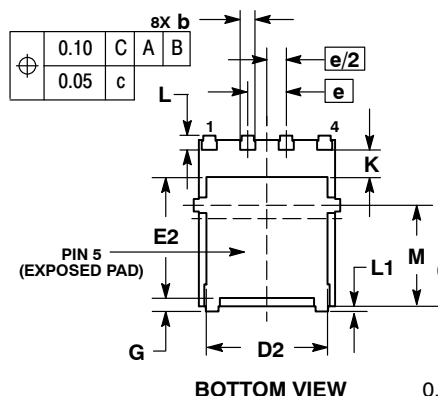
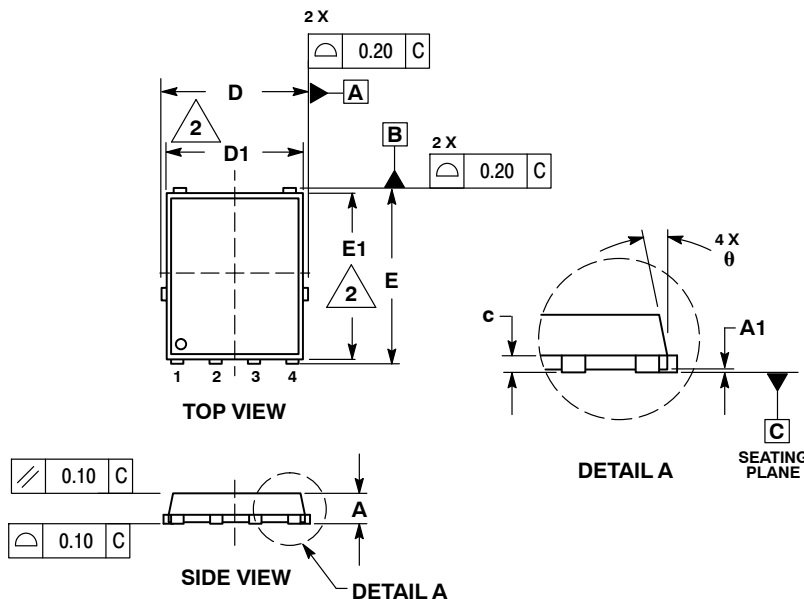
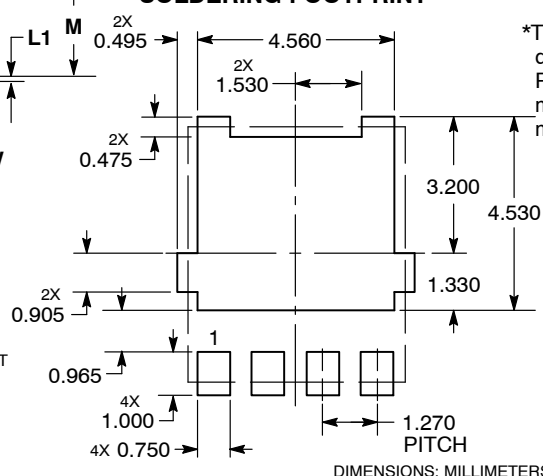
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION D1 AND E1 DO NOT INCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS.

| DIM | MILLIMETERS |       |      |
|-----|-------------|-------|------|
|     | MIN         | NOM   | MAX  |
| A   | 0.90        | 1.00  | 1.10 |
| A1  | 0.00        | ---   | 0.05 |
| b   | 0.33        | 0.41  | 0.51 |
| c   | 0.23        | 0.28  | 0.33 |
| D   | 5.00        | 5.15  | 5.30 |
| D1  | 4.70        | 4.90  | 5.10 |
| D2  | 3.80        | 4.00  | 4.20 |
| E   | 6.00        | 6.15  | 6.30 |
| E1  | 5.70        | 5.90  | 6.10 |
| E2  | 3.45        | 3.65  | 3.85 |
| e   | 1.27 BSC    |       |      |
| G   | 0.51        | 0.575 | 0.71 |
| K   | 1.20        | 1.35  | 1.50 |
| L   | 0.51        | 0.575 | 0.71 |
| L1  | 0.125 REF   |       |      |
| M   | 3.00        | 3.40  | 3.80 |
| θ   | 0°          | ---   | 12°  |

**GENERIC**  
**MARKING DIAGRAM\***


XXXXXX = Specific Device Code  
A = Assembly Location  
Y = Year  
W = Work Week  
ZZ = Lot Traceability

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.


**RECOMMENDED**  
**SOLDERING FOOTPRINT\***


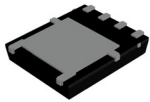
STYLE 1:  
PIN 1. SOURCE  
2. SOURCE  
3. SOURCE  
4. GATE  
5. DRAIN

STYLE 2:  
PIN 1. ANODE  
2. ANODE  
3. ANODE  
4. NO CONNECT  
5. CATHODE

\*For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

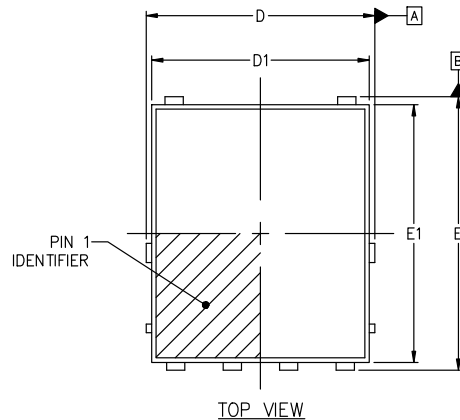
|                         |                                 |                                                                                                                                                                                  |
|-------------------------|---------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>DOCUMENT NUMBER:</b> | <b>98AON14036D</b>              | Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| <b>DESCRIPTION:</b>     | <b>DFN5 5x6, 1.27P (SO-8FL)</b> | <b>PAGE 1 OF 1</b>                                                                                                                                                               |

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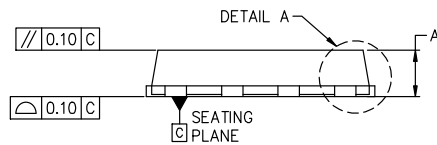


DFNW5 4.90x5.90x1.00, 1.27P  
CASE 507BE  
ISSUE B

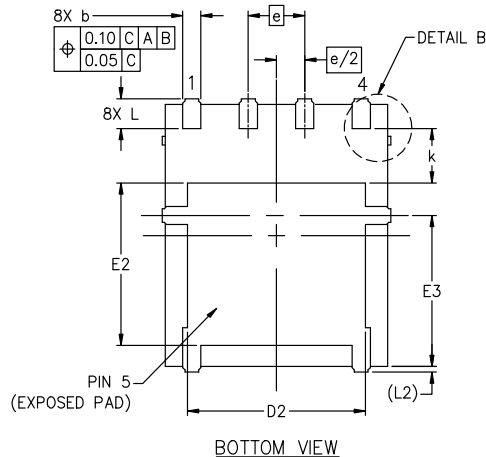
DATE 19 SEP 2024



TOP VIEW

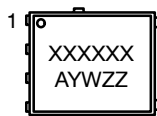


SIDE VIEW



BOTTOM VIEW

GENERIC  
MARKING DIAGRAM\*

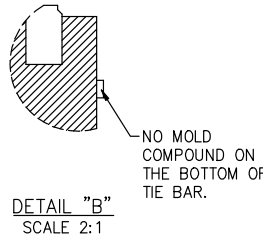
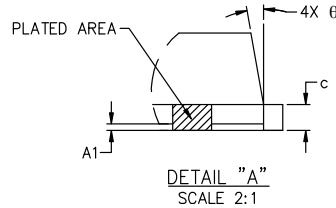


XXXXXX = Specific Device Code  
A = Assembly Location  
Y = Year  
W = Work Week  
ZZ = Lot Traceability

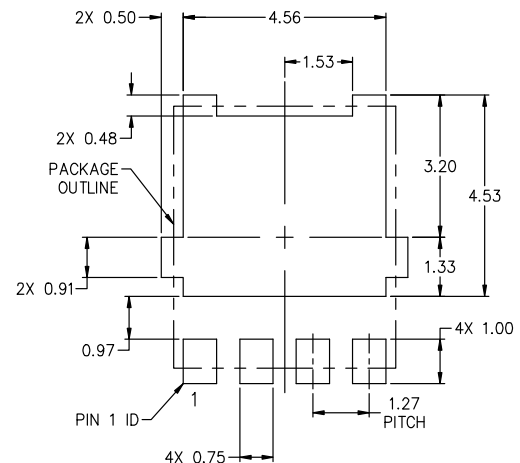
\*This information is generic. Please refer to device data sheet for actual part marking.  
Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5M-2018.
2. ALL DIMENSIONS ARE IN MILLIMETERS.
3. DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.
4. THIS PACKAGE CONTAINS WETTABLE FLANK DESIGN FEATURES TO AID IN FILLET FORMATION ON THE LEADS DURING MOUNTING.



| DIM | MILLIMETERS |      |      |
|-----|-------------|------|------|
|     | MIN         | NOM  | MAX  |
| A   | 0.90        | 1.00 | 1.10 |
| A1  | 0.00        | ---  | 0.05 |
| b   | 0.33        | 0.41 | 0.51 |
| c   | 0.23        | 0.28 | 0.33 |
| D   | 5.00        | 5.15 | 5.30 |
| D1  | 4.70        | 4.90 | 5.10 |
| D2  | 3.80        | 4.00 | 4.20 |
| E   | 6.00        | 6.15 | 6.30 |
| E1  | 5.70        | 5.90 | 6.10 |
| E2  | 3.45        | 3.65 | 3.85 |
| E3  | 3.00        | 3.40 | 3.80 |
| e   | 1.27 BSC    |      |      |
| k   | 1.20        | 1.35 | 1.50 |
| L   | 0.51        | 0.57 | 0.71 |
| L2  | 0.15 REF.   |      |      |
| θ   | 0°          | 6°   | 12°  |



RECOMMENDED MOUNTING FOOTPRINT\*  
\*FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ONSEMI SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERM/D.

|                  |                             |                                                                                                                                                                                     |
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